Keysight Test Report

PASS

Report Header:	Keysight Inc.
Report Date:	2025/06/04 00:39:27
Type:	Unframed

SUMMARY

IxOS Version	10.00.1000.17
Test Start Time	2025/06/04 00:37:45
Test End Time	2025/06/04 00:39:27
Test Duration	00:00:30
Pass/Fail Verdict	PASS
Module Type	1,1,3 - 100GBASE-DR
Module Version	1,1,3 - CMIS 5.0
Serial Number	1,1,3 - UNA8490004

Transceiver DOM (Digital Optical Monitoring) - BERT

1,1,3					
Manufacturer	Eoptolink	Model	EOLO-138HG-5H-SM	Mfg Revision	01
Type	100GBASE-DR	SN	UNA8490004	Firmware Revision	3.0
MSA	CMIS 5.0	Date Code(YYMMDDLL)	221109	Hardware Revision	1.0
Media Tech	1310 nm DFB	Media Connector	MPO 1x16		
Cable Lenth	0.0 m	Identifier Type	OSFP		
Reported Power Class	8	Reported Max Power	18.000000 W		

Modlue	High Alarm	High Warn	Low Warn	Low Alarm	
Temperature	78 C	73 C	-3 C	-8 C	
Supply Voltage	3.630 V	3.465 V	3.135 V	2.970 V	

Lane Limits	High Alarm	High Warn	Low Warn	Low Alarm	
Tx Optical Power	6.00 dBm	5.00 dBm	-3.90 dBm	-4.90 dBm	
Rx Optical Power	6.50 dBm	5.50 dBm	-7.40 dBm	-8.40 dBm	
Tx Bias Current	6.50 dBm	5.50 dBm	-7.40 dBm	-8.40 dBm	

Host Lane	Port	Data Path State	Tx LOS	Tx CDR LOL	Media	Tx Optical	Tx Bias	Rx Optical	Rx LOS	Rx CDR LOL
1	3.1	Activated (4)	No	No	1	1.81 dBm	233.816 mA	2.04 dBm	No	No
2	3.1	Activated (4)	No	No	2	2.21 dBm	234.296 mA	1.47 dBm	No	No
3	3.1	Activated (4)	No	No	3	2.16 dBm	214.208 mA	-0.18 dBm	No	No
4	3.1	Activated (4)	No	No	4	1.95 dBm	214.088 mA	1.57 dBm	No	No
5	3.1	Activated (4)	No	No	5	2.24 dBm	211.040 mA	2.18 dBm	No	No
6	3.1	Activated (4)	No	No	6	2.04 dBm	210.920 mA	1.99 dBm	No	No
7	3.1	Activated (4)	No	No	7	2.35 dBm	214.208 mA	2.12 dBm	No	No
8	3.1	Activated (4)	No	No	8	2.48 dBm	214.208 mA	1.91 dBm	No	No

CMIS Applicatio Select

1,1,3

Current AppSel   1

Available Applications

App	Host Side						Line Side Media				
	Interface	Lane Speed (G bit/s)	Modulation	Lane Groups	Lanes	ID (HEex)	Interface	Lane Speed (G bit/s)	Lane Groups	Lanes	ID (Hex)
1	100GAUI-1-S C2M	106	PAM4	8	1	75	100GBASE-DR	106	8	1	20

Preview of Auto selected applications

Host Port Mode	Mod	Lane Groups	Lanes	AppSel	Link	Host Electrical	Lane Groups	Lanes	Note
800G-R8	PAM4	1	8	1	M	100GAUI-1-S C2M	1	8	First app matching host lane speed for host id: 0x51 (800G S C2M 8x53.125 PAM4)
400G-R4	PAM4	2	4	1	M	100GAUI-1-S C2M	2	4	First app matching host lane speed for host id: 0x4f (400GAUI-4-S C2M 4x53.125 PAM4)
200G-R2	PAM4	4	2	1	M	100GAUI-1-S C2M	4	2	First app matching host lane speed for host id: 0x4d (200GAUI-2-S C2M 2x53.125 PAM4)
100G-R	PAM4	8	1	1	Y	100GAUI-1-S C2M	8	1	First app matching compatible electrical mode: 0x4b (100GAUI-1-S C2M 1x53.125 PAM4)
400G-R8	PAM4	1	8	1	N	100GAUI-1-S C2M	1	8	No matching electrical mode
200G-R4	PAM4	2	4	1	N	100GAUI-1-S C2M	2	4	No matching electrical mode
100G-R2	PAM4	4	2	1	N	100GAUI-1-S C2M	4	2	No matching electrical mode
50G-R	PAM4	8	1	1	N	100GAUI-1-S C2M	8	1	No matching electrical mode
200G-R8	NRZ	1	8	1	N	100GAUI-1-S C2M	1	8	No matching electrical mode
100G-R4	NRZ	2	4	1	N	100GAUI-1-S C2M	2	4	No matching electrical mode
50G-R2	NRZ	4	2	1	N	100GAUI-1-S C2M	4	2	No matching electrical mode
25G-R	NRZ	8	1	1	N	100GAUI-1-S C2M	8	1	No matching electrical mode

BERT Result Summary

Bit Error Ratio	1,1,3
BER Threshold	2.400000e-04
Pre-BER Pass/Fail Verdict	PASS

BERT Statistics

Port - 1,1,3

Physical Lane	Pattern Lock	Pattern Transmitted	Pattern Received	Bits Send	Bits Received	Bit Error Received	Bit Error Ratio
Totals	lock	PRBS-31Q	PRBS-31Q	13893519437600.0	13893519439680.0	262.0	3.020700e-10
0	lock	PRBS-31Q	PRBS-31Q	1732144326720	1732144327680	26	2.997000e-11
1	lock	PRBS-31Q	PRBS-31Q	1732144326880	1732144327680	42	4.847000e-11
2	lock	PRBS-31Q	PRBS-31Q	1732144326800	1732144327680	36	4.156000e-11
3	lock	PRBS-31Q	PRBS-31Q	1732144326880	1732144327680	9	1.040000e-11
4	lock	PRBS-31Q	PRBS-31Q	1732144326720	1732144327680	28	3.240000e-11
5	lock	PRBS-31Q	PRBS-31Q	1732144326720	1732144325760	80	9.240000e-11
6	lock	PRBS-31Q	PRBS-31Q	1744265934560	1744265933760	14	1.614000e-11
7	lock	PRBS-31Q	PRBS-31Q	1756387542320	1756387541760	27	3.073000e-11

BERT Statistics

FEC Result Summary

PCS Lane Statistics

L2 Traffic Test Summary

Port Statistics - BERT

Port Statistics	Port 1,1,3
link	link up
transmitState	0
lineSpeed	400000
bertBitsSent	13857154614320
bertBitsReceived	13857154615680
bertBitErrorsReceived	262
bertBitErrorRatio	1.890000e-11
centralTemperature	42
portTemperature	52
pcpuFpgaTemperature	33
bertTransmitDuration	00:03:54.539181660
encoding	Unknown
transceiverTemp	55
transceiverVoltage	3.2338
minLatency	0
averageLatency	0
maxLatency	0