



## Keysight Test Report

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**PASS**

Report Header:

Keysight Inc.

Report Date:

2025/07/15 14:33:40

Type:

Framed

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**SUMMARY**

IxOS Version	10.80.8001.4
Test Start Time	2025/07/15 14:32:13
Test End Time	2025/07/15 14:33:40
Test Duration	00:00:10
Pass/Fail Verdict	PASS
Module Type	1,1,1 - PassiveCopper 1,1,2 - PassiveCopper
Module Version	1,1,1 - CMIS 5.0 1,1,2 - CMIS 5.0
Serial Number	1,1,1 - 2320333189 1,1,2 - 2320333189

## Transceiver DOM (Digital Optical Monitoring) - 128

1.1.1										
Manufacturer		Molex		Model		2126753010		Mfg Revision		01
Type		PassiveCopper		SN		2320333189		Firmware Revision		
MSA		CMIS 5.0		Date Code(YYMMDDLL)		230722		Hardware Revision		
Media Tech		Copper cable unequalized		Media Connector		No separable connector				
Cable Lenth		1.0 m		Identifier Type		QSFP-DD				
Reported Power Class		1		Reported Max Power		1.500000 W				
Modlue		High Alarm		High Warn		Low Warn		Low Alarm		
Temperature		-0.001 C		-0.001 C		-0.001 C		-0.001 C		
Supply Voltage		-0.000 V		-0.000 V		-0.000 V		-0.000 V		
Lane Limits		High Alarm		High Warn		Low Warn		Low Alarm		
Tx Optical Power		-40.00 dBm		-40.00 dBm		-40.00 dBm		-40.00 dBm		
Rx Optical Power		-40.00 dBm		-40.00 dBm		-40.00 dBm		-40.00 dBm		
Tx Bias Current		-40.00 dBm		-40.00 dBm		-40.00 dBm		-40.00 dBm		
Host Lane	Port	Data Path State	Tx LOS	Tx CDR LOL	Media	Tx Optical	Tx Bias	Rx Optical	Rx LOS	Rx CDR LOL
1	1.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No
2	1.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No
3	1.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No
4	1.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No
5	1.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No
6	1.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No
7	1.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No
8	1.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No

1,1,2										
Manufacturer		Molex		Model		2126753010		Mfg Revision		01
Type		PassiveCopper		SN		2320333189		Firmware Revision		
MSA		CMIS 5.0		Date Code(YYMMDDLL)		230722		Hardware Revision		
Media Tech		Copper cable unequalized		Media Connector		No separable connector				
Cable Lenth		1.0 m		Identifier Type		QSFP-DD				
Reported Power Class		1		Reported Max Power		1.500000 W				
Module		High Alarm		High Warn		Low Warn		Low Alarm		
Temperature		-0.001 C		-0.001 C		-0.001 C		-0.001 C		
Supply Voltage		-0.000 V		-0.000 V		-0.000 V		-0.000 V		
Lane Limits		High Alarm		High Warn		Low Warn		Low Alarm		
Tx Optical Power		-40.00 dBm		-40.00 dBm		-40.00 dBm		-40.00 dBm		
Rx Optical Power		-40.00 dBm		-40.00 dBm		-40.00 dBm		-40.00 dBm		
Tx Bias Current		-40.00 dBm		-40.00 dBm		-40.00 dBm		-40.00 dBm		
Host Lane	Port	Data Path State	Tx LOS	Tx CDR LOL	Media	Tx Optical	Tx Bias	Rx Optical	Rx LOS	Rx CDR LOL
1	2.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No
2	2.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No
3	2.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No
4	2.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No
5	2.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No
6	2.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No
7	2.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No
8	2.1	0	No	No	0	-40.00 dBm	0.000 mA	-40.00 dBm	No	No

CMIS Applicatio Select

1,1,1

Current AppSel 0

Available Applications

App	Host Side						Line Side Media				
	Interface	Lane Speed (G bit/s)	Modulation	Lane Groups	Lanes	ID (HEex)	Interface	Lane Speed (G bit/s)	Lane Groups	Lanes	ID (Hex)
1	800GBASE-CR8	106	PAM4	1	8	73	PassiveCopper		0	8	1
2	400GBASE-CR4	106	PAM4	2	4	72	PassiveCopper		0	4	1
3	200GBASE-CR2	106	PAM4	4	2	71	PassiveCopper		0	2	1
4	100GBASE-CR1	106	PAM4	8	1	70	PassiveCopper		0	1	1
5	400GBASE-CR8	53	PAM4	1	8	29	PassiveCopper		0	8	1
6	200GBASE-CR4	53	PAM4	2	4	28	PassiveCopper		0	4	1
7	100GBASE-CR2	53	PAM4	4	2	27	PassiveCopper		0	2	1
8	25GBASE-CR CA-25G-L	26	NRZ	8	1	20	PassiveCopper		0	1	1

Preview of Auto selected applications

Host Port Mode	Mod	Lane Groups	Lanes	AppSel	Link	Host Electrical	Lane Groups	Lanes	Note
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1,1,2

Current AppSel0

Available Applications

App	Host Side						Line Side Media				
	Interface	Lane Speed (G bit/s)	Modulation	Lane Groups	Lanes	ID (HEex)	Interface	Lane Speed (G bit/s)	Lane Groups	Lanes	ID (Hex)
1	800GBASE-CR8	106	PAM4	1	8	73	PassiveCopper		0	8	1
2	400GBASE-CR4	106	PAM4	2	4	72	PassiveCopper		0	4	1
3	200GBASE-CR2	106	PAM4	4	2	71	PassiveCopper		0	2	1
4	100GBASE-CR1	106	PAM4	8	1	70	PassiveCopper		0	1	1
5	400GBASE-CR8	53	PAM4	1	8	29	PassiveCopper		0	8	1
6	200GBASE-CR4	53	PAM4	2	4	28	PassiveCopper		0	4	1
7	100GBASE-CR2	53	PAM4	4	2	27	PassiveCopper		0	2	1
8	25GBASE-CR CA-25G-L	26	NRZ	8	1	20	PassiveCopper		0	1	1

Preview of Auto selected applications

Host Port Mode	Mod	Lane Groups	Lanes	AppSel	Link	Host Electrical	Lane Groups	Lanes	Note
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**BERT Result Summary**

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**BERT Statistics**



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**FEC Result Summary - 128**

Frame Loss Ratio	1,1,1	1,1,2
Pre-FEC Standard	2.400000e-04	2.400000e-04
<b>Pre-FEC Pass/Fail Verdict</b>	PASS	PASS
Post-FEC Standard	9.200000e-13	9.200000e-13
<b>Post-FEC Pass/Fail Verdict</b>	PASS	PASS

## PCS Lane Statistics - 128

Port - 1,1,1

Physical Lane	PCS Lane Marker Lock	PCS Lane Marker Map	Relative Lane Skew (ns)	PCS Lane Marker Error Count	FEC Symbol Error Count	FEC Correct Bit Count	FEC Symbol Error	FEC Correct Bit Rate
Totals	Lock	all	9.035	0.0	0.0	0.0	0.000000e+00	0.000000e+00
0	Lock	0	6.023	0	0	0	0.000000e+00	0.000000e+00
1	Lock	16	6.023	0	0	0	0.000000e+00	0.000000e+00
2	Lock	1	6.023	0	0	0	0.000000e+00	0.000000e+00
3	Lock	17	6.023	0	0	0	0.000000e+00	0.000000e+00
4	Lock	2	3.011	0	0	0	0.000000e+00	0.000000e+00
5	Lock	18	3.011	0	0	0	0.000000e+00	0.000000e+00
6	Lock	3	6.023	0	0	0	0.000000e+00	0.000000e+00
7	Lock	19	6.023	0	0	0	0.000000e+00	0.000000e+00
8	Lock	20	6.023	0	0	0	0.000000e+00	0.000000e+00
9	Lock	4	6.023	0	0	0	0.000000e+00	0.000000e+00
10	Lock	21	6.023	0	0	0	0.000000e+00	0.000000e+00
11	Lock	5	6.023	0	0	0	0.000000e+00	0.000000e+00
12	Lock	22	9.035	0	0	0	0.000000e+00	0.000000e+00
13	Lock	6	9.035	0	0	0	0.000000e+00	0.000000e+00
14	Lock	23	6.023	0	0	0	0.000000e+00	0.000000e+00
15	Lock	7	6.023	0	0	0	0.000000e+00	0.000000e+00
16	Lock	24	3.011	0	0	0	0.000000e+00	0.000000e+00
17	Lock	8	3.011	0	0	0	0.000000e+00	0.000000e+00
18	Lock	25	0.0	0	0	0	0.000000e+00	0.000000e+00
19	Lock	9	3.011	0	0	0	0.000000e+00	0.000000e+00
20	Lock	26	3.011	0	0	0	0.000000e+00	0.000000e+00
21	Lock	10	3.011	0	0	0	0.000000e+00	0.000000e+00
22	Lock	27	3.011	0	0	0	0.000000e+00	0.000000e+00
23	Lock	11	3.011	0	0	0	0.000000e+00	0.000000e+00
24	Lock	12	6.023	0	0	0	0.000000e+00	0.000000e+00
25	Lock	28	6.023	0	0	0	0.000000e+00	0.000000e+00
26	Lock	13	3.011	0	0	0	0.000000e+00	0.000000e+00
27	Lock	29	3.011	0	0	0	0.000000e+00	0.000000e+00
28	Lock	30	6.023	0	0	0	0.000000e+00	0.000000e+00
29	Lock	14	6.023	0	0	0	0.000000e+00	0.000000e+00
30	Lock	31	3.011	0	0	0	0.000000e+00	0.000000e+00
31	Lock	15	3.011	0	0	0	0.000000e+00	0.000000e+00

## Port - 1,1,2

Physical Lane	PCS Lane Marker Lock	PCS Lane Marker Map	Relative Lane Skew (ns)	PCS Lane Marker Error Count	FEC Symbol Error Count	FEC Correct Bit Count	FEC Symbol Error	FEC Correct Bit Rate
Totals	Lock	all	9.035	0.0	0.0	0.0	0.000000e+00	0.000000e+00
0	Lock	16	9.035	0	0	0	0.000000e+00	0.000000e+00
1	Lock	0	9.035	0	0	0	0.000000e+00	0.000000e+00
2	Lock	17	9.035	0	0	0	0.000000e+00	0.000000e+00
3	Lock	1	9.035	0	0	0	0.000000e+00	0.000000e+00
4	Lock	18	6.023	0	0	0	0.000000e+00	0.000000e+00
5	Lock	2	6.023	0	0	0	0.000000e+00	0.000000e+00
6	Lock	19	6.023	0	0	0	0.000000e+00	0.000000e+00
7	Lock	3	6.023	0	0	0	0.000000e+00	0.000000e+00
8	Lock	20	9.035	0	0	0	0.000000e+00	0.000000e+00
9	Lock	4	9.035	0	0	0	0.000000e+00	0.000000e+00
10	Lock	21	9.035	0	0	0	0.000000e+00	0.000000e+00
11	Lock	5	9.035	0	0	0	0.000000e+00	0.000000e+00
12	Lock	6	3.011	0	0	0	0.000000e+00	0.000000e+00
13	Lock	22	3.011	0	0	0	0.000000e+00	0.000000e+00
14	Lock	7	6.023	0	0	0	0.000000e+00	0.000000e+00
15	Lock	23	6.023	0	0	0	0.000000e+00	0.000000e+00
16	Lock	24	6.023	0	0	0	0.000000e+00	0.000000e+00
17	Lock	8	6.023	0	0	0	0.000000e+00	0.000000e+00
18	Lock	25	3.011	0	0	0	0.000000e+00	0.000000e+00
19	Lock	9	3.011	0	0	0	0.000000e+00	0.000000e+00
20	Lock	10	3.011	0	0	0	0.000000e+00	0.000000e+00
21	Lock	26	3.011	0	0	0	0.000000e+00	0.000000e+00
22	Lock	11	6.023	0	0	0	0.000000e+00	0.000000e+00
23	Lock	27	6.023	0	0	0	0.000000e+00	0.000000e+00
24	Lock	12	6.023	0	0	0	0.000000e+00	0.000000e+00
25	Lock	28	6.023	0	0	0	0.000000e+00	0.000000e+00
26	Lock	13	3.011	0	0	0	0.000000e+00	0.000000e+00
27	Lock	29	3.011	0	0	0	0.000000e+00	0.000000e+00
28	Lock	14	0.0	0	0	0	0.000000e+00	0.000000e+00
29	Lock	30	0.0	0	0	0	0.000000e+00	0.000000e+00
30	Lock	15	3.011	0	0	0	0.000000e+00	0.000000e+00
31	Lock	31	3.011	0	0	0	0.000000e+00	0.000000e+00

L2 Traffic Test Summary

Frame Size	Tx Count	Rx Count	Loss Count	Loss %
128	13636549869	13636549869	0	0.0

## Port Statistics - 128

Port Statistics	Port 1,1,1	Port 1,1,2
link	link up	link up
lineSpeed	800000	800000
transmitDuration	00:00:10.081395145	00:00:10.100698821
framesSent	6811753421	6824796448
framesReceived	6811753421	6824796448
fragments	0	0
undersize	0	0
oversizeAndCrcErrors	0	0
vlanTaggedFramesRx	0	0
flowControlFrames	0	0
bitsSent	6975235503104	6988591562752
bitsReceived	6988591562752	6975235503104
pcsSyncErrorsReceived	0	0
pcsRemoteFaultsReceived	0	0
pcsLocalFaultsReceived	0	0
fecTotalBitErrors	0	0
fecMaxSymbolErrors	0	0
fecCorrectedCodewords	0	0
fecTotalCodewords	2035988608	2034730576
fecFrameLossRatio	0.000000e+00	0.000000e+00
preFecBer	0.000000e+00	0.000000e+00
fecMaxSymbolErrorsBin0	2035988608	2034730576
fecMaxSymbolErrorsBin1	0	0
fecMaxSymbolErrorsBin2	0	0
fecMaxSymbolErrorsBin3	0	0
fecMaxSymbolErrorsBin4	0	0
fecMaxSymbolErrorsBin5	0	0
fecMaxSymbolErrorsBin6	0	0
fecMaxSymbolErrorsBin7	0	0
fecMaxSymbolErrorsBin8	0	0
fecMaxSymbolErrorsBin9	0	0
fecMaxSymbolErrorsBin10	0	0
fecMaxSymbolErrorsBin11	0	0
fecMaxSymbolErrorsBin12	0	0
fecMaxSymbolErrorsBin13	0	0
fecMaxSymbolErrorsBin14	0	0
fecMaxSymbolErrorsBin15	0	0
fecUncorrectableCodewords	0	0
fecTranscodingUncorrectableErrors	0	0
l1BitsSent	8065116050464	8080558994432
l1BitsReceived	8080558994432	8065116050464
transceiverTemp	0	0
encoding	PAM4 106G	PAM4 106G
fecStatus	KP4-FEC	KP4-FEC
transceiverVoltage	0	0
minLatency	0	0
averageLatency	2	1
maxLatency	6	4
Loss_Frames	0	0
Loss%	0.0	0.0