



Keysight Test Report

PASS

Report Header:	Keysight Inc.
Report Date:	2025/06/03 22:28:46
Type:	Unframed

SUMMARY

IxOS Version	10.00.1000.17
Test Start Time	2025/06/03 22:26:31
Test End Time	2025/06/03 22:28:45
Test Duration	00:01:00
Pass/Fail Verdict	PASS
Module Type	1,1,3 - 100GBASE-DR
Module Version	1,1,3 - CMIS 5.0
Serial Number	1,1,3 - UNA8490004

Transceiver DOM (Digital Optical Monitoring) - BERT

1,1,3										
Manufacturer	Eoptolink			Model	EOL0-138HG-5H-SM			Mfg Revision	01	
Type	100GBASE-DR			SN	UNA8490004			Firmware Revision	3.0	
MSA	CMIS 5.0			Date Code(YMMDDLL)	221109			Hardware Revision	1.0	
Media Tech	1310 nm DFB			Media Connector	MPO 1x16					
Cable Length	0.0 m			Identifier Type	OSFP					
Reported Power Class	8			Reported Max Power	18.000000 W					
Module	High Alarm			High Warn	Low Warn			Low Alarm		
Temperature	78 C			73 C	-3 C			-8 C		
Supply Voltage	3.630 V			3.465 V	3.135 V			2.970 V		
Lane Limits	High Alarm			High Warn	Low Warn			Low Alarm		
Tx Optical Power	6.00 dBm			5.00 dBm	-3.90 dBm			-4.90 dBm		
Rx Optical Power	6.50 dBm			5.50 dBm	-7.40 dBm			-8.40 dBm		
Tx Bias Current	6.50 dBm			5.50 dBm	-7.40 dBm			-8.40 dBm		
Host Lane	Port	Date Path State	Tx LOS	Tx CDR LOL	Media	Tx Optical	Tx Bias	Rx Optical	Rx LOS	Rx CDR LOL
1	3.1	Activated (4)	No	No	1	1.80 dBm	234.288 mA	2.10 dBm	No	No
2	3.1	Activated (4)	No	No	2	2.19 dBm	234.168 mA	1.57 dBm	No	No
3	3.1	Activated (4)	No	No	3	2.03 dBm	214.040 mA	0.03 dBm	No	No
4	3.1	Activated (4)	No	No	4	1.83 dBm	214.408 mA	1.80 dBm	No	No
5	3.1	Activated (4)	No	No	5	2.21 dBm	211.240 mA	2.20 dBm	No	No
6	3.1	Activated (4)	No	No	6	1.98 dBm	211.360 mA	2.03 dBm	No	No
7	3.1	Activated (4)	No	No	7	2.27 dBm	214.528 mA	2.26 dBm	No	No
8	3.1	Activated (4)	No	No	8	2.39 dBm	214.288 mA	2.07 dBm	No	No

CMIS Applicatio Select

1,1,3

Current AppSel

1

Available Applications

App	Host Side						Line Side Media				
	Interface	Lane Speed (G bit/s)	Modulation	Lane Groups	Lanes	ID (HEex)	Interface	Lane Speed (G bit/s)	Lane Groups	Lanes	ID (Hex)
1	100GAUI-1-S C2M	106	PAM4	8	1	75	100GBASE-DR	106	8	1	20

Preview of Auto selected applications

Host Port Mode	Mod	Lane Groups	Lanes	AppSel	Link	Host Electrical	Lane Groups	Lanes	Note
800G-R8	PAM4	1	8	1	M	100GAUI-1-S C2M	1	8	First app matching host lane speed for host id: 0x51 (800G S C2M 8x53.125 PAM4)
400G-R4	PAM4	2	4	1	M	100GAUI-1-S C2M	2	4	First app matching host lane speed for host id: 0x4f (400GAUI-4-S C2M 4x53.125 PAM4)
200G-R2	PAM4	4	2	1	M	100GAUI-1-S C2M	4	2	First app matching host lane speed for host id: 0x4d (200GAUI-2-S C2M 2x53.125 PAM4)
100G-R	PAM4	8	1	1	Y	100GAUI-1-S C2M	8	1	First app matching compatible electrical mode: 0x4b (100GAUI-1-S C2M 1x53.125 PAM4)
400G-R8	PAM4	1	8	1	N	100GAUI-1-S C2M	1	8	No matching electrical mode
200G-R4	PAM4	2	4	1	N	100GAUI-1-S C2M	2	4	No matching electrical mode
100G-R2	PAM4	4	2	1	N	100GAUI-1-S C2M	4	2	No matching electrical mode
50G-R	PAM4	8	1	1	N	100GAUI-1-S C2M	8	1	No matching electrical mode
200G-R8	NRZ	1	8	1	N	100GAUI-1-S C2M	1	8	No matching electrical mode
100G-R4	NRZ	2	4	1	N	100GAUI-1-S C2M	2	4	No matching electrical mode
50G-R2	NRZ	4	2	1	N	100GAUI-1-S C2M	4	2	No matching electrical mode
25G-R	NRZ	8	1	1	N	100GAUI-1-S C2M	8	1	No matching electrical mode

BERT Result Summary

Bit Error Ratio	1,1,3
BER Threshold	2.400000e-04
Pre-BER Pass/Fail Verdict	PASS

BERT Statistics

Port - 1,1,3

Physical Lane	Pattern Lock	Pattern Transmitted	Pattern Received	Bits Send	Bits Received	Bit Error Received	Bit Error Ratio
Totals	lock	11Q	11Q	55605611138400.0	55605611141760.0	29.0	1.664700e-11
0	lock	11Q	11Q	6857870256720	6857870256000	0	0.000000e+00
1	lock	11Q	11Q	6857870256640	6857870257920	1	5.830000e-13
2	lock	11Q	11Q	6966108598000	6966108599040	3	1.720000e-12
3	lock	11Q	11Q	6966108597920	6966108599040	0	0.000000e+00
4	lock	11Q	11Q	6966108598080	6966108599040	1	5.740000e-13
5	lock	11Q	11Q	6966108598080	6966108599040	23	1.320000e-11
6	lock	11Q	11Q	7006059613760	7006059613440	1	5.700000e-13
7	lock	11Q	11Q	7019376619200	7019376618240	0	0.000000e+00

FEC Result Summary

PCS Lane Statistics

L2 Traffic Test Summary

Port Statistics - BERT

Port Statistics	Port 1,1,3
link	link up
transmitState	0
lineSpeed	800000
bertBitsSent	54862962054160
bertBitsReceived	54862962059520
bertBitErrorsReceived	28
bertBitErrorRatio	5.100000e-13
centralTemperature	45
portTemperature	54
pcpuFpgaTemperature	34
bertTransmitDuration	00:12:27.864098860
encoding	PAM4 106G
transceiverTemp	55
transceiverVoltage	3.2275
minLatency	0
averageLatency	0
maxLatency	0