



# Keysight Test Report

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**PASS**

Report Header:	Keysight Inc.
Report Date:	2025/06/03 22:21:30
Type:	Unframed

SUMMARY

IxOS Version	10.00.1000.17
Test Start Time	2025/06/03 22:19:15
Test End Time	2025/06/03 22:21:30
Test Duration	00:01:00
Pass/Fail Verdict	PASS
Module Type	1,1,3 - 100GBASE-DR
Module Version	1,1,3 - CMIS 5.0
Serial Number	1,1,3 - UNA8490004

## Transceiver DOM (Digital Optical Monitoring) - BERT

1,1,3										
Manufacturer		Eoptolink		Model		EOLO-138HG-5H-SM		Mfg Revision		01
Type		100GBASE-DR		SN		UNA8490004		Firmware Revision		3.0
MSA		CMIS 5.0		Date Code(YMMDDLL)		221109		Hardware Revision		1.0
Media Tech		1310 nm DFB		Media Connector		MPO 1x16				
Cable Length		0.0 m		Identifier Type		OSFP				
Reported Power Class		8		Reported Max Power		18.000000 W				
Module		High Alarm		High Warn		Low Warn		Low Alarm		
Temperature		78 C		73 C		-3 C		-8 C		
Supply Voltage		3.630 V		3.465 V		3.135 V		2.970 V		
Lane Limits		High Alarm		High Warn		Low Warn		Low Alarm		
Tx Optical Power		6.00 dBm		5.00 dBm		-3.90 dBm		-4.90 dBm		
Rx Optical Power		6.50 dBm		5.50 dBm		-7.40 dBm		-8.40 dBm		
Tx Bias Current		6.50 dBm		5.50 dBm		-7.40 dBm		-8.40 dBm		
Host Lane	Port	Date Path State	Tx LOS	Tx CDR LOL	Media	Tx Optical	Tx Bias	Rx Optical	Rx LOS	Rx CDR LOL
1	3.1	Activated (4)	No	No	1	1.80 dBm	233.312 mA	2.11 dBm	No	No
2	3.1	Activated (4)	No	No	2	2.18 dBm	233.320 mA	1.56 dBm	No	No
3	3.1	Activated (4)	No	No	3	2.03 dBm	213.312 mA	0.04 dBm	No	No
4	3.1	Activated (4)	No	No	4	1.83 dBm	214.160 mA	1.79 dBm	No	No
5	3.1	Activated (4)	No	No	5	2.20 dBm	210.536 mA	2.20 dBm	No	No
6	3.1	Activated (4)	No	No	6	1.98 dBm	210.872 mA	2.05 dBm	No	No
7	3.1	Activated (4)	No	No	7	2.28 dBm	213.672 mA	2.28 dBm	No	No
8	3.1	Activated (4)	No	No	8	2.39 dBm	213.920 mA	2.07 dBm	No	No

## CMIS Applicatio Select

1,1,3

Current AppSel

1

Available Applications

App	Host Side						Line Side Media				
	Interface	Lane Speed (G bit/s)	Modulation	Lane Groups	Lanes	ID (HEex)	Interface	Lane Speed (G bit/s)	Lane Groups	Lanes	ID (Hex)
1	100GAUI-1-S C2M	106	PAM4	8	1	75	100GBASE-DR	106	8	1	20

Preview of Auto selected applications

Host Port Mode	Mod	Lane Groups	Lanes	AppSel	Link	Host Electrical	Lane Groups	Lanes	Note
800G-R8	PAM4	1	8	1	M	100GAUI-1-S C2M	1	8	First app matching host lane speed for host id: 0x51 (800G S C2M 8x53.125 PAM4)
400G-R4	PAM4	2	4	1	M	100GAUI-1-S C2M	2	4	First app matching host lane speed for host id: 0x4f (400GAUI-4-S C2M 4x53.125 PAM4)
200G-R2	PAM4	4	2	1	M	100GAUI-1-S C2M	4	2	First app matching host lane speed for host id: 0x4d (200GAUI-2-S C2M 2x53.125 PAM4)
100G-R	PAM4	8	1	1	Y	100GAUI-1-S C2M	8	1	First app matching compatible electrical mode: 0x4b (100GAUI-1-S C2M 1x53.125 PAM4)
400G-R8	PAM4	1	8	1	N	100GAUI-1-S C2M	1	8	No matching electrical mode
200G-R4	PAM4	2	4	1	N	100GAUI-1-S C2M	2	4	No matching electrical mode
100G-R2	PAM4	4	2	1	N	100GAUI-1-S C2M	4	2	No matching electrical mode
50G-R	PAM4	8	1	1	N	100GAUI-1-S C2M	8	1	No matching electrical mode
200G-R8	NRZ	1	8	1	N	100GAUI-1-S C2M	1	8	No matching electrical mode
100G-R4	NRZ	2	4	1	N	100GAUI-1-S C2M	2	4	No matching electrical mode
50G-R2	NRZ	4	2	1	N	100GAUI-1-S C2M	4	2	No matching electrical mode
25G-R	NRZ	8	1	1	N	100GAUI-1-S C2M	8	1	No matching electrical mode

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**BERT Result Summary**

Bit Error Ratio	1,1,3
BER Threshold	2.400000e-04
<b>Pre-BER Pass/Fail Verdict</b>	<b>PASS</b>

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**BERT Statistics**

Port - 1,1,3

Physical Lane	Pattern Lock	Pattern Transmitted	Pattern Received	Bits Send	Bits Received	Bit Error Received	Bit Error Ratio
Totals	lock	11Q	11Q	27879138175520.0	21290335958320.0	433687.0	6.337975e-07
0	lock	11Q	11Q	3453395338240	2607939040320	0	0.000000e+00
1	lock	11Q	11Q	3453395338080	2621477202240	1	1.530000e-12
2	lock	11Q	11Q	3453395338000	2621948258720	0	0.000000e+00
3	lock	11Q	11Q	3493180938800	2682512235840	2	2.970000e-12
4	lock	11Q	11Q	3506442805600	2673782400960	0	0.000000e+00
5	lock	11Q	11Q	3506442805520	2671645580080	9456	1.411500e-08
6	lock	11Q	11Q	3506442805520	2739344380160	405721	5.920000e-07
7	lock	11Q	11Q	3506442805760	2671686860000	18507	2.767800e-08

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## **FEC Result Summary**

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**PCS Lane Statistics**



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**L2 Traffic Test Summary**

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**Port Statistics - BERT**

Port Statistics	Port 1,1,3
link	link up
transmitState	0
lineSpeed	800000
bertBitsSent	55805586724240
bertBitsReceived	36889168112720
bertBitErrorsReceived	1577725
bertBitErrorRatio	4.280000e-08
centralTemperature	46
portTemperature	54
pcpuFpgaTemperature	34
bertTransmitDuration	00:05:12.553696620
encoding	PAM4 106G
transceiverTemp	54
transceiverVoltage	3.2322000000000002
minLatency	0
averageLatency	0
maxLatency	0