

Keysight Test Report

**PASS**

|  |  |
| --- | --- |
| Report Header: | Keysight Inc. |
| Report Date: | 2025/04/13 23:57:17 |
| Type: | Framed |

# SUMMARY

|  |  |
| --- | --- |
| IxOS Version | 10.00.1000.17 |
| Test Start Time | 2025/04/13 23:45:16 |
| Test End Time | 2025/04/13 23:57:17 |
| Test Duration | 00:01:00 |
| Pass/Fail Verdict | PASS |
| Module Type | 1,1,3 - PassiveCopper 1,1,4 - PassiveCopper |
| Module Version | 1,1,3 - CMIS 5.0 1,1,4 - CMIS 5.0 |
| Serial Number | 1,1,3 - 57ADUAD233140Z 1,1,4 - 57ADUAD233140Z |

# Transceiver DOM (Digital Optical Monitoring) - 64

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **1,1,3** |  |  |  |  |  |  |  |  |  |  |
| **Manufactuer** | TE Connectivity | **Model** | 2369405-2 | **Mfg Revision** | 11 |  |  |  |  |  |
| **Type** | PassiveCopper | **SN** | 57ADUAD233140Z | **Firmware Revision** |  |  |  |  |  |  |
| **MSA** | CMIS 5.0 | **Date Code(YYMMDDLL)** | 230904 | **Hardware Revision** |  |  |  |  |  |  |
| **Media Tech** | Copper cable unequalized | **Media Connector** | No separable connector |  |  |  |  |  |  |  |
| **Cable Lenth** | 1.0 m | **Identifier Type** | OSFP |  |  |  |  |  |  |  |
| **Reported Power Class** | 1 | **Reported Max Power** | 0.250000 W |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Modlue** | **High Alarm** | **High Warn** | **Low Warn** | **Low Alarm** |  |  |  |  |  |  |
| **Temperature** | -0.001 C | -0.001 C | -0.001 C | -0.001 C |  |  |  |  |  |  |
| **Supply Voltage** | -0.000 V | -0.000 V | -0.000 V | -0.000 V |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Lane Limits** | **High Alarm** | **High Warn** | **Low Warn** | **Low Alarm** |  |  |  |  |  |  |
| **Tx Optical Power** | -40.00 dBm | -40.00 dBm | -40.00 dBm | -40.00 dBm |  |  |  |  |  |  |
| **Rx Optical Power** | -40.00 dBm | -40.00 dBm | -40.00 dBm | -40.00 dBm |  |  |  |  |  |  |
| **Tx Bias Current** | -40.00 dBm | -40.00 dBm | -40.00 dBm | -40.00 dBm |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Host Lane** | **Port** | **Data Path State** | **Tx LOS** | **Tx CDR LOL** | **Media Lane** | **Tx Optical Power** | **Tx Bias Cureent** | **Rx Optical Power** | **Rx LOS** | **Rx CDR LOL** |
| 1 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 2 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 3 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 4 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 5 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 6 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 7 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 8 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **1,1,4** |  |  |  |  |  |  |  |  |  |  |
| **Manufactuer** | TE Connectivity | **Model** | 2369405-2 | **Mfg Revision** | 11 |  |  |  |  |  |
| **Type** | PassiveCopper | **SN** | 57ADUAD233140Z | **Firmware Revision** |  |  |  |  |  |  |
| **MSA** | CMIS 5.0 | **Date Code(YYMMDDLL)** | 230904 | **Hardware Revision** |  |  |  |  |  |  |
| **Media Tech** | Copper cable unequalized | **Media Connector** | No separable connector |  |  |  |  |  |  |  |
| **Cable Lenth** | 1.0 m | **Identifier Type** | OSFP |  |  |  |  |  |  |  |
| **Reported Power Class** | 1 | **Reported Max Power** | 0.250000 W |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Modlue** | **High Alarm** | **High Warn** | **Low Warn** | **Low Alarm** |  |  |  |  |  |  |
| **Temperature** | -0.001 C | -0.001 C | -0.001 C | -0.001 C |  |  |  |  |  |  |
| **Supply Voltage** | -0.000 V | -0.000 V | -0.000 V | -0.000 V |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Lane Limits** | **High Alarm** | **High Warn** | **Low Warn** | **Low Alarm** |  |  |  |  |  |  |
| **Tx Optical Power** | -40.00 dBm | -40.00 dBm | -40.00 dBm | -40.00 dBm |  |  |  |  |  |  |
| **Rx Optical Power** | -40.00 dBm | -40.00 dBm | -40.00 dBm | -40.00 dBm |  |  |  |  |  |  |
| **Tx Bias Current** | -40.00 dBm | -40.00 dBm | -40.00 dBm | -40.00 dBm |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Host Lane** | **Port** | **Data Path State** | **Tx LOS** | **Tx CDR LOL** | **Media Lane** | **Tx Optical Power** | **Tx Bias Cureent** | **Rx Optical Power** | **Rx LOS** | **Rx CDR LOL** |
| 1 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 2 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 3 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 4 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 5 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 6 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 7 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 8 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |

# Transceiver DOM (Digital Optical Monitoring) - 128

No DOM data available.

# Transceiver DOM (Digital Optical Monitoring) - 256

No DOM data available.

# Transceiver DOM (Digital Optical Monitoring) - 512

No DOM data available.

# Transceiver DOM (Digital Optical Monitoring) - 1024

No DOM data available.

# Transceiver DOM (Digital Optical Monitoring) - 1280

No DOM data available.

# Transceiver DOM (Digital Optical Monitoring) - 1518

No DOM data available.

# CMIS Applicatio Select

|  |  |
| --- | --- |
| 1,1,3 |  |
| Current AppSel | 0 |

Available Applications

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **App** | **Host Side** | | | | | | **Line Side Media** | | | | |
|  | **Interface** | **Lane Speed (G bit/s)** | **Modulation** | **Lane Groups** | **Lanes** | **ID (HEex)** | **Interface** | **Lane Speed (G bit/s)** | **Lane Groups** | **Lanes** | **ID (Hex)** |
| 1 | 800GBASE-CR8 | 106 | PAM4 | 1 | 8 | 73 | PassiveCopper |  | 0 | 8 | 1 |
| 2 | 400GBASE-CR4 | 106 | PAM4 | 2 | 4 | 72 | PassiveCopper |  | 0 | 4 | 1 |
| 3 | 200GBASE-CR2 | 106 | PAM4 | 4 | 2 | 71 | PassiveCopper |  | 0 | 2 | 1 |
| 4 | 100GBASE-CR1 | 106 | PAM4 | 8 | 1 | 70 | PassiveCopper |  | 0 | 1 | 1 |
| 5 | 400GBASE-CR8 | 53 | PAM4 | 1 | 8 | 29 | PassiveCopper |  | 0 | 8 | 1 |
| 6 | 200GBASE-CR4 | 53 | PAM4 | 2 | 4 | 28 | PassiveCopper |  | 0 | 4 | 1 |
| 7 | 100GBASE-CR2 | 53 | PAM4 | 4 | 2 | 27 | PassiveCopper |  | 0 | 2 | 1 |
| 8 | 50GBASE-CR | 53 | PAM4 | 8 | 1 | 24 | PassiveCopper |  | 0 | 1 | 1 |

Preview of Auto selected applications

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Host Port Mode** | **Mod** | **Lane Groups** | **Lanes** | **AppSel** | **Link** | **Host Electrical** | **Lane Groups** | **Lanes** | **Note** |

|  |  |
| --- | --- |
| 1,1,4 |  |
| Current AppSel | 0 |

Available Applications

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **App** | **Host Side** | | | | | | **Line Side Media** | | | | |
|  | **Interface** | **Lane Speed (G bit/s)** | **Modulation** | **Lane Groups** | **Lanes** | **ID (HEex)** | **Interface** | **Lane Speed (G bit/s)** | **Lane Groups** | **Lanes** | **ID (Hex)** |
| 1 | 800GBASE-CR8 | 106 | PAM4 | 1 | 8 | 73 | PassiveCopper |  | 0 | 8 | 1 |
| 2 | 400GBASE-CR4 | 106 | PAM4 | 2 | 4 | 72 | PassiveCopper |  | 0 | 4 | 1 |
| 3 | 200GBASE-CR2 | 106 | PAM4 | 4 | 2 | 71 | PassiveCopper |  | 0 | 2 | 1 |
| 4 | 100GBASE-CR1 | 106 | PAM4 | 8 | 1 | 70 | PassiveCopper |  | 0 | 1 | 1 |
| 5 | 400GBASE-CR8 | 53 | PAM4 | 1 | 8 | 29 | PassiveCopper |  | 0 | 8 | 1 |
| 6 | 200GBASE-CR4 | 53 | PAM4 | 2 | 4 | 28 | PassiveCopper |  | 0 | 4 | 1 |
| 7 | 100GBASE-CR2 | 53 | PAM4 | 4 | 2 | 27 | PassiveCopper |  | 0 | 2 | 1 |
| 8 | 50GBASE-CR | 53 | PAM4 | 8 | 1 | 24 | PassiveCopper |  | 0 | 1 | 1 |

Preview of Auto selected applications

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Host Port Mode** | **Mod** | **Lane Groups** | **Lanes** | **AppSel** | **Link** | **Host Electrical** | **Lane Groups** | **Lanes** | **Note** |

# BERT Result Summary

No BERT Result Summary data available.

# BERT Statistics

No BERT Statistics data available.

# FEC Result Summary - 64

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,3** | **1,1,4** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# FEC Result Summary - 128

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,3** | **1,1,4** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# FEC Result Summary - 256

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,3** | **1,1,4** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# FEC Result Summary - 512

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,3** | **1,1,4** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# FEC Result Summary - 1024

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,3** | **1,1,4** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# FEC Result Summary - 1280

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,3** | **1,1,4** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# FEC Result Summary - 1518

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,3** | **1,1,4** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# PCS Lane Statistics - 64

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 6078.0 | 0.000000e+00 | 3.572172e-09 |
| **0** | Lock | 16 | 6.023 | 0 | 0 | 52 | 0.000000e+00 | 3.079058e-11 |
| **1** | Lock | 0 | 3.011 | 0 | 0 | 30 | 0.000000e+00 | 1.776380e-11 |
| **2** | Lock | 17 | 3.011 | 0 | 0 | 4 | 0.000000e+00 | 2.368506e-12 |
| **3** | Lock | 1 | 3.011 | 0 | 0 | 5 | 0.000000e+00 | 2.960633e-12 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 25 | 0.000000e+00 | 1.480316e-11 |
| **5** | Lock | 18 | 3.011 | 0 | 0 | 42 | 0.000000e+00 | 2.486932e-11 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.921266e-13 |
| **8** | Lock | 20 | 6.023 | 0 | 0 | 104 | 0.000000e+00 | 6.158116e-11 |
| **9** | Lock | 4 | 3.011 | 0 | 0 | 122 | 0.000000e+00 | 7.167772e-11 |
| **10** | Lock | 21 | 6.023 | 0 | 0 | 11 | 0.000000e+00 | 6.462745e-12 |
| **11** | Lock | 5 | 3.011 | 0 | 0 | 6 | 0.000000e+00 | 3.525134e-12 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 5 | 0.000000e+00 | 2.937612e-12 |
| **13** | Lock | 6 | 0.0 | 0 | 0 | 13 | 0.000000e+00 | 7.637790e-12 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 7 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **16** | Lock | 8 | 3.011 | 0 | 0 | 1182 | 0.000000e+00 | 6.944514e-10 |
| **17** | Lock | 24 | 6.023 | 0 | 0 | 3390 | 0.000000e+00 | 1.991701e-09 |
| **18** | Lock | 9 | 0.0 | 0 | 0 | 230 | 0.000000e+00 | 1.351301e-10 |
| **19** | Lock | 25 | 3.011 | 0 | 0 | 403 | 0.000000e+00 | 2.367715e-10 |
| **20** | Lock | 26 | 6.023 | 0 | 0 | 95 | 0.000000e+00 | 5.581462e-11 |
| **21** | Lock | 10 | 0.0 | 0 | 0 | 72 | 0.000000e+00 | 4.230161e-11 |
| **22** | Lock | 27 | 3.011 | 0 | 0 | 12 | 0.000000e+00 | 7.050268e-12 |
| **23** | Lock | 11 | 3.011 | 0 | 0 | 5 | 0.000000e+00 | 2.937612e-12 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 118 | 0.000000e+00 | 6.932763e-11 |
| **25** | Lock | 28 | 0.0 | 0 | 0 | 65 | 0.000000e+00 | 3.818895e-11 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 24 | 0.000000e+00 | 1.410054e-11 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 24 | 0.000000e+00 | 1.410054e-11 |
| **28** | Lock | 14 | 0.0 | 0 | 0 | 29 | 0.000000e+00 | 1.703815e-11 |
| **29** | Lock | 30 | 0.0 | 0 | 0 | 8 | 0.000000e+00 | 4.700178e-12 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.875223e-13 |

Port - 1,1,4

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 30902.0 | 0.000000e+00 | 1.800895e-08 |
| **0** | Lock | 16 | 3.011 | 0 | 0 | 65 | 0.000000e+00 | 3.818895e-11 |
| **1** | Lock | 0 | 0.0 | 0 | 0 | 51 | 0.000000e+00 | 2.973118e-11 |
| **2** | Lock | 17 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.829643e-13 |
| **3** | Lock | 1 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.829643e-13 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 108 | 0.000000e+00 | 6.296015e-11 |
| **5** | Lock | 18 | 6.023 | 0 | 0 | 74 | 0.000000e+00 | 4.313936e-11 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 1.165929e-12 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **8** | Lock | 4 | 3.011 | 0 | 0 | 343 | 0.000000e+00 | 1.999568e-10 |
| **9** | Lock | 20 | 6.023 | 0 | 0 | 155 | 0.000000e+00 | 9.035947e-11 |
| **10** | Lock | 5 | 0.0 | 0 | 0 | 54 | 0.000000e+00 | 3.148007e-11 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 36 | 0.000000e+00 | 2.098672e-11 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 6 | 0.000000e+00 | 3.497786e-12 |
| **13** | Lock | 22 | 6.023 | 0 | 0 | 27 | 0.000000e+00 | 1.574004e-11 |
| **14** | Lock | 7 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 23 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 9763 | 0.000000e+00 | 5.691481e-09 |
| **17** | Lock | 8 | 0.0 | 0 | 0 | 10149 | 0.000000e+00 | 5.916505e-09 |
| **18** | Lock | 25 | 0.0 | 0 | 0 | 3419 | 0.000000e+00 | 1.993155e-09 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 2600 | 0.000000e+00 | 1.515707e-09 |
| **20** | Lock | 10 | 0.0 | 0 | 0 | 1220 | 0.000000e+00 | 7.112165e-10 |
| **21** | Lock | 26 | 3.011 | 0 | 0 | 1275 | 0.000000e+00 | 7.432795e-10 |
| **22** | Lock | 11 | 0.0 | 0 | 0 | 185 | 0.000000e+00 | 1.078484e-10 |
| **23** | Lock | 27 | 0.0 | 0 | 0 | 254 | 0.000000e+00 | 1.469378e-10 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 453 | 0.000000e+00 | 2.620583e-10 |
| **25** | Lock | 28 | 0.0 | 0 | 0 | 358 | 0.000000e+00 | 2.071013e-10 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 90 | 0.000000e+00 | 5.206457e-11 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 60 | 0.000000e+00 | 3.470972e-11 |
| **28** | Lock | 14 | 0.0 | 0 | 0 | 78 | 0.000000e+00 | 4.512263e-11 |
| **29** | Lock | 30 | 0.0 | 0 | 0 | 45 | 0.000000e+00 | 2.603229e-11 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 19 | 0.000000e+00 | 1.099141e-11 |
| **31** | Lock | 31 | 3.011 | 0 | 0 | 11 | 0.000000e+00 | 6.363448e-12 |

# PCS Lane Statistics - 128

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 6501.0 | 0.000000e+00 | 3.821668e-09 |
| **0** | Lock | 16 | 6.023 | 0 | 0 | 49 | 0.000000e+00 | 2.901620e-11 |
| **1** | Lock | 0 | 3.011 | 0 | 0 | 28 | 0.000000e+00 | 1.658069e-11 |
| **2** | Lock | 17 | 3.011 | 0 | 0 | 6 | 0.000000e+00 | 3.553004e-12 |
| **3** | Lock | 1 | 3.011 | 0 | 0 | 3 | 0.000000e+00 | 1.776502e-12 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 25 | 0.000000e+00 | 1.480418e-11 |
| **5** | Lock | 18 | 3.011 | 0 | 0 | 36 | 0.000000e+00 | 2.131802e-11 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.877681e-13 |
| **8** | Lock | 20 | 6.023 | 0 | 0 | 95 | 0.000000e+00 | 5.583797e-11 |
| **9** | Lock | 4 | 3.011 | 0 | 0 | 155 | 0.000000e+00 | 9.110405e-11 |
| **10** | Lock | 21 | 6.023 | 0 | 0 | 13 | 0.000000e+00 | 7.640985e-12 |
| **11** | Lock | 5 | 3.011 | 0 | 0 | 10 | 0.000000e+00 | 5.877681e-12 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 4 | 0.000000e+00 | 2.351072e-12 |
| **13** | Lock | 6 | 0.0 | 0 | 0 | 7 | 0.000000e+00 | 4.114377e-12 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 7 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.877681e-13 |
| **16** | Lock | 8 | 3.011 | 0 | 0 | 1306 | 0.000000e+00 | 7.676251e-10 |
| **17** | Lock | 24 | 6.023 | 0 | 0 | 3703 | 0.000000e+00 | 2.176505e-09 |
| **18** | Lock | 9 | 0.0 | 0 | 0 | 248 | 0.000000e+00 | 1.457665e-10 |
| **19** | Lock | 25 | 3.011 | 0 | 0 | 385 | 0.000000e+00 | 2.262907e-10 |
| **20** | Lock | 26 | 6.023 | 0 | 0 | 85 | 0.000000e+00 | 4.996029e-11 |
| **21** | Lock | 10 | 0.0 | 0 | 0 | 71 | 0.000000e+00 | 4.173153e-11 |
| **22** | Lock | 27 | 3.011 | 0 | 0 | 5 | 0.000000e+00 | 2.938840e-12 |
| **23** | Lock | 11 | 3.011 | 0 | 0 | 5 | 0.000000e+00 | 2.938840e-12 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 135 | 0.000000e+00 | 7.934869e-11 |
| **25** | Lock | 28 | 0.0 | 0 | 0 | 53 | 0.000000e+00 | 3.115171e-11 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 23 | 0.000000e+00 | 1.351867e-11 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 18 | 0.000000e+00 | 1.057983e-11 |
| **28** | Lock | 14 | 0.0 | 0 | 0 | 18 | 0.000000e+00 | 1.057983e-11 |
| **29** | Lock | 30 | 0.0 | 0 | 0 | 13 | 0.000000e+00 | 7.581757e-12 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |

Port - 1,1,4

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 28806.0 | 0.000000e+00 | 1.677166e-08 |
| **0** | Lock | 16 | 3.011 | 0 | 0 | 50 | 0.000000e+00 | 2.916060e-11 |
| **1** | Lock | 0 | 0.0 | 0 | 0 | 53 | 0.000000e+00 | 3.091024e-11 |
| **2** | Lock | 17 | 0.0 | 0 | 0 | 3 | 0.000000e+00 | 1.749636e-12 |
| **3** | Lock | 1 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.832120e-13 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 106 | 0.000000e+00 | 6.182048e-11 |
| **5** | Lock | 18 | 6.023 | 0 | 0 | 63 | 0.000000e+00 | 3.674236e-11 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 6 | 0.000000e+00 | 3.499272e-12 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 4 | 0.000000e+00 | 2.332848e-12 |
| **8** | Lock | 4 | 3.011 | 0 | 0 | 284 | 0.000000e+00 | 1.656322e-10 |
| **9** | Lock | 20 | 6.023 | 0 | 0 | 128 | 0.000000e+00 | 7.465114e-11 |
| **10** | Lock | 5 | 0.0 | 0 | 0 | 50 | 0.000000e+00 | 2.916060e-11 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 25 | 0.000000e+00 | 1.458030e-11 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 5 | 0.000000e+00 | 2.916060e-12 |
| **13** | Lock | 22 | 6.023 | 0 | 0 | 27 | 0.000000e+00 | 1.574673e-11 |
| **14** | Lock | 7 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 23 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.832120e-13 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 8945 | 0.000000e+00 | 5.216832e-09 |
| **17** | Lock | 8 | 0.0 | 0 | 0 | 9566 | 0.000000e+00 | 5.579006e-09 |
| **18** | Lock | 25 | 0.0 | 0 | 0 | 3171 | 0.000000e+00 | 1.849365e-09 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 2463 | 0.000000e+00 | 1.425402e-09 |
| **20** | Lock | 10 | 0.0 | 0 | 0 | 1097 | 0.000000e+00 | 6.348624e-10 |
| **21** | Lock | 26 | 3.011 | 0 | 0 | 1239 | 0.000000e+00 | 7.170415e-10 |
| **22** | Lock | 11 | 0.0 | 0 | 0 | 182 | 0.000000e+00 | 1.053281e-10 |
| **23** | Lock | 27 | 0.0 | 0 | 0 | 254 | 0.000000e+00 | 1.469964e-10 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 427 | 0.000000e+00 | 2.471160e-10 |
| **25** | Lock | 28 | 0.0 | 0 | 0 | 342 | 0.000000e+00 | 1.979243e-10 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 93 | 0.000000e+00 | 5.382152e-11 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 66 | 0.000000e+00 | 3.819592e-11 |
| **28** | Lock | 14 | 0.0 | 0 | 0 | 90 | 0.000000e+00 | 5.208534e-11 |
| **29** | Lock | 30 | 0.0 | 0 | 0 | 37 | 0.000000e+00 | 2.141286e-11 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 20 | 0.000000e+00 | 1.157452e-11 |
| **31** | Lock | 31 | 3.011 | 0 | 0 | 8 | 0.000000e+00 | 4.629808e-12 |

# PCS Lane Statistics - 256

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 6473.0 | 0.000000e+00 | 3.802492e-09 |
| **0** | Lock | 16 | 6.023 | 0 | 0 | 48 | 0.000000e+00 | 2.842587e-11 |
| **1** | Lock | 0 | 3.011 | 0 | 0 | 26 | 0.000000e+00 | 1.527722e-11 |
| **2** | Lock | 17 | 3.011 | 0 | 0 | 12 | 0.000000e+00 | 7.051024e-12 |
| **3** | Lock | 1 | 3.011 | 0 | 0 | 6 | 0.000000e+00 | 3.525512e-12 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 19 | 0.000000e+00 | 1.116412e-11 |
| **5** | Lock | 18 | 3.011 | 0 | 0 | 32 | 0.000000e+00 | 1.880273e-11 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **8** | Lock | 20 | 6.023 | 0 | 0 | 75 | 0.000000e+00 | 4.406890e-11 |
| **9** | Lock | 4 | 3.011 | 0 | 0 | 149 | 0.000000e+00 | 8.755022e-11 |
| **10** | Lock | 21 | 6.023 | 0 | 0 | 11 | 0.000000e+00 | 6.463439e-12 |
| **11** | Lock | 5 | 3.011 | 0 | 0 | 15 | 0.000000e+00 | 8.813780e-12 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 8 | 0.000000e+00 | 4.700683e-12 |
| **13** | Lock | 6 | 0.0 | 0 | 0 | 15 | 0.000000e+00 | 8.813780e-12 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 7 | 3.011 | 0 | 0 | 2 | 0.000000e+00 | 1.175171e-12 |
| **16** | Lock | 8 | 3.011 | 0 | 0 | 1228 | 0.000000e+00 | 7.215548e-10 |
| **17** | Lock | 24 | 6.023 | 0 | 0 | 3770 | 0.000000e+00 | 2.215197e-09 |
| **18** | Lock | 9 | 0.0 | 0 | 0 | 227 | 0.000000e+00 | 1.333819e-10 |
| **19** | Lock | 25 | 3.011 | 0 | 0 | 402 | 0.000000e+00 | 2.362093e-10 |
| **20** | Lock | 26 | 6.023 | 0 | 0 | 86 | 0.000000e+00 | 5.053234e-11 |
| **21** | Lock | 10 | 0.0 | 0 | 0 | 73 | 0.000000e+00 | 4.289373e-11 |
| **22** | Lock | 27 | 3.011 | 0 | 0 | 12 | 0.000000e+00 | 7.051024e-12 |
| **23** | Lock | 11 | 3.011 | 0 | 0 | 6 | 0.000000e+00 | 3.498199e-12 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 126 | 0.000000e+00 | 7.346217e-11 |
| **25** | Lock | 28 | 0.0 | 0 | 0 | 59 | 0.000000e+00 | 3.439895e-11 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 17 | 0.000000e+00 | 9.911563e-12 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 12 | 0.000000e+00 | 6.996397e-12 |
| **28** | Lock | 14 | 0.0 | 0 | 0 | 21 | 0.000000e+00 | 1.224370e-11 |
| **29** | Lock | 30 | 0.0 | 0 | 0 | 12 | 0.000000e+00 | 6.996397e-12 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 1.166066e-12 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 1.166066e-12 |

Port - 1,1,4

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 28667.0 | 0.000000e+00 | 1.658913e-08 |
| **0** | Lock | 16 | 3.011 | 0 | 0 | 50 | 0.000000e+00 | 2.915165e-11 |
| **1** | Lock | 0 | 0.0 | 0 | 0 | 50 | 0.000000e+00 | 2.915165e-11 |
| **2** | Lock | 17 | 0.0 | 0 | 0 | 3 | 0.000000e+00 | 1.749099e-12 |
| **3** | Lock | 1 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.830331e-13 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 99 | 0.000000e+00 | 5.772028e-11 |
| **5** | Lock | 18 | 6.023 | 0 | 0 | 69 | 0.000000e+00 | 4.022928e-11 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 5 | 0.000000e+00 | 2.915165e-12 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 7 | 0.000000e+00 | 4.081232e-12 |
| **8** | Lock | 4 | 3.011 | 0 | 0 | 311 | 0.000000e+00 | 1.813233e-10 |
| **9** | Lock | 20 | 6.023 | 0 | 0 | 154 | 0.000000e+00 | 8.978710e-11 |
| **10** | Lock | 5 | 0.0 | 0 | 0 | 51 | 0.000000e+00 | 2.973469e-11 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 32 | 0.000000e+00 | 1.865706e-11 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 4 | 0.000000e+00 | 2.332132e-12 |
| **13** | Lock | 22 | 6.023 | 0 | 0 | 23 | 0.000000e+00 | 1.330672e-11 |
| **14** | Lock | 7 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 23 | 3.011 | 0 | 0 | 2 | 0.000000e+00 | 1.157106e-12 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 8911 | 0.000000e+00 | 5.155486e-09 |
| **17** | Lock | 8 | 0.0 | 0 | 0 | 9417 | 0.000000e+00 | 5.448234e-09 |
| **18** | Lock | 25 | 0.0 | 0 | 0 | 3229 | 0.000000e+00 | 1.868148e-09 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 2353 | 0.000000e+00 | 1.361335e-09 |
| **20** | Lock | 10 | 0.0 | 0 | 0 | 1127 | 0.000000e+00 | 6.520293e-10 |
| **21** | Lock | 26 | 3.011 | 0 | 0 | 1211 | 0.000000e+00 | 7.006278e-10 |
| **22** | Lock | 11 | 0.0 | 0 | 0 | 204 | 0.000000e+00 | 1.180248e-10 |
| **23** | Lock | 27 | 0.0 | 0 | 0 | 240 | 0.000000e+00 | 1.388527e-10 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 431 | 0.000000e+00 | 2.493564e-10 |
| **25** | Lock | 28 | 0.0 | 0 | 0 | 339 | 0.000000e+00 | 1.961295e-10 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 111 | 0.000000e+00 | 6.421939e-11 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 57 | 0.000000e+00 | 3.297752e-11 |
| **28** | Lock | 14 | 0.0 | 0 | 0 | 88 | 0.000000e+00 | 5.091267e-11 |
| **29** | Lock | 30 | 0.0 | 0 | 0 | 53 | 0.000000e+00 | 3.066331e-11 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 20 | 0.000000e+00 | 1.157106e-11 |
| **31** | Lock | 31 | 3.011 | 0 | 0 | 15 | 0.000000e+00 | 8.678296e-12 |

# PCS Lane Statistics - 512

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 6124.0 | 0.000000e+00 | 3.570486e-09 |
| **0** | Lock | 16 | 6.023 | 0 | 0 | 46 | 0.000000e+00 | 2.682869e-11 |
| **1** | Lock | 0 | 3.011 | 0 | 0 | 24 | 0.000000e+00 | 1.399758e-11 |
| **2** | Lock | 17 | 3.011 | 0 | 0 | 10 | 0.000000e+00 | 5.832325e-12 |
| **3** | Lock | 1 | 3.011 | 0 | 0 | 7 | 0.000000e+00 | 4.082627e-12 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 28 | 0.000000e+00 | 1.633051e-11 |
| **5** | Lock | 18 | 3.011 | 0 | 0 | 31 | 0.000000e+00 | 1.808021e-11 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.832325e-13 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **8** | Lock | 20 | 6.023 | 0 | 0 | 93 | 0.000000e+00 | 5.424062e-11 |
| **9** | Lock | 4 | 3.011 | 0 | 0 | 124 | 0.000000e+00 | 7.232083e-11 |
| **10** | Lock | 21 | 6.023 | 0 | 0 | 6 | 0.000000e+00 | 3.499395e-12 |
| **11** | Lock | 5 | 3.011 | 0 | 0 | 10 | 0.000000e+00 | 5.832325e-12 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.832325e-13 |
| **13** | Lock | 6 | 0.0 | 0 | 0 | 7 | 0.000000e+00 | 4.082627e-12 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 1 | 0.000000e+00 | 5.832325e-13 |
| **15** | Lock | 7 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.832325e-13 |
| **16** | Lock | 8 | 3.011 | 0 | 0 | 1172 | 0.000000e+00 | 6.835485e-10 |
| **17** | Lock | 24 | 6.023 | 0 | 0 | 3506 | 0.000000e+00 | 2.044813e-09 |
| **18** | Lock | 9 | 0.0 | 0 | 0 | 222 | 0.000000e+00 | 1.294776e-10 |
| **19** | Lock | 25 | 3.011 | 0 | 0 | 386 | 0.000000e+00 | 2.251277e-10 |
| **20** | Lock | 26 | 6.023 | 0 | 0 | 96 | 0.000000e+00 | 5.599032e-11 |
| **21** | Lock | 10 | 0.0 | 0 | 0 | 76 | 0.000000e+00 | 4.432567e-11 |
| **22** | Lock | 27 | 3.011 | 0 | 0 | 11 | 0.000000e+00 | 6.366544e-12 |
| **23** | Lock | 11 | 3.011 | 0 | 0 | 6 | 0.000000e+00 | 3.472660e-12 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 145 | 0.000000e+00 | 8.392263e-11 |
| **25** | Lock | 28 | 0.0 | 0 | 0 | 63 | 0.000000e+00 | 3.646293e-11 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 15 | 0.000000e+00 | 8.681651e-12 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 13 | 0.000000e+00 | 7.524098e-12 |
| **28** | Lock | 14 | 0.0 | 0 | 0 | 11 | 0.000000e+00 | 6.366544e-12 |
| **29** | Lock | 30 | 0.0 | 0 | 0 | 12 | 0.000000e+00 | 6.945321e-12 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |

Port - 1,1,4

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 29023.0 | 0.000000e+00 | 1.666756e-08 |
| **0** | Lock | 16 | 3.011 | 0 | 0 | 56 | 0.000000e+00 | 3.241150e-11 |
| **1** | Lock | 0 | 0.0 | 0 | 0 | 59 | 0.000000e+00 | 3.414783e-11 |
| **2** | Lock | 17 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **3** | Lock | 1 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 109 | 0.000000e+00 | 6.308666e-11 |
| **5** | Lock | 18 | 6.023 | 0 | 0 | 68 | 0.000000e+00 | 3.935682e-11 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 9 | 0.000000e+00 | 5.208991e-12 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 1.157553e-12 |
| **8** | Lock | 4 | 3.011 | 0 | 0 | 300 | 0.000000e+00 | 1.736330e-10 |
| **9** | Lock | 20 | 6.023 | 0 | 0 | 168 | 0.000000e+00 | 9.723449e-11 |
| **10** | Lock | 5 | 0.0 | 0 | 0 | 56 | 0.000000e+00 | 3.241150e-11 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 29 | 0.000000e+00 | 1.678453e-11 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 11 | 0.000000e+00 | 6.315667e-12 |
| **13** | Lock | 22 | 6.023 | 0 | 0 | 27 | 0.000000e+00 | 1.550209e-11 |
| **14** | Lock | 7 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 23 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 8913 | 0.000000e+00 | 5.117413e-09 |
| **17** | Lock | 8 | 0.0 | 0 | 0 | 9584 | 0.000000e+00 | 5.502669e-09 |
| **18** | Lock | 25 | 0.0 | 0 | 0 | 3243 | 0.000000e+00 | 1.861974e-09 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 2450 | 0.000000e+00 | 1.406671e-09 |
| **20** | Lock | 10 | 0.0 | 0 | 0 | 1070 | 0.000000e+00 | 6.143422e-10 |
| **21** | Lock | 26 | 3.011 | 0 | 0 | 1272 | 0.000000e+00 | 7.303208e-10 |
| **22** | Lock | 11 | 0.0 | 0 | 0 | 179 | 0.000000e+00 | 1.027731e-10 |
| **23** | Lock | 27 | 0.0 | 0 | 0 | 266 | 0.000000e+00 | 1.527243e-10 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 451 | 0.000000e+00 | 2.589424e-10 |
| **25** | Lock | 28 | 0.0 | 0 | 0 | 376 | 0.000000e+00 | 2.158810e-10 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 115 | 0.000000e+00 | 6.602743e-11 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 50 | 0.000000e+00 | 2.870758e-11 |
| **28** | Lock | 14 | 0.0 | 0 | 0 | 83 | 0.000000e+00 | 4.765458e-11 |
| **29** | Lock | 30 | 0.0 | 0 | 0 | 52 | 0.000000e+00 | 2.985588e-11 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 15 | 0.000000e+00 | 8.612274e-12 |
| **31** | Lock | 31 | 3.011 | 0 | 0 | 10 | 0.000000e+00 | 5.741516e-12 |

# PCS Lane Statistics - 1024

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 6074.0 | 0.000000e+00 | 3.544787e-09 |
| **0** | Lock | 16 | 6.023 | 0 | 0 | 48 | 0.000000e+00 | 2.822381e-11 |
| **1** | Lock | 0 | 3.011 | 0 | 0 | 28 | 0.000000e+00 | 1.646389e-11 |
| **2** | Lock | 17 | 3.011 | 0 | 0 | 11 | 0.000000e+00 | 6.467958e-12 |
| **3** | Lock | 1 | 3.011 | 0 | 0 | 3 | 0.000000e+00 | 1.763988e-12 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 28 | 0.000000e+00 | 1.633985e-11 |
| **5** | Lock | 18 | 3.011 | 0 | 0 | 45 | 0.000000e+00 | 2.626048e-11 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.835662e-13 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **8** | Lock | 20 | 6.023 | 0 | 0 | 73 | 0.000000e+00 | 4.260033e-11 |
| **9** | Lock | 4 | 3.011 | 0 | 0 | 147 | 0.000000e+00 | 8.578423e-11 |
| **10** | Lock | 21 | 6.023 | 0 | 0 | 8 | 0.000000e+00 | 4.668529e-12 |
| **11** | Lock | 5 | 3.011 | 0 | 0 | 20 | 0.000000e+00 | 1.167132e-11 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 5 | 0.000000e+00 | 2.917831e-12 |
| **13** | Lock | 6 | 0.0 | 0 | 0 | 7 | 0.000000e+00 | 4.084963e-12 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 7 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **16** | Lock | 8 | 3.011 | 0 | 0 | 1233 | 0.000000e+00 | 7.195371e-10 |
| **17** | Lock | 24 | 6.023 | 0 | 0 | 3359 | 0.000000e+00 | 1.960199e-09 |
| **18** | Lock | 9 | 0.0 | 0 | 0 | 242 | 0.000000e+00 | 1.412230e-10 |
| **19** | Lock | 25 | 3.011 | 0 | 0 | 395 | 0.000000e+00 | 2.305086e-10 |
| **20** | Lock | 26 | 6.023 | 0 | 0 | 89 | 0.000000e+00 | 5.193739e-11 |
| **21** | Lock | 10 | 0.0 | 0 | 0 | 67 | 0.000000e+00 | 3.909893e-11 |
| **22** | Lock | 27 | 3.011 | 0 | 0 | 9 | 0.000000e+00 | 5.252096e-12 |
| **23** | Lock | 11 | 3.011 | 0 | 0 | 5 | 0.000000e+00 | 2.917831e-12 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 120 | 0.000000e+00 | 7.002794e-11 |
| **25** | Lock | 28 | 0.0 | 0 | 0 | 69 | 0.000000e+00 | 4.026607e-11 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 21 | 0.000000e+00 | 1.225489e-11 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 9 | 0.000000e+00 | 5.209771e-12 |
| **28** | Lock | 14 | 0.0 | 0 | 0 | 20 | 0.000000e+00 | 1.157727e-11 |
| **29** | Lock | 30 | 0.0 | 0 | 0 | 10 | 0.000000e+00 | 5.788634e-12 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.788634e-13 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.788634e-13 |

Port - 1,1,4

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 28665.0 | 0.000000e+00 | 1.647064e-08 |
| **0** | Lock | 16 | 3.011 | 0 | 0 | 64 | 0.000000e+00 | 3.704726e-11 |
| **1** | Lock | 0 | 0.0 | 0 | 0 | 49 | 0.000000e+00 | 2.836431e-11 |
| **2** | Lock | 17 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.788634e-13 |
| **3** | Lock | 1 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 91 | 0.000000e+00 | 5.267657e-11 |
| **5** | Lock | 18 | 6.023 | 0 | 0 | 65 | 0.000000e+00 | 3.762612e-11 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 5 | 0.000000e+00 | 2.894317e-12 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.788634e-13 |
| **8** | Lock | 4 | 3.011 | 0 | 0 | 318 | 0.000000e+00 | 1.840786e-10 |
| **9** | Lock | 20 | 6.023 | 0 | 0 | 149 | 0.000000e+00 | 8.625064e-11 |
| **10** | Lock | 5 | 0.0 | 0 | 0 | 47 | 0.000000e+00 | 2.720658e-11 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 25 | 0.000000e+00 | 1.447158e-11 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 6 | 0.000000e+00 | 3.473180e-12 |
| **13** | Lock | 22 | 6.023 | 0 | 0 | 37 | 0.000000e+00 | 2.141795e-11 |
| **14** | Lock | 7 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 23 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.744588e-13 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 8806 | 0.000000e+00 | 5.058684e-09 |
| **17** | Lock | 8 | 0.0 | 0 | 0 | 9385 | 0.000000e+00 | 5.391295e-09 |
| **18** | Lock | 25 | 0.0 | 0 | 0 | 3382 | 0.000000e+00 | 1.942820e-09 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 2378 | 0.000000e+00 | 1.366063e-09 |
| **20** | Lock | 10 | 0.0 | 0 | 0 | 1121 | 0.000000e+00 | 6.439683e-10 |
| **21** | Lock | 26 | 3.011 | 0 | 0 | 1208 | 0.000000e+00 | 6.939462e-10 |
| **22** | Lock | 11 | 0.0 | 0 | 0 | 154 | 0.000000e+00 | 8.846665e-11 |
| **23** | Lock | 27 | 0.0 | 0 | 0 | 226 | 0.000000e+00 | 1.298277e-10 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 470 | 0.000000e+00 | 2.699956e-10 |
| **25** | Lock | 28 | 0.0 | 0 | 0 | 343 | 0.000000e+00 | 1.970394e-10 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 104 | 0.000000e+00 | 5.974371e-11 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 59 | 0.000000e+00 | 3.389307e-11 |
| **28** | Lock | 14 | 0.0 | 0 | 0 | 84 | 0.000000e+00 | 4.825454e-11 |
| **29** | Lock | 30 | 0.0 | 0 | 0 | 57 | 0.000000e+00 | 3.274415e-11 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 15 | 0.000000e+00 | 8.616881e-12 |
| **31** | Lock | 31 | 3.011 | 0 | 0 | 14 | 0.000000e+00 | 8.042423e-12 |

# PCS Lane Statistics - 1280

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 6127.0 | 0.000000e+00 | 3.575310e-09 |
| **0** | Lock | 16 | 6.023 | 0 | 0 | 47 | 0.000000e+00 | 2.765006e-11 |
| **1** | Lock | 0 | 3.011 | 0 | 0 | 34 | 0.000000e+00 | 2.000217e-11 |
| **2** | Lock | 17 | 3.011 | 0 | 0 | 12 | 0.000000e+00 | 7.059589e-12 |
| **3** | Lock | 1 | 3.011 | 0 | 0 | 8 | 0.000000e+00 | 4.706393e-12 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 25 | 0.000000e+00 | 1.470748e-11 |
| **5** | Lock | 18 | 3.011 | 0 | 0 | 34 | 0.000000e+00 | 1.983969e-11 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 1.167040e-12 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 1.167040e-12 |
| **8** | Lock | 20 | 6.023 | 0 | 0 | 84 | 0.000000e+00 | 4.901570e-11 |
| **9** | Lock | 4 | 3.011 | 0 | 0 | 133 | 0.000000e+00 | 7.760819e-11 |
| **10** | Lock | 21 | 6.023 | 0 | 0 | 6 | 0.000000e+00 | 3.501121e-12 |
| **11** | Lock | 5 | 3.011 | 0 | 0 | 10 | 0.000000e+00 | 5.835202e-12 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 7 | 0.000000e+00 | 4.084641e-12 |
| **13** | Lock | 6 | 0.0 | 0 | 0 | 7 | 0.000000e+00 | 4.084641e-12 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 7 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **16** | Lock | 8 | 3.011 | 0 | 0 | 1191 | 0.000000e+00 | 6.949726e-10 |
| **17** | Lock | 24 | 6.023 | 0 | 0 | 3521 | 0.000000e+00 | 2.054575e-09 |
| **18** | Lock | 9 | 0.0 | 0 | 0 | 216 | 0.000000e+00 | 1.260404e-10 |
| **19** | Lock | 25 | 3.011 | 0 | 0 | 387 | 0.000000e+00 | 2.258223e-10 |
| **20** | Lock | 26 | 6.023 | 0 | 0 | 76 | 0.000000e+00 | 4.434754e-11 |
| **21** | Lock | 10 | 0.0 | 0 | 0 | 63 | 0.000000e+00 | 3.676177e-11 |
| **22** | Lock | 27 | 3.011 | 0 | 0 | 11 | 0.000000e+00 | 6.418722e-12 |
| **23** | Lock | 11 | 3.011 | 0 | 0 | 2 | 0.000000e+00 | 1.167040e-12 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 133 | 0.000000e+00 | 7.760819e-11 |
| **25** | Lock | 28 | 0.0 | 0 | 0 | 50 | 0.000000e+00 | 2.895176e-11 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 21 | 0.000000e+00 | 1.215974e-11 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 12 | 0.000000e+00 | 6.948422e-12 |
| **28** | Lock | 14 | 0.0 | 0 | 0 | 19 | 0.000000e+00 | 1.100167e-11 |
| **29** | Lock | 30 | 0.0 | 0 | 0 | 9 | 0.000000e+00 | 5.211317e-12 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 5 | 0.000000e+00 | 2.895176e-12 |

Port - 1,1,4

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 29759.0 | 0.000000e+00 | 1.710947e-08 |
| **0** | Lock | 16 | 3.011 | 0 | 0 | 52 | 0.000000e+00 | 3.010983e-11 |
| **1** | Lock | 0 | 0.0 | 0 | 0 | 56 | 0.000000e+00 | 3.242597e-11 |
| **2** | Lock | 17 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **3** | Lock | 1 | 0.0 | 0 | 0 | 4 | 0.000000e+00 | 2.316141e-12 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 90 | 0.000000e+00 | 5.211317e-11 |
| **5** | Lock | 18 | 6.023 | 0 | 0 | 59 | 0.000000e+00 | 3.416308e-11 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 4 | 0.000000e+00 | 2.316141e-12 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 5 | 0.000000e+00 | 2.895176e-12 |
| **8** | Lock | 4 | 3.011 | 0 | 0 | 323 | 0.000000e+00 | 1.870284e-10 |
| **9** | Lock | 20 | 6.023 | 0 | 0 | 169 | 0.000000e+00 | 9.785694e-11 |
| **10** | Lock | 5 | 0.0 | 0 | 0 | 52 | 0.000000e+00 | 3.010983e-11 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 32 | 0.000000e+00 | 1.852913e-11 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 10 | 0.000000e+00 | 5.790352e-12 |
| **13** | Lock | 22 | 6.023 | 0 | 0 | 27 | 0.000000e+00 | 1.551995e-11 |
| **14** | Lock | 7 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.748129e-13 |
| **15** | Lock | 23 | 3.011 | 0 | 0 | 2 | 0.000000e+00 | 1.149626e-12 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 9245 | 0.000000e+00 | 5.314145e-09 |
| **17** | Lock | 8 | 0.0 | 0 | 0 | 9759 | 0.000000e+00 | 5.609599e-09 |
| **18** | Lock | 25 | 0.0 | 0 | 0 | 3397 | 0.000000e+00 | 1.952639e-09 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 2512 | 0.000000e+00 | 1.443930e-09 |
| **20** | Lock | 10 | 0.0 | 0 | 0 | 1136 | 0.000000e+00 | 6.529875e-10 |
| **21** | Lock | 26 | 3.011 | 0 | 0 | 1208 | 0.000000e+00 | 6.943740e-10 |
| **22** | Lock | 11 | 0.0 | 0 | 0 | 176 | 0.000000e+00 | 1.011671e-10 |
| **23** | Lock | 27 | 0.0 | 0 | 0 | 252 | 0.000000e+00 | 1.448529e-10 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 468 | 0.000000e+00 | 2.690124e-10 |
| **25** | Lock | 28 | 0.0 | 0 | 0 | 407 | 0.000000e+00 | 2.339489e-10 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 113 | 0.000000e+00 | 6.495386e-11 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 49 | 0.000000e+00 | 2.816583e-11 |
| **28** | Lock | 14 | 0.0 | 0 | 0 | 76 | 0.000000e+00 | 4.368578e-11 |
| **29** | Lock | 30 | 0.0 | 0 | 0 | 43 | 0.000000e+00 | 2.471696e-11 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 21 | 0.000000e+00 | 1.207107e-11 |
| **31** | Lock | 31 | 3.011 | 0 | 0 | 11 | 0.000000e+00 | 6.322942e-12 |

# PCS Lane Statistics - 1518

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 6178.0 | 0.000000e+00 | 3.607093e-09 |
| **0** | Lock | 16 | 6.023 | 0 | 0 | 49 | 0.000000e+00 | 2.882798e-11 |
| **1** | Lock | 0 | 3.011 | 0 | 0 | 21 | 0.000000e+00 | 1.235485e-11 |
| **2** | Lock | 17 | 3.011 | 0 | 0 | 10 | 0.000000e+00 | 5.883260e-12 |
| **3** | Lock | 1 | 3.011 | 0 | 0 | 5 | 0.000000e+00 | 2.941630e-12 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 29 | 0.000000e+00 | 1.706145e-11 |
| **5** | Lock | 18 | 3.011 | 0 | 0 | 44 | 0.000000e+00 | 2.588635e-11 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.837649e-13 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.837649e-13 |
| **8** | Lock | 20 | 6.023 | 0 | 0 | 90 | 0.000000e+00 | 5.253884e-11 |
| **9** | Lock | 4 | 3.011 | 0 | 0 | 135 | 0.000000e+00 | 7.880826e-11 |
| **10** | Lock | 21 | 6.023 | 0 | 0 | 8 | 0.000000e+00 | 4.670119e-12 |
| **11** | Lock | 5 | 3.011 | 0 | 0 | 13 | 0.000000e+00 | 7.588944e-12 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 3 | 0.000000e+00 | 1.751295e-12 |
| **13** | Lock | 6 | 0.0 | 0 | 0 | 6 | 0.000000e+00 | 3.502590e-12 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 7 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.837649e-13 |
| **16** | Lock | 8 | 3.011 | 0 | 0 | 1214 | 0.000000e+00 | 7.086906e-10 |
| **17** | Lock | 24 | 6.023 | 0 | 0 | 3480 | 0.000000e+00 | 2.031502e-09 |
| **18** | Lock | 9 | 0.0 | 0 | 0 | 220 | 0.000000e+00 | 1.284283e-10 |
| **19** | Lock | 25 | 3.011 | 0 | 0 | 387 | 0.000000e+00 | 2.259170e-10 |
| **20** | Lock | 26 | 6.023 | 0 | 0 | 85 | 0.000000e+00 | 4.962002e-11 |
| **21** | Lock | 10 | 0.0 | 0 | 0 | 78 | 0.000000e+00 | 4.553366e-11 |
| **22** | Lock | 27 | 3.011 | 0 | 0 | 20 | 0.000000e+00 | 1.167530e-11 |
| **23** | Lock | 11 | 3.011 | 0 | 0 | 5 | 0.000000e+00 | 2.918825e-12 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 151 | 0.000000e+00 | 8.814850e-11 |
| **25** | Lock | 28 | 0.0 | 0 | 0 | 51 | 0.000000e+00 | 2.977201e-11 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 31 | 0.000000e+00 | 1.809671e-11 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 12 | 0.000000e+00 | 7.005179e-12 |
| **28** | Lock | 14 | 0.0 | 0 | 0 | 16 | 0.000000e+00 | 9.267435e-12 |
| **29** | Lock | 30 | 0.0 | 0 | 0 | 9 | 0.000000e+00 | 5.212932e-12 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.792147e-13 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 1.158429e-12 |

Port - 1,1,4

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 29536.0 | 0.000000e+00 | 1.709005e-08 |
| **0** | Lock | 16 | 3.011 | 0 | 0 | 42 | 0.000000e+00 | 2.432702e-11 |
| **1** | Lock | 0 | 0.0 | 0 | 0 | 55 | 0.000000e+00 | 3.185681e-11 |
| **2** | Lock | 17 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.792147e-13 |
| **3** | Lock | 1 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 99 | 0.000000e+00 | 5.734225e-11 |
| **5** | Lock | 18 | 6.023 | 0 | 0 | 73 | 0.000000e+00 | 4.228267e-11 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 5 | 0.000000e+00 | 2.896073e-12 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 4 | 0.000000e+00 | 2.316859e-12 |
| **8** | Lock | 4 | 3.011 | 0 | 0 | 300 | 0.000000e+00 | 1.737644e-10 |
| **9** | Lock | 20 | 6.023 | 0 | 0 | 164 | 0.000000e+00 | 9.499120e-11 |
| **10** | Lock | 5 | 0.0 | 0 | 0 | 47 | 0.000000e+00 | 2.722309e-11 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 29 | 0.000000e+00 | 1.679723e-11 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 7 | 0.000000e+00 | 4.054503e-12 |
| **13** | Lock | 22 | 6.023 | 0 | 0 | 34 | 0.000000e+00 | 1.969330e-11 |
| **14** | Lock | 7 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.792147e-13 |
| **15** | Lock | 23 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.792147e-13 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 9185 | 0.000000e+00 | 5.320087e-09 |
| **17** | Lock | 8 | 0.0 | 0 | 0 | 9727 | 0.000000e+00 | 5.634021e-09 |
| **18** | Lock | 25 | 0.0 | 0 | 0 | 3297 | 0.000000e+00 | 1.909671e-09 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 2426 | 0.000000e+00 | 1.405175e-09 |
| **20** | Lock | 10 | 0.0 | 0 | 0 | 1186 | 0.000000e+00 | 6.817714e-10 |
| **21** | Lock | 26 | 3.011 | 0 | 0 | 1239 | 0.000000e+00 | 7.122384e-10 |
| **22** | Lock | 11 | 0.0 | 0 | 0 | 206 | 0.000000e+00 | 1.184190e-10 |
| **23** | Lock | 27 | 0.0 | 0 | 0 | 260 | 0.000000e+00 | 1.494608e-10 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 458 | 0.000000e+00 | 2.632810e-10 |
| **25** | Lock | 28 | 0.0 | 0 | 0 | 379 | 0.000000e+00 | 2.178679e-10 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 101 | 0.000000e+00 | 5.805979e-11 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 59 | 0.000000e+00 | 3.391612e-11 |
| **28** | Lock | 14 | 0.0 | 0 | 0 | 95 | 0.000000e+00 | 5.461069e-11 |
| **29** | Lock | 30 | 0.0 | 0 | 0 | 36 | 0.000000e+00 | 2.069458e-11 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 11 | 0.000000e+00 | 6.323344e-12 |
| **31** | Lock | 31 | 3.011 | 0 | 0 | 9 | 0.000000e+00 | 5.173645e-12 |

# L2 Traffic Test Summary

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Frame Size** | **Tx Count** | **Rx Count** | **Loss Count** | **Loss %** |
| **64** | 143204631480 | 143204631480 | 0 | 0.0 |
| **128** | 81173533113 | 81173533113 | 0 | 0.0 |
| **256** | 43560947611 | 43560947611 | 0 | 0.0 |
| **512** | 22663141934 | 22663141934 | 0 | 0.0 |
| **1024** | 11560229070 | 11560229070 | 0 | 0.0 |
| **1280** | 9284002976 | 9284002976 | 0 | 0.0 |
| **1518** | 7846566121 | 7846566121 | 0 | 0.0 |

# Port Statistics - 64

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,3** | **Port 1,1,4** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.138464747 | 00:01:00.153425703 |
| framesSent | 71593410408 | 71611221072 |
| framesReceived | 71593410408 | 71611221072 |
| bitsSent | 36655826128896 | 36664945188864 |
| bitsReceived | 36664945188864 | 36655826128896 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 5977 | 30098 |
| fecMaxSymbolErrors | 2 | 2 |
| fecCorrectedCodewords | 5974 | 30035 |
| fecTotalCodewords | 9848727376 | 9848428240 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 1.115589e-10 | 5.617872e-10 |
| fecMaxSymbolErrorsBin0 | 9848721402 | 9848398205 |
| fecMaxSymbolErrorsBin1 | 5971 | 29972 |
| fecMaxSymbolErrorsBin2 | 3 | 63 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48110771794176 | 48122740560384 |
| l1BitsReceived | 48122740560384 | 48110771794176 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2220875 | 85897121934 |
| averageLatency | 2220881 | 25679021 |
| maxLatency | 2220886 | 85897121946 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |

# Port Statistics - 128

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,3** | **Port 1,1,4** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.066769627 | 00:01:00.070059545 |
| framesSent | 40585655098 | 40587878015 |
| framesReceived | 40585655098 | 40587878015 |
| bitsSent | 41559710820352 | 41561987087360 |
| bitsReceived | 41561987087360 | 41559710820352 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 6400 | 28007 |
| fecMaxSymbolErrors | 2 | 2 |
| fecCorrectedCodewords | 6393 | 27967 |
| fecTotalCodewords | 9848016576 | 9847699952 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 1.194627e-10 | 5.227968e-10 |
| fecMaxSymbolErrorsBin0 | 9848010183 | 9847671985 |
| fecMaxSymbolErrorsBin1 | 6386 | 27927 |
| fecMaxSymbolErrorsBin2 | 7 | 40 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48053415636032 | 48056047569760 |
| l1BitsReceived | 48056047569760 | 48053415636032 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2220875 | 85897121934 |
| averageLatency | 2220881 | 38073742 |
| maxLatency | 2220886 | 85897121946 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |

# Port Statistics - 256

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,3** | **Port 1,1,4** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.112761463 | 00:01:00.115453950 |
| framesSent | 21779986037 | 21780961574 |
| framesReceived | 21779986037 | 21780961574 |
| bitsSent | 44605411403776 | 44607409303552 |
| bitsReceived | 44607409303552 | 44605411403776 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 6366 | 27585 |
| fecMaxSymbolErrors | 2 | 2 |
| fecCorrectedCodewords | 6356 | 27564 |
| fecTotalCodewords | 9842758864 | 9842011248 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 1.188915e-10 | 5.152171e-10 |
| fecMaxSymbolErrorsBin0 | 9842752508 | 9841983684 |
| fecMaxSymbolErrorsBin1 | 6346 | 27543 |
| fecMaxSymbolErrorsBin2 | 10 | 21 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48090209169696 | 48092363155392 |
| l1BitsReceived | 48092363155392 | 48090209169696 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2220875 | 85897121934 |
| averageLatency | 2220881 | 42225215 |
| maxLatency | 2220886 | 85897121946 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |

# Port Statistics - 512

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,3** | **Port 1,1,4** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.282340668 | 00:01:00.285574428 |
| framesSent | 11331267042 | 11331874892 |
| framesReceived | 11331267042 | 11331874892 |
| bitsSent | 46412869804032 | 46415359557632 |
| bitsReceived | 46415359557632 | 46412869804032 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 5996 | 27889 |
| fecMaxSymbolErrors | 2 | 2 |
| fecCorrectedCodewords | 5992 | 27842 |
| fecTotalCodewords | 9892435176 | 9892123888 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 1.114191e-10 | 5.182562e-10 |
| fecMaxSymbolErrorsBin0 | 9892429184 | 9892096046 |
| fecMaxSymbolErrorsBin1 | 5988 | 27795 |
| fecMaxSymbolErrorsBin2 | 4 | 47 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48225872530752 | 48228459540352 |
| l1BitsReceived | 48228459540352 | 48225872530752 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2220875 | 85897121934 |
| averageLatency | 2220881 | 8028136 |
| maxLatency | 2220886 | 85897121946 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |

# Port Statistics - 1024

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,3** | **Port 1,1,4** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.340030995 | 00:01:00.348760507 |
| framesSent | 5779696455 | 5780532615 |
| framesReceived | 5779696455 | 5780532615 |
| bitsSent | 47347273359360 | 47354123182080 |
| bitsReceived | 47354123182080 | 47347273359360 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 5932 | 27529 |
| fecMaxSymbolErrors | 2 | 2 |
| fecCorrectedCodewords | 5925 | 27478 |
| fecTotalCodewords | 9885929424 | 9884946112 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 1.103023e-10 | 5.119378e-10 |
| fecMaxSymbolErrorsBin0 | 9885923499 | 9884918634 |
| fecMaxSymbolErrorsBin1 | 5918 | 27427 |
| fecMaxSymbolErrorsBin2 | 7 | 51 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48272024792160 | 48279008400480 |
| l1BitsReceived | 48279008400480 | 48272024792160 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2220875 | 85897121934 |
| averageLatency | 2220881 | 182022783 |
| maxLatency | 2220886 | 85897121946 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |

# Port Statistics - 1280

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,3** | **Port 1,1,4** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.344381947 | 00:01:00.347656748 |
| framesSent | 4641875534 | 4642127442 |
| framesReceived | 4641875534 | 4642127442 |
| bitsSent | 47532805468160 | 47535385006080 |
| bitsReceived | 47535385006080 | 47532805468160 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 5997 | 28624 |
| fecMaxSymbolErrors | 2 | 2 |
| fecCorrectedCodewords | 5993 | 28586 |
| fecTotalCodewords | 9893591856 | 9893295632 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 1.114246e-10 | 5.318516e-10 |
| fecMaxSymbolErrorsBin0 | 9893585863 | 9893267046 |
| fecMaxSymbolErrorsBin1 | 5989 | 28548 |
| fecMaxSymbolErrorsBin2 | 4 | 38 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48275505553600 | 48278125396800 |
| l1BitsReceived | 48278125396800 | 48275505553600 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2220875 | 85897121934 |
| averageLatency | 2220881 | 97063058 |
| maxLatency | 2220886 | 85897121946 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |

# Port Statistics - 1518

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,3** | **Port 1,1,4** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.338556114 | 00:01:00.341630836 |
| framesSent | 3923183102 | 3923383019 |
| framesReceived | 3923183102 | 3923383019 |
| bitsSent | 47643135590688 | 47645563382736 |
| bitsReceived | 47645563382736 | 47643135590688 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 6070 | 28617 |
| fecMaxSymbolErrors | 2 | 2 |
| fecCorrectedCodewords | 6060 | 28571 |
| fecTotalCodewords | 9892781152 | 9892430320 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 1.127902e-10 | 5.317680e-10 |
| fecMaxSymbolErrorsBin0 | 9892775092 | 9892401749 |
| fecMaxSymbolErrorsBin1 | 6050 | 28525 |
| fecMaxSymbolErrorsBin2 | 10 | 46 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48270844887008 | 48273304665776 |
| l1BitsReceived | 48273304665776 | 48270844887008 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2220875 | 85897121934 |
| averageLatency | 2220881 | 290227843 |
| maxLatency | 2220886 | 85897121946 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |