

Keysight Test Report

**PASS**

|  |  |
| --- | --- |
| Report Header: | Keysight Inc. |
| Report Date: | 2025/04/23 10:24:06 |
| Type: | Framed |

# SUMMARY

|  |  |
| --- | --- |
| IxOS Version | 10.00.1000.17 |
| Test Start Time | 2025/04/23 10:11:57 |
| Test End Time | 2025/04/23 10:24:06 |
| Test Duration | 00:01:00 |
| Pass/Fail Verdict | PASS |
| Module Type | 1,1,3 - PassiveCopper 1,1,4 - PassiveCopper |
| Module Version | 1,1,3 - CMIS 5.0 1,1,4 - CMIS 5.0 |
| Serial Number | 1,1,3 - 57ADUAD233140Z 1,1,4 - 57ADUAD233140Z |

# Transceiver DOM (Digital Optical Monitoring) - 64

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **1,1,3** |  |  |  |  |  |  |  |  |  |  |
| **Manufactuer** | TE Connectivity | **Model** | 2369405-2 | **Mfg Revision** | 11 |  |  |  |  |  |
| **Type** | PassiveCopper | **SN** | 57ADUAD233140Z | **Firmware Revision** |  |  |  |  |  |  |
| **MSA** | CMIS 5.0 | **Date Code(YYMMDDLL)** | 230904 | **Hardware Revision** |  |  |  |  |  |  |
| **Media Tech** | Copper cable unequalized | **Media Connector** | No separable connector |  |  |  |  |  |  |  |
| **Cable Lenth** | 1.0 m | **Identifier Type** | OSFP |  |  |  |  |  |  |  |
| **Reported Power Class** | 1 | **Reported Max Power** | 0.250000 W |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Modlue** | **High Alarm** | **High Warn** | **Low Warn** | **Low Alarm** |  |  |  |  |  |  |
| **Temperature** | -0.001 C | -0.001 C | -0.001 C | -0.001 C |  |  |  |  |  |  |
| **Supply Voltage** | -0.000 V | -0.000 V | -0.000 V | -0.000 V |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Lane Limits** | **High Alarm** | **High Warn** | **Low Warn** | **Low Alarm** |  |  |  |  |  |  |
| **Tx Optical Power** | -40.00 dBm | -40.00 dBm | -40.00 dBm | -40.00 dBm |  |  |  |  |  |  |
| **Rx Optical Power** | -40.00 dBm | -40.00 dBm | -40.00 dBm | -40.00 dBm |  |  |  |  |  |  |
| **Tx Bias Current** | -40.00 dBm | -40.00 dBm | -40.00 dBm | -40.00 dBm |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Host Lane** | **Port** | **Data Path State** | **Tx LOS** | **Tx CDR LOL** | **Media Lane** | **Tx Optical Power** | **Tx Bias Cureent** | **Rx Optical Power** | **Rx LOS** | **Rx CDR LOL** |
| 1 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 2 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 3 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 4 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 5 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 6 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 7 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 8 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **1,1,4** |  |  |  |  |  |  |  |  |  |  |
| **Manufactuer** | TE Connectivity | **Model** | 2369405-2 | **Mfg Revision** | 11 |  |  |  |  |  |
| **Type** | PassiveCopper | **SN** | 57ADUAD233140Z | **Firmware Revision** |  |  |  |  |  |  |
| **MSA** | CMIS 5.0 | **Date Code(YYMMDDLL)** | 230904 | **Hardware Revision** |  |  |  |  |  |  |
| **Media Tech** | Copper cable unequalized | **Media Connector** | No separable connector |  |  |  |  |  |  |  |
| **Cable Lenth** | 1.0 m | **Identifier Type** | OSFP |  |  |  |  |  |  |  |
| **Reported Power Class** | 1 | **Reported Max Power** | 0.250000 W |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Modlue** | **High Alarm** | **High Warn** | **Low Warn** | **Low Alarm** |  |  |  |  |  |  |
| **Temperature** | -0.001 C | -0.001 C | -0.001 C | -0.001 C |  |  |  |  |  |  |
| **Supply Voltage** | -0.000 V | -0.000 V | -0.000 V | -0.000 V |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Lane Limits** | **High Alarm** | **High Warn** | **Low Warn** | **Low Alarm** |  |  |  |  |  |  |
| **Tx Optical Power** | -40.00 dBm | -40.00 dBm | -40.00 dBm | -40.00 dBm |  |  |  |  |  |  |
| **Rx Optical Power** | -40.00 dBm | -40.00 dBm | -40.00 dBm | -40.00 dBm |  |  |  |  |  |  |
| **Tx Bias Current** | -40.00 dBm | -40.00 dBm | -40.00 dBm | -40.00 dBm |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Host Lane** | **Port** | **Data Path State** | **Tx LOS** | **Tx CDR LOL** | **Media Lane** | **Tx Optical Power** | **Tx Bias Cureent** | **Rx Optical Power** | **Rx LOS** | **Rx CDR LOL** |
| 1 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 2 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 3 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 4 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 5 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 6 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 7 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 8 | 4.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |

# Transceiver DOM (Digital Optical Monitoring) - 128

No DOM data available.

# Transceiver DOM (Digital Optical Monitoring) - 256

No DOM data available.

# Transceiver DOM (Digital Optical Monitoring) - 512

No DOM data available.

# Transceiver DOM (Digital Optical Monitoring) - 1024

No DOM data available.

# Transceiver DOM (Digital Optical Monitoring) - 1280

No DOM data available.

# Transceiver DOM (Digital Optical Monitoring) - 1518

No DOM data available.

# CMIS Applicatio Select

|  |  |
| --- | --- |
| 1,1,3 |  |
| Current AppSel | 0 |

Available Applications

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **App** | **Host Side** | | | | | | **Line Side Media** | | | | |
|  | **Interface** | **Lane Speed (G bit/s)** | **Modulation** | **Lane Groups** | **Lanes** | **ID (HEex)** | **Interface** | **Lane Speed (G bit/s)** | **Lane Groups** | **Lanes** | **ID (Hex)** |
| 1 | 800GBASE-CR8 | 106 | PAM4 | 1 | 8 | 73 | PassiveCopper |  | 0 | 8 | 1 |
| 2 | 400GBASE-CR4 | 106 | PAM4 | 2 | 4 | 72 | PassiveCopper |  | 0 | 4 | 1 |
| 3 | 200GBASE-CR2 | 106 | PAM4 | 4 | 2 | 71 | PassiveCopper |  | 0 | 2 | 1 |
| 4 | 100GBASE-CR1 | 106 | PAM4 | 8 | 1 | 70 | PassiveCopper |  | 0 | 1 | 1 |
| 5 | 400GBASE-CR8 | 53 | PAM4 | 1 | 8 | 29 | PassiveCopper |  | 0 | 8 | 1 |
| 6 | 200GBASE-CR4 | 53 | PAM4 | 2 | 4 | 28 | PassiveCopper |  | 0 | 4 | 1 |
| 7 | 100GBASE-CR2 | 53 | PAM4 | 4 | 2 | 27 | PassiveCopper |  | 0 | 2 | 1 |
| 8 | 50GBASE-CR | 53 | PAM4 | 8 | 1 | 24 | PassiveCopper |  | 0 | 1 | 1 |

Preview of Auto selected applications

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Host Port Mode** | **Mod** | **Lane Groups** | **Lanes** | **AppSel** | **Link** | **Host Electrical** | **Lane Groups** | **Lanes** | **Note** |

|  |  |
| --- | --- |
| 1,1,4 |  |
| Current AppSel | 0 |

Available Applications

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **App** | **Host Side** | | | | | | **Line Side Media** | | | | |
|  | **Interface** | **Lane Speed (G bit/s)** | **Modulation** | **Lane Groups** | **Lanes** | **ID (HEex)** | **Interface** | **Lane Speed (G bit/s)** | **Lane Groups** | **Lanes** | **ID (Hex)** |
| 1 | 800GBASE-CR8 | 106 | PAM4 | 1 | 8 | 73 | PassiveCopper |  | 0 | 8 | 1 |
| 2 | 400GBASE-CR4 | 106 | PAM4 | 2 | 4 | 72 | PassiveCopper |  | 0 | 4 | 1 |
| 3 | 200GBASE-CR2 | 106 | PAM4 | 4 | 2 | 71 | PassiveCopper |  | 0 | 2 | 1 |
| 4 | 100GBASE-CR1 | 106 | PAM4 | 8 | 1 | 70 | PassiveCopper |  | 0 | 1 | 1 |
| 5 | 400GBASE-CR8 | 53 | PAM4 | 1 | 8 | 29 | PassiveCopper |  | 0 | 8 | 1 |
| 6 | 200GBASE-CR4 | 53 | PAM4 | 2 | 4 | 28 | PassiveCopper |  | 0 | 4 | 1 |
| 7 | 100GBASE-CR2 | 53 | PAM4 | 4 | 2 | 27 | PassiveCopper |  | 0 | 2 | 1 |
| 8 | 50GBASE-CR | 53 | PAM4 | 8 | 1 | 24 | PassiveCopper |  | 0 | 1 | 1 |

Preview of Auto selected applications

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Host Port Mode** | **Mod** | **Lane Groups** | **Lanes** | **AppSel** | **Link** | **Host Electrical** | **Lane Groups** | **Lanes** | **Note** |

# BERT Result Summary

No BERT Result Summary data available.

# BERT Statistics

No BERT Statistics data available.

# FEC Result Summary - 64

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,3** | **1,1,4** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# FEC Result Summary - 128

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,3** | **1,1,4** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# FEC Result Summary - 256

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,3** | **1,1,4** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# FEC Result Summary - 512

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,3** | **1,1,4** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# FEC Result Summary - 1024

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,3** | **1,1,4** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# FEC Result Summary - 1280

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,3** | **1,1,4** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# FEC Result Summary - 1518

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,3** | **1,1,4** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# PCS Lane Statistics - 64

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 3336.0 | 0.000000e+00 | 1.950831e-09 |
| **0** | Lock | 16 | 3.011 | 0 | 0 | 90 | 0.000000e+00 | 5.268297e-11 |
| **1** | Lock | 0 | 0.0 | 0 | 0 | 27 | 0.000000e+00 | 1.580489e-11 |
| **2** | Lock | 17 | 0.0 | 0 | 0 | 8 | 0.000000e+00 | 4.682930e-12 |
| **3** | Lock | 1 | 0.0 | 0 | 0 | 4 | 0.000000e+00 | 2.341465e-12 |
| **4** | Lock | 18 | 0.0 | 0 | 0 | 153 | 0.000000e+00 | 8.956105e-11 |
| **5** | Lock | 2 | 0.0 | 0 | 0 | 82 | 0.000000e+00 | 4.800004e-11 |
| **6** | Lock | 19 | 0.0 | 0 | 0 | 13 | 0.000000e+00 | 7.609762e-12 |
| **7** | Lock | 3 | 3.011 | 0 | 0 | 14 | 0.000000e+00 | 8.195128e-12 |
| **8** | Lock | 4 | 3.011 | 0 | 0 | 151 | 0.000000e+00 | 8.839031e-11 |
| **9** | Lock | 20 | 3.011 | 0 | 0 | 158 | 0.000000e+00 | 9.248788e-11 |
| **10** | Lock | 5 | 3.011 | 0 | 0 | 26 | 0.000000e+00 | 1.521952e-11 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 35 | 0.000000e+00 | 2.048782e-11 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 4 | 0.000000e+00 | 2.341465e-12 |
| **13** | Lock | 6 | 3.011 | 0 | 0 | 10 | 0.000000e+00 | 5.853663e-12 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 1 | 0.000000e+00 | 5.853663e-13 |
| **15** | Lock | 7 | 6.023 | 0 | 0 | 4 | 0.000000e+00 | 2.341465e-12 |
| **16** | Lock | 24 | 9.035 | 0 | 0 | 841 | 0.000000e+00 | 4.922931e-10 |
| **17** | Lock | 8 | 3.011 | 0 | 0 | 729 | 0.000000e+00 | 4.267320e-10 |
| **18** | Lock | 25 | 3.011 | 0 | 0 | 204 | 0.000000e+00 | 1.194147e-10 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 165 | 0.000000e+00 | 9.658544e-11 |
| **20** | Lock | 10 | 3.011 | 0 | 0 | 74 | 0.000000e+00 | 4.331711e-11 |
| **21** | Lock | 26 | 9.035 | 0 | 0 | 96 | 0.000000e+00 | 5.619517e-11 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 8 | 0.000000e+00 | 4.648017e-12 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 19 | 0.000000e+00 | 1.103904e-11 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 215 | 0.000000e+00 | 1.249155e-10 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 79 | 0.000000e+00 | 4.589917e-11 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 25 | 0.000000e+00 | 1.452505e-11 |
| **27** | Lock | 29 | 6.023 | 0 | 0 | 25 | 0.000000e+00 | 1.452505e-11 |
| **28** | Lock | 14 | 3.011 | 0 | 0 | 56 | 0.000000e+00 | 3.253612e-11 |
| **29** | Lock | 30 | 3.011 | 0 | 0 | 16 | 0.000000e+00 | 9.296035e-12 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 1.162004e-12 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 1.162004e-12 |

Port - 1,1,4

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 118580.0 | 0.000000e+00 | 6.832209e-08 |
| **0** | Lock | 0 | 0.0 | 0 | 0 | 87 | 0.000000e+00 | 5.054719e-11 |
| **1** | Lock | 16 | 0.0 | 0 | 0 | 49 | 0.000000e+00 | 2.846911e-11 |
| **2** | Lock | 1 | 6.023 | 0 | 0 | 2 | 0.000000e+00 | 1.162004e-12 |
| **3** | Lock | 17 | 3.011 | 0 | 0 | 3 | 0.000000e+00 | 1.743006e-12 |
| **4** | Lock | 18 | 9.035 | 0 | 0 | 216 | 0.000000e+00 | 1.254965e-10 |
| **5** | Lock | 2 | 6.023 | 0 | 0 | 110 | 0.000000e+00 | 6.391024e-11 |
| **6** | Lock | 19 | 6.023 | 0 | 0 | 13 | 0.000000e+00 | 7.491174e-12 |
| **7** | Lock | 3 | 6.023 | 0 | 0 | 3 | 0.000000e+00 | 1.728732e-12 |
| **8** | Lock | 20 | 3.011 | 0 | 0 | 315 | 0.000000e+00 | 1.815169e-10 |
| **9** | Lock | 4 | 3.011 | 0 | 0 | 634 | 0.000000e+00 | 3.653388e-10 |
| **10** | Lock | 21 | 6.023 | 0 | 0 | 58 | 0.000000e+00 | 3.342216e-11 |
| **11** | Lock | 5 | 9.035 | 0 | 0 | 129 | 0.000000e+00 | 7.433549e-11 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 21 | 0.000000e+00 | 1.210113e-11 |
| **13** | Lock | 22 | 6.023 | 0 | 0 | 55 | 0.000000e+00 | 3.169343e-11 |
| **14** | Lock | 7 | 6.023 | 0 | 0 | 4 | 0.000000e+00 | 2.304977e-12 |
| **15** | Lock | 23 | 3.011 | 0 | 0 | 6 | 0.000000e+00 | 3.457465e-12 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 39300 | 0.000000e+00 | 2.264639e-08 |
| **17** | Lock | 8 | 3.011 | 0 | 0 | 35526 | 0.000000e+00 | 2.047165e-08 |
| **18** | Lock | 25 | 6.023 | 0 | 0 | 10968 | 0.000000e+00 | 6.320246e-09 |
| **19** | Lock | 9 | 3.011 | 0 | 0 | 11154 | 0.000000e+00 | 6.427427e-09 |
| **20** | Lock | 10 | 0.0 | 0 | 0 | 6100 | 0.000000e+00 | 3.515089e-09 |
| **21** | Lock | 26 | 3.011 | 0 | 0 | 9086 | 0.000000e+00 | 5.235754e-09 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 834 | 0.000000e+00 | 4.805876e-10 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 1372 | 0.000000e+00 | 7.906069e-10 |
| **24** | Lock | 12 | 3.011 | 0 | 0 | 661 | 0.000000e+00 | 3.779865e-10 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 453 | 0.000000e+00 | 2.590437e-10 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 142 | 0.000000e+00 | 8.120135e-11 |
| **27** | Lock | 29 | 0.0 | 0 | 0 | 76 | 0.000000e+00 | 4.345988e-11 |
| **28** | Lock | 30 | 0.0 | 0 | 0 | 451 | 0.000000e+00 | 2.579001e-10 |
| **29** | Lock | 14 | 0.0 | 0 | 0 | 599 | 0.000000e+00 | 3.425324e-10 |
| **30** | Lock | 31 | 3.011 | 0 | 0 | 65 | 0.000000e+00 | 3.716963e-11 |
| **31** | Lock | 15 | 3.011 | 0 | 0 | 88 | 0.000000e+00 | 5.032196e-11 |

# PCS Lane Statistics - 128

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 3356.0 | 0.000000e+00 | 1.974822e-09 |
| **0** | Lock | 16 | 3.011 | 0 | 0 | 95 | 0.000000e+00 | 5.596886e-11 |
| **1** | Lock | 0 | 0.0 | 0 | 0 | 26 | 0.000000e+00 | 1.531779e-11 |
| **2** | Lock | 17 | 0.0 | 0 | 0 | 6 | 0.000000e+00 | 3.534875e-12 |
| **3** | Lock | 1 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 1.178292e-12 |
| **4** | Lock | 18 | 0.0 | 0 | 0 | 147 | 0.000000e+00 | 8.660444e-11 |
| **5** | Lock | 2 | 0.0 | 0 | 0 | 71 | 0.000000e+00 | 4.182935e-11 |
| **6** | Lock | 19 | 0.0 | 0 | 0 | 11 | 0.000000e+00 | 6.480604e-12 |
| **7** | Lock | 3 | 3.011 | 0 | 0 | 8 | 0.000000e+00 | 4.713167e-12 |
| **8** | Lock | 4 | 3.011 | 0 | 0 | 165 | 0.000000e+00 | 9.720906e-11 |
| **9** | Lock | 20 | 3.011 | 0 | 0 | 114 | 0.000000e+00 | 6.716263e-11 |
| **10** | Lock | 5 | 3.011 | 0 | 0 | 30 | 0.000000e+00 | 1.767438e-11 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 39 | 0.000000e+00 | 2.297669e-11 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 3 | 0.000000e+00 | 1.767438e-12 |
| **13** | Lock | 6 | 3.011 | 0 | 0 | 7 | 0.000000e+00 | 4.124021e-12 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 7 | 6.023 | 0 | 0 | 1 | 0.000000e+00 | 5.891458e-13 |
| **16** | Lock | 24 | 9.035 | 0 | 0 | 838 | 0.000000e+00 | 4.937042e-10 |
| **17** | Lock | 8 | 3.011 | 0 | 0 | 837 | 0.000000e+00 | 4.931151e-10 |
| **18** | Lock | 25 | 3.011 | 0 | 0 | 172 | 0.000000e+00 | 1.013331e-10 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 183 | 0.000000e+00 | 1.078137e-10 |
| **20** | Lock | 10 | 3.011 | 0 | 0 | 82 | 0.000000e+00 | 4.830996e-11 |
| **21** | Lock | 26 | 9.035 | 0 | 0 | 83 | 0.000000e+00 | 4.852298e-11 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 8 | 0.000000e+00 | 4.676914e-12 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 12 | 0.000000e+00 | 7.015370e-12 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 216 | 0.000000e+00 | 1.262767e-10 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 92 | 0.000000e+00 | 5.378451e-11 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 24 | 0.000000e+00 | 1.403074e-11 |
| **27** | Lock | 29 | 6.023 | 0 | 0 | 21 | 0.000000e+00 | 1.227690e-11 |
| **28** | Lock | 14 | 3.011 | 0 | 0 | 50 | 0.000000e+00 | 2.923071e-11 |
| **29** | Lock | 30 | 3.011 | 0 | 0 | 12 | 0.000000e+00 | 7.015370e-12 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.846142e-13 |

Port - 1,1,4

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 118498.0 | 0.000000e+00 | 6.874518e-08 |
| **0** | Lock | 0 | 0.0 | 0 | 0 | 81 | 0.000000e+00 | 4.735375e-11 |
| **1** | Lock | 16 | 0.0 | 0 | 0 | 58 | 0.000000e+00 | 3.365104e-11 |
| **2** | Lock | 1 | 6.023 | 0 | 0 | 4 | 0.000000e+00 | 2.320761e-12 |
| **3** | Lock | 17 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.801904e-13 |
| **4** | Lock | 18 | 9.035 | 0 | 0 | 223 | 0.000000e+00 | 1.293825e-10 |
| **5** | Lock | 2 | 6.023 | 0 | 0 | 95 | 0.000000e+00 | 5.511808e-11 |
| **6** | Lock | 19 | 6.023 | 0 | 0 | 18 | 0.000000e+00 | 1.044343e-11 |
| **7** | Lock | 3 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **8** | Lock | 20 | 3.011 | 0 | 0 | 328 | 0.000000e+00 | 1.903024e-10 |
| **9** | Lock | 4 | 3.011 | 0 | 0 | 624 | 0.000000e+00 | 3.620388e-10 |
| **10** | Lock | 21 | 6.023 | 0 | 0 | 58 | 0.000000e+00 | 3.365104e-11 |
| **11** | Lock | 5 | 9.035 | 0 | 0 | 137 | 0.000000e+00 | 7.948608e-11 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 26 | 0.000000e+00 | 1.508495e-11 |
| **13** | Lock | 22 | 6.023 | 0 | 0 | 69 | 0.000000e+00 | 4.003314e-11 |
| **14** | Lock | 7 | 6.023 | 0 | 0 | 3 | 0.000000e+00 | 1.740571e-12 |
| **15** | Lock | 23 | 3.011 | 0 | 0 | 4 | 0.000000e+00 | 2.320761e-12 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 39162 | 0.000000e+00 | 2.272142e-08 |
| **17** | Lock | 8 | 3.011 | 0 | 0 | 35990 | 0.000000e+00 | 2.088105e-08 |
| **18** | Lock | 25 | 6.023 | 0 | 0 | 10943 | 0.000000e+00 | 6.349023e-09 |
| **19** | Lock | 9 | 3.011 | 0 | 0 | 11272 | 0.000000e+00 | 6.539906e-09 |
| **20** | Lock | 10 | 0.0 | 0 | 0 | 6119 | 0.000000e+00 | 3.550185e-09 |
| **21** | Lock | 26 | 3.011 | 0 | 0 | 8874 | 0.000000e+00 | 5.148609e-09 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 853 | 0.000000e+00 | 4.949024e-10 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 1341 | 0.000000e+00 | 7.780353e-10 |
| **24** | Lock | 12 | 3.011 | 0 | 0 | 731 | 0.000000e+00 | 4.241192e-10 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 428 | 0.000000e+00 | 2.464243e-10 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 117 | 0.000000e+00 | 6.736366e-11 |
| **27** | Lock | 29 | 0.0 | 0 | 0 | 70 | 0.000000e+00 | 4.030305e-11 |
| **28** | Lock | 30 | 0.0 | 0 | 0 | 348 | 0.000000e+00 | 2.003637e-10 |
| **29** | Lock | 14 | 0.0 | 0 | 0 | 425 | 0.000000e+00 | 2.446971e-10 |
| **30** | Lock | 31 | 3.011 | 0 | 0 | 40 | 0.000000e+00 | 2.303031e-11 |
| **31** | Lock | 15 | 3.011 | 0 | 0 | 56 | 0.000000e+00 | 3.224244e-11 |

# PCS Lane Statistics - 256

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 3447.0 | 0.000000e+00 | 2.031387e-09 |
| **0** | Lock | 16 | 3.011 | 0 | 0 | 90 | 0.000000e+00 | 5.344287e-11 |
| **1** | Lock | 0 | 0.0 | 0 | 0 | 16 | 0.000000e+00 | 9.500955e-12 |
| **2** | Lock | 17 | 0.0 | 0 | 0 | 7 | 0.000000e+00 | 4.125212e-12 |
| **3** | Lock | 1 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.893161e-13 |
| **4** | Lock | 18 | 0.0 | 0 | 0 | 163 | 0.000000e+00 | 9.605852e-11 |
| **5** | Lock | 2 | 0.0 | 0 | 0 | 71 | 0.000000e+00 | 4.184144e-11 |
| **6** | Lock | 19 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 1.178632e-12 |
| **7** | Lock | 3 | 3.011 | 0 | 0 | 11 | 0.000000e+00 | 6.482477e-12 |
| **8** | Lock | 4 | 3.011 | 0 | 0 | 157 | 0.000000e+00 | 9.252262e-11 |
| **9** | Lock | 20 | 3.011 | 0 | 0 | 127 | 0.000000e+00 | 7.484314e-11 |
| **10** | Lock | 5 | 3.011 | 0 | 0 | 30 | 0.000000e+00 | 1.767948e-11 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 23 | 0.000000e+00 | 1.355427e-11 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 3 | 0.000000e+00 | 1.767948e-12 |
| **13** | Lock | 6 | 3.011 | 0 | 0 | 7 | 0.000000e+00 | 4.125212e-12 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 7 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **16** | Lock | 24 | 9.035 | 0 | 0 | 902 | 0.000000e+00 | 5.315631e-10 |
| **17** | Lock | 8 | 3.011 | 0 | 0 | 788 | 0.000000e+00 | 4.643810e-10 |
| **18** | Lock | 25 | 3.011 | 0 | 0 | 184 | 0.000000e+00 | 1.084342e-10 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 186 | 0.000000e+00 | 1.096128e-10 |
| **20** | Lock | 10 | 3.011 | 0 | 0 | 91 | 0.000000e+00 | 5.362776e-11 |
| **21** | Lock | 26 | 9.035 | 0 | 0 | 87 | 0.000000e+00 | 5.127050e-11 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 11 | 0.000000e+00 | 6.482477e-12 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 13 | 0.000000e+00 | 7.661109e-12 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 230 | 0.000000e+00 | 1.355427e-10 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 115 | 0.000000e+00 | 6.777135e-11 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 31 | 0.000000e+00 | 1.826880e-11 |
| **27** | Lock | 29 | 6.023 | 0 | 0 | 25 | 0.000000e+00 | 1.461865e-11 |
| **28** | Lock | 14 | 3.011 | 0 | 0 | 36 | 0.000000e+00 | 2.105086e-11 |
| **29** | Lock | 30 | 3.011 | 0 | 0 | 33 | 0.000000e+00 | 1.929662e-11 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 1.169492e-12 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 5 | 0.000000e+00 | 2.923731e-12 |

Port - 1,1,4

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 126607.0 | 0.000000e+00 | 7.338366e-08 |
| **0** | Lock | 0 | 0.0 | 0 | 0 | 105 | 0.000000e+00 | 6.139835e-11 |
| **1** | Lock | 16 | 0.0 | 0 | 0 | 38 | 0.000000e+00 | 2.222036e-11 |
| **2** | Lock | 1 | 6.023 | 0 | 0 | 4 | 0.000000e+00 | 2.338985e-12 |
| **3** | Lock | 17 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.847462e-13 |
| **4** | Lock | 18 | 9.035 | 0 | 0 | 222 | 0.000000e+00 | 1.298137e-10 |
| **5** | Lock | 2 | 6.023 | 0 | 0 | 100 | 0.000000e+00 | 5.847462e-11 |
| **6** | Lock | 19 | 6.023 | 0 | 0 | 16 | 0.000000e+00 | 9.355939e-12 |
| **7** | Lock | 3 | 6.023 | 0 | 0 | 4 | 0.000000e+00 | 2.338985e-12 |
| **8** | Lock | 20 | 3.011 | 0 | 0 | 333 | 0.000000e+00 | 1.947205e-10 |
| **9** | Lock | 4 | 3.011 | 0 | 0 | 561 | 0.000000e+00 | 3.280426e-10 |
| **10** | Lock | 21 | 6.023 | 0 | 0 | 75 | 0.000000e+00 | 4.347637e-11 |
| **11** | Lock | 5 | 9.035 | 0 | 0 | 154 | 0.000000e+00 | 8.927148e-11 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 29 | 0.000000e+00 | 1.681086e-11 |
| **13** | Lock | 22 | 6.023 | 0 | 0 | 58 | 0.000000e+00 | 3.362173e-11 |
| **14** | Lock | 7 | 6.023 | 0 | 0 | 8 | 0.000000e+00 | 4.637480e-12 |
| **15** | Lock | 23 | 3.011 | 0 | 0 | 6 | 0.000000e+00 | 3.478110e-12 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 40978 | 0.000000e+00 | 2.375433e-08 |
| **17** | Lock | 8 | 3.011 | 0 | 0 | 41097 | 0.000000e+00 | 2.382331e-08 |
| **18** | Lock | 25 | 6.023 | 0 | 0 | 13003 | 0.000000e+00 | 7.537643e-09 |
| **19** | Lock | 9 | 3.011 | 0 | 0 | 10542 | 0.000000e+00 | 6.111039e-09 |
| **20** | Lock | 10 | 0.0 | 0 | 0 | 6147 | 0.000000e+00 | 3.563323e-09 |
| **21** | Lock | 26 | 3.011 | 0 | 0 | 8767 | 0.000000e+00 | 5.082098e-09 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 837 | 0.000000e+00 | 4.851963e-10 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 1363 | 0.000000e+00 | 7.841049e-10 |
| **24** | Lock | 12 | 3.011 | 0 | 0 | 760 | 0.000000e+00 | 4.372119e-10 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 393 | 0.000000e+00 | 2.260846e-10 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 116 | 0.000000e+00 | 6.673234e-11 |
| **27** | Lock | 29 | 0.0 | 0 | 0 | 60 | 0.000000e+00 | 3.451673e-11 |
| **28** | Lock | 30 | 0.0 | 0 | 0 | 313 | 0.000000e+00 | 1.800623e-10 |
| **29** | Lock | 14 | 0.0 | 0 | 0 | 426 | 0.000000e+00 | 2.450688e-10 |
| **30** | Lock | 31 | 3.011 | 0 | 0 | 41 | 0.000000e+00 | 2.358643e-11 |
| **31** | Lock | 15 | 3.011 | 0 | 0 | 50 | 0.000000e+00 | 2.876394e-11 |

# PCS Lane Statistics - 512

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 3479.0 | 0.000000e+00 | 2.022725e-09 |
| **0** | Lock | 16 | 3.011 | 0 | 0 | 86 | 0.000000e+00 | 5.074328e-11 |
| **1** | Lock | 0 | 0.0 | 0 | 0 | 20 | 0.000000e+00 | 1.170663e-11 |
| **2** | Lock | 17 | 0.0 | 0 | 0 | 6 | 0.000000e+00 | 3.511990e-12 |
| **3** | Lock | 1 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 1.170663e-12 |
| **4** | Lock | 18 | 0.0 | 0 | 0 | 182 | 0.000000e+00 | 1.065304e-10 |
| **5** | Lock | 2 | 0.0 | 0 | 0 | 65 | 0.000000e+00 | 3.804656e-11 |
| **6** | Lock | 19 | 0.0 | 0 | 0 | 4 | 0.000000e+00 | 2.341327e-12 |
| **7** | Lock | 3 | 3.011 | 0 | 0 | 13 | 0.000000e+00 | 7.609311e-12 |
| **8** | Lock | 4 | 3.011 | 0 | 0 | 165 | 0.000000e+00 | 9.583316e-11 |
| **9** | Lock | 20 | 3.011 | 0 | 0 | 123 | 0.000000e+00 | 7.143926e-11 |
| **10** | Lock | 5 | 3.011 | 0 | 0 | 30 | 0.000000e+00 | 1.742421e-11 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 48 | 0.000000e+00 | 2.787874e-11 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 11 | 0.000000e+00 | 6.388877e-12 |
| **13** | Lock | 6 | 3.011 | 0 | 0 | 6 | 0.000000e+00 | 3.484842e-12 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 7 | 6.023 | 0 | 0 | 1 | 0.000000e+00 | 5.808070e-13 |
| **16** | Lock | 24 | 9.035 | 0 | 0 | 842 | 0.000000e+00 | 4.890395e-10 |
| **17** | Lock | 8 | 3.011 | 0 | 0 | 840 | 0.000000e+00 | 4.878779e-10 |
| **18** | Lock | 25 | 3.011 | 0 | 0 | 208 | 0.000000e+00 | 1.208079e-10 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 186 | 0.000000e+00 | 1.080301e-10 |
| **20** | Lock | 10 | 3.011 | 0 | 0 | 69 | 0.000000e+00 | 4.007568e-11 |
| **21** | Lock | 26 | 9.035 | 0 | 0 | 115 | 0.000000e+00 | 6.679281e-11 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 4 | 0.000000e+00 | 2.323228e-12 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 14 | 0.000000e+00 | 8.131298e-12 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 220 | 0.000000e+00 | 1.277775e-10 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 83 | 0.000000e+00 | 4.820698e-11 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 33 | 0.000000e+00 | 1.916663e-11 |
| **27** | Lock | 29 | 6.023 | 0 | 0 | 25 | 0.000000e+00 | 1.452018e-11 |
| **28** | Lock | 14 | 3.011 | 0 | 0 | 39 | 0.000000e+00 | 2.265147e-11 |
| **29** | Lock | 30 | 3.011 | 0 | 0 | 32 | 0.000000e+00 | 1.858582e-11 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 3 | 0.000000e+00 | 1.742421e-12 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 4 | 0.000000e+00 | 2.305910e-12 |

Port - 1,1,4

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 127897.0 | 0.000000e+00 | 7.314154e-08 |
| **0** | Lock | 0 | 0.0 | 0 | 0 | 103 | 0.000000e+00 | 5.937718e-11 |
| **1** | Lock | 16 | 0.0 | 0 | 0 | 52 | 0.000000e+00 | 2.997683e-11 |
| **2** | Lock | 1 | 6.023 | 0 | 0 | 1 | 0.000000e+00 | 5.764775e-13 |
| **3** | Lock | 17 | 3.011 | 0 | 0 | 4 | 0.000000e+00 | 2.305910e-12 |
| **4** | Lock | 18 | 9.035 | 0 | 0 | 245 | 0.000000e+00 | 1.412370e-10 |
| **5** | Lock | 2 | 6.023 | 0 | 0 | 120 | 0.000000e+00 | 6.917730e-11 |
| **6** | Lock | 19 | 6.023 | 0 | 0 | 12 | 0.000000e+00 | 6.917730e-12 |
| **7** | Lock | 3 | 6.023 | 0 | 0 | 5 | 0.000000e+00 | 2.882387e-12 |
| **8** | Lock | 20 | 3.011 | 0 | 0 | 328 | 0.000000e+00 | 1.890846e-10 |
| **9** | Lock | 4 | 3.011 | 0 | 0 | 659 | 0.000000e+00 | 3.798987e-10 |
| **10** | Lock | 21 | 6.023 | 0 | 0 | 69 | 0.000000e+00 | 3.977695e-11 |
| **11** | Lock | 5 | 9.035 | 0 | 0 | 147 | 0.000000e+00 | 8.474219e-11 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 25 | 0.000000e+00 | 1.441194e-11 |
| **13** | Lock | 22 | 6.023 | 0 | 0 | 65 | 0.000000e+00 | 3.747104e-11 |
| **14** | Lock | 7 | 6.023 | 0 | 0 | 3 | 0.000000e+00 | 1.729432e-12 |
| **15** | Lock | 23 | 3.011 | 0 | 0 | 2 | 0.000000e+00 | 1.152955e-12 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 41296 | 0.000000e+00 | 2.361352e-08 |
| **17** | Lock | 8 | 3.011 | 0 | 0 | 41260 | 0.000000e+00 | 2.359293e-08 |
| **18** | Lock | 25 | 6.023 | 0 | 0 | 12775 | 0.000000e+00 | 7.304890e-09 |
| **19** | Lock | 9 | 3.011 | 0 | 0 | 10662 | 0.000000e+00 | 6.096652e-09 |
| **20** | Lock | 10 | 0.0 | 0 | 0 | 6291 | 0.000000e+00 | 3.597265e-09 |
| **21** | Lock | 26 | 3.011 | 0 | 0 | 9330 | 0.000000e+00 | 5.335000e-09 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 904 | 0.000000e+00 | 5.169174e-10 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 1333 | 0.000000e+00 | 7.622245e-10 |
| **24** | Lock | 12 | 3.011 | 0 | 0 | 716 | 0.000000e+00 | 4.094169e-10 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 398 | 0.000000e+00 | 2.275809e-10 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 144 | 0.000000e+00 | 8.234083e-11 |
| **27** | Lock | 29 | 0.0 | 0 | 0 | 54 | 0.000000e+00 | 3.087781e-11 |
| **28** | Lock | 30 | 0.0 | 0 | 0 | 366 | 0.000000e+00 | 2.092829e-10 |
| **29** | Lock | 14 | 0.0 | 0 | 0 | 443 | 0.000000e+00 | 2.533124e-10 |
| **30** | Lock | 31 | 3.011 | 0 | 0 | 38 | 0.000000e+00 | 2.172883e-11 |
| **31** | Lock | 15 | 3.011 | 0 | 0 | 47 | 0.000000e+00 | 2.687513e-11 |

# PCS Lane Statistics - 1024

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 3481.0 | 0.000000e+00 | 2.008715e-09 |
| **0** | Lock | 16 | 3.011 | 0 | 0 | 95 | 0.000000e+00 | 5.513703e-11 |
| **1** | Lock | 0 | 0.0 | 0 | 0 | 32 | 0.000000e+00 | 1.857247e-11 |
| **2** | Lock | 17 | 0.0 | 0 | 0 | 6 | 0.000000e+00 | 3.482339e-12 |
| **3** | Lock | 1 | 0.0 | 0 | 0 | 7 | 0.000000e+00 | 4.062729e-12 |
| **4** | Lock | 18 | 0.0 | 0 | 0 | 161 | 0.000000e+00 | 9.344276e-11 |
| **5** | Lock | 2 | 0.0 | 0 | 0 | 77 | 0.000000e+00 | 4.469001e-11 |
| **6** | Lock | 19 | 0.0 | 0 | 0 | 5 | 0.000000e+00 | 2.901949e-12 |
| **7** | Lock | 3 | 3.011 | 0 | 0 | 16 | 0.000000e+00 | 9.286237e-12 |
| **8** | Lock | 4 | 3.011 | 0 | 0 | 157 | 0.000000e+00 | 9.112120e-11 |
| **9** | Lock | 20 | 3.011 | 0 | 0 | 152 | 0.000000e+00 | 8.821925e-11 |
| **10** | Lock | 5 | 3.011 | 0 | 0 | 21 | 0.000000e+00 | 1.218819e-11 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 38 | 0.000000e+00 | 2.205481e-11 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 2 | 0.000000e+00 | 1.160780e-12 |
| **13** | Lock | 6 | 3.011 | 0 | 0 | 5 | 0.000000e+00 | 2.901949e-12 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 7 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **16** | Lock | 24 | 9.035 | 0 | 0 | 858 | 0.000000e+00 | 4.942952e-10 |
| **17** | Lock | 8 | 3.011 | 0 | 0 | 836 | 0.000000e+00 | 4.816209e-10 |
| **18** | Lock | 25 | 3.011 | 0 | 0 | 180 | 0.000000e+00 | 1.036983e-10 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 181 | 0.000000e+00 | 1.042744e-10 |
| **20** | Lock | 10 | 3.011 | 0 | 0 | 65 | 0.000000e+00 | 3.744660e-11 |
| **21** | Lock | 26 | 9.035 | 0 | 0 | 114 | 0.000000e+00 | 6.567558e-11 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 7 | 0.000000e+00 | 4.032711e-12 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 9 | 0.000000e+00 | 5.184914e-12 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 209 | 0.000000e+00 | 1.204052e-10 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 94 | 0.000000e+00 | 5.415355e-11 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 43 | 0.000000e+00 | 2.477237e-11 |
| **27** | Lock | 29 | 6.023 | 0 | 0 | 33 | 0.000000e+00 | 1.901135e-11 |
| **28** | Lock | 14 | 3.011 | 0 | 0 | 43 | 0.000000e+00 | 2.477237e-11 |
| **29** | Lock | 30 | 3.011 | 0 | 0 | 27 | 0.000000e+00 | 1.555474e-11 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 5 | 0.000000e+00 | 2.880508e-12 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 3 | 0.000000e+00 | 1.714422e-12 |

Port - 1,1,4

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 128381.0 | 0.000000e+00 | 7.279685e-08 |
| **0** | Lock | 0 | 0.0 | 0 | 0 | 121 | 0.000000e+00 | 6.914836e-11 |
| **1** | Lock | 16 | 0.0 | 0 | 0 | 45 | 0.000000e+00 | 2.571633e-11 |
| **2** | Lock | 1 | 6.023 | 0 | 0 | 4 | 0.000000e+00 | 2.285896e-12 |
| **3** | Lock | 17 | 3.011 | 0 | 0 | 2 | 0.000000e+00 | 1.142948e-12 |
| **4** | Lock | 18 | 9.035 | 0 | 0 | 237 | 0.000000e+00 | 1.354394e-10 |
| **5** | Lock | 2 | 6.023 | 0 | 0 | 128 | 0.000000e+00 | 7.314868e-11 |
| **6** | Lock | 19 | 6.023 | 0 | 0 | 14 | 0.000000e+00 | 8.000637e-12 |
| **7** | Lock | 3 | 6.023 | 0 | 0 | 5 | 0.000000e+00 | 2.857370e-12 |
| **8** | Lock | 20 | 3.011 | 0 | 0 | 292 | 0.000000e+00 | 1.668704e-10 |
| **9** | Lock | 4 | 3.011 | 0 | 0 | 649 | 0.000000e+00 | 3.708867e-10 |
| **10** | Lock | 21 | 6.023 | 0 | 0 | 75 | 0.000000e+00 | 4.286056e-11 |
| **11** | Lock | 5 | 9.035 | 0 | 0 | 154 | 0.000000e+00 | 8.800701e-11 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 29 | 0.000000e+00 | 1.657275e-11 |
| **13** | Lock | 22 | 6.023 | 0 | 0 | 55 | 0.000000e+00 | 3.143107e-11 |
| **14** | Lock | 7 | 6.023 | 0 | 0 | 2 | 0.000000e+00 | 1.142948e-12 |
| **15** | Lock | 23 | 3.011 | 0 | 0 | 6 | 0.000000e+00 | 3.401962e-12 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 41214 | 0.000000e+00 | 2.336808e-08 |
| **17** | Lock | 8 | 3.011 | 0 | 0 | 41892 | 0.000000e+00 | 2.375250e-08 |
| **18** | Lock | 25 | 6.023 | 0 | 0 | 12783 | 0.000000e+00 | 7.247880e-09 |
| **19** | Lock | 9 | 3.011 | 0 | 0 | 10762 | 0.000000e+00 | 6.101986e-09 |
| **20** | Lock | 10 | 0.0 | 0 | 0 | 6233 | 0.000000e+00 | 3.534072e-09 |
| **21** | Lock | 26 | 3.011 | 0 | 0 | 9117 | 0.000000e+00 | 5.169281e-09 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 874 | 0.000000e+00 | 4.955525e-10 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 1410 | 0.000000e+00 | 7.994611e-10 |
| **24** | Lock | 12 | 3.011 | 0 | 0 | 774 | 0.000000e+00 | 4.388531e-10 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 381 | 0.000000e+00 | 2.160246e-10 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 132 | 0.000000e+00 | 7.484316e-11 |
| **27** | Lock | 29 | 0.0 | 0 | 0 | 60 | 0.000000e+00 | 3.401962e-11 |
| **28** | Lock | 30 | 0.0 | 0 | 0 | 365 | 0.000000e+00 | 2.069527e-10 |
| **29** | Lock | 14 | 0.0 | 0 | 0 | 455 | 0.000000e+00 | 2.559863e-10 |
| **30** | Lock | 31 | 3.011 | 0 | 0 | 47 | 0.000000e+00 | 2.644255e-11 |
| **31** | Lock | 15 | 3.011 | 0 | 0 | 64 | 0.000000e+00 | 3.600687e-11 |

# PCS Lane Statistics - 1280

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 3483.0 | 0.000000e+00 | 2.008726e-09 |
| **0** | Lock | 16 | 3.011 | 0 | 0 | 78 | 0.000000e+00 | 4.529278e-11 |
| **1** | Lock | 0 | 0.0 | 0 | 0 | 14 | 0.000000e+00 | 8.129474e-12 |
| **2** | Lock | 17 | 0.0 | 0 | 0 | 4 | 0.000000e+00 | 2.322707e-12 |
| **3** | Lock | 1 | 0.0 | 0 | 0 | 3 | 0.000000e+00 | 1.742030e-12 |
| **4** | Lock | 18 | 0.0 | 0 | 0 | 148 | 0.000000e+00 | 8.594015e-11 |
| **5** | Lock | 2 | 0.0 | 0 | 0 | 65 | 0.000000e+00 | 3.774399e-11 |
| **6** | Lock | 19 | 0.0 | 0 | 0 | 5 | 0.000000e+00 | 2.903384e-12 |
| **7** | Lock | 3 | 3.011 | 0 | 0 | 7 | 0.000000e+00 | 4.064737e-12 |
| **8** | Lock | 4 | 3.011 | 0 | 0 | 171 | 0.000000e+00 | 9.929572e-11 |
| **9** | Lock | 20 | 3.011 | 0 | 0 | 127 | 0.000000e+00 | 7.374594e-11 |
| **10** | Lock | 5 | 3.011 | 0 | 0 | 24 | 0.000000e+00 | 1.393624e-11 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 42 | 0.000000e+00 | 2.419456e-11 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **13** | Lock | 6 | 3.011 | 0 | 0 | 11 | 0.000000e+00 | 6.336670e-12 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 1 | 0.000000e+00 | 5.760609e-13 |
| **15** | Lock | 7 | 6.023 | 0 | 0 | 2 | 0.000000e+00 | 1.152122e-12 |
| **16** | Lock | 24 | 9.035 | 0 | 0 | 862 | 0.000000e+00 | 4.965645e-10 |
| **17** | Lock | 8 | 3.011 | 0 | 0 | 853 | 0.000000e+00 | 4.913800e-10 |
| **18** | Lock | 25 | 3.011 | 0 | 0 | 195 | 0.000000e+00 | 1.123319e-10 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 203 | 0.000000e+00 | 1.169404e-10 |
| **20** | Lock | 10 | 3.011 | 0 | 0 | 70 | 0.000000e+00 | 4.032427e-11 |
| **21** | Lock | 26 | 9.035 | 0 | 0 | 79 | 0.000000e+00 | 4.550881e-11 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 6 | 0.000000e+00 | 3.456366e-12 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 18 | 0.000000e+00 | 1.036910e-11 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 247 | 0.000000e+00 | 1.422870e-10 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 95 | 0.000000e+00 | 5.472579e-11 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 45 | 0.000000e+00 | 2.572383e-11 |
| **27** | Lock | 29 | 6.023 | 0 | 0 | 32 | 0.000000e+00 | 1.829250e-11 |
| **28** | Lock | 14 | 3.011 | 0 | 0 | 38 | 0.000000e+00 | 2.172235e-11 |
| **29** | Lock | 30 | 3.011 | 0 | 0 | 30 | 0.000000e+00 | 1.714922e-11 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 4 | 0.000000e+00 | 2.286563e-12 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 4 | 0.000000e+00 | 2.286563e-12 |

Port - 1,1,4

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 129088.0 | 0.000000e+00 | 7.322612e-08 |
| **0** | Lock | 0 | 0.0 | 0 | 0 | 89 | 0.000000e+00 | 5.087602e-11 |
| **1** | Lock | 16 | 0.0 | 0 | 0 | 47 | 0.000000e+00 | 2.686711e-11 |
| **2** | Lock | 1 | 6.023 | 0 | 0 | 4 | 0.000000e+00 | 2.286563e-12 |
| **3** | Lock | 17 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.716407e-13 |
| **4** | Lock | 18 | 9.035 | 0 | 0 | 222 | 0.000000e+00 | 1.269042e-10 |
| **5** | Lock | 2 | 6.023 | 0 | 0 | 111 | 0.000000e+00 | 6.345212e-11 |
| **6** | Lock | 19 | 6.023 | 0 | 0 | 14 | 0.000000e+00 | 7.943036e-12 |
| **7** | Lock | 3 | 6.023 | 0 | 0 | 8 | 0.000000e+00 | 4.538878e-12 |
| **8** | Lock | 20 | 3.011 | 0 | 0 | 345 | 0.000000e+00 | 1.957391e-10 |
| **9** | Lock | 4 | 3.011 | 0 | 0 | 684 | 0.000000e+00 | 3.880740e-10 |
| **10** | Lock | 21 | 6.023 | 0 | 0 | 53 | 0.000000e+00 | 3.007007e-11 |
| **11** | Lock | 5 | 9.035 | 0 | 0 | 132 | 0.000000e+00 | 7.489148e-11 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 24 | 0.000000e+00 | 1.361663e-11 |
| **13** | Lock | 22 | 6.023 | 0 | 0 | 50 | 0.000000e+00 | 2.836799e-11 |
| **14** | Lock | 7 | 6.023 | 0 | 0 | 7 | 0.000000e+00 | 3.971518e-12 |
| **15** | Lock | 23 | 3.011 | 0 | 0 | 3 | 0.000000e+00 | 1.702079e-12 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 41560 | 0.000000e+00 | 2.357947e-08 |
| **17** | Lock | 8 | 3.011 | 0 | 0 | 41633 | 0.000000e+00 | 2.362089e-08 |
| **18** | Lock | 25 | 6.023 | 0 | 0 | 13236 | 0.000000e+00 | 7.509573e-09 |
| **19** | Lock | 9 | 3.011 | 0 | 0 | 10537 | 0.000000e+00 | 5.978269e-09 |
| **20** | Lock | 10 | 0.0 | 0 | 0 | 6333 | 0.000000e+00 | 3.593089e-09 |
| **21** | Lock | 26 | 3.011 | 0 | 0 | 9385 | 0.000000e+00 | 5.324671e-09 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 881 | 0.000000e+00 | 4.998439e-10 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 1495 | 0.000000e+00 | 8.420926e-10 |
| **24** | Lock | 12 | 3.011 | 0 | 0 | 727 | 0.000000e+00 | 4.094992e-10 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 435 | 0.000000e+00 | 2.450236e-10 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 132 | 0.000000e+00 | 7.435199e-11 |
| **27** | Lock | 29 | 0.0 | 0 | 0 | 70 | 0.000000e+00 | 3.942908e-11 |
| **28** | Lock | 30 | 0.0 | 0 | 0 | 332 | 0.000000e+00 | 1.870065e-10 |
| **29** | Lock | 14 | 0.0 | 0 | 0 | 450 | 0.000000e+00 | 2.534727e-10 |
| **30** | Lock | 31 | 3.011 | 0 | 0 | 35 | 0.000000e+00 | 1.971454e-11 |
| **31** | Lock | 15 | 3.011 | 0 | 0 | 53 | 0.000000e+00 | 2.985345e-11 |

# PCS Lane Statistics - 1518

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 3370.0 | 0.000000e+00 | 1.963353e-09 |
| **0** | Lock | 16 | 3.011 | 0 | 0 | 93 | 0.000000e+00 | 5.456319e-11 |
| **1** | Lock | 0 | 0.0 | 0 | 0 | 22 | 0.000000e+00 | 1.290742e-11 |
| **2** | Lock | 17 | 0.0 | 0 | 0 | 9 | 0.000000e+00 | 5.280308e-12 |
| **3** | Lock | 1 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 1.173402e-12 |
| **4** | Lock | 18 | 0.0 | 0 | 0 | 123 | 0.000000e+00 | 7.216421e-11 |
| **5** | Lock | 2 | 0.0 | 0 | 0 | 84 | 0.000000e+00 | 4.928288e-11 |
| **6** | Lock | 19 | 0.0 | 0 | 0 | 5 | 0.000000e+00 | 2.933505e-12 |
| **7** | Lock | 3 | 3.011 | 0 | 0 | 13 | 0.000000e+00 | 7.627112e-12 |
| **8** | Lock | 4 | 3.011 | 0 | 0 | 139 | 0.000000e+00 | 8.155143e-11 |
| **9** | Lock | 20 | 3.011 | 0 | 0 | 136 | 0.000000e+00 | 7.979132e-11 |
| **10** | Lock | 5 | 3.011 | 0 | 0 | 33 | 0.000000e+00 | 1.936113e-11 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 35 | 0.000000e+00 | 2.053453e-11 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 2 | 0.000000e+00 | 1.164541e-12 |
| **13** | Lock | 6 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.822703e-13 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 1 | 0.000000e+00 | 5.822703e-13 |
| **15** | Lock | 7 | 6.023 | 0 | 0 | 2 | 0.000000e+00 | 1.164541e-12 |
| **16** | Lock | 24 | 9.035 | 0 | 0 | 842 | 0.000000e+00 | 4.902716e-10 |
| **17** | Lock | 8 | 3.011 | 0 | 0 | 847 | 0.000000e+00 | 4.931830e-10 |
| **18** | Lock | 25 | 3.011 | 0 | 0 | 184 | 0.000000e+00 | 1.071377e-10 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 194 | 0.000000e+00 | 1.129604e-10 |
| **20** | Lock | 10 | 3.011 | 0 | 0 | 70 | 0.000000e+00 | 4.075892e-11 |
| **21** | Lock | 26 | 9.035 | 0 | 0 | 74 | 0.000000e+00 | 4.308800e-11 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 9 | 0.000000e+00 | 5.240433e-12 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 12 | 0.000000e+00 | 6.934630e-12 |
| **24** | Lock | 12 | 0.0 | 0 | 0 | 229 | 0.000000e+00 | 1.323358e-10 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 98 | 0.000000e+00 | 5.663281e-11 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 27 | 0.000000e+00 | 1.560292e-11 |
| **27** | Lock | 29 | 6.023 | 0 | 0 | 27 | 0.000000e+00 | 1.560292e-11 |
| **28** | Lock | 14 | 3.011 | 0 | 0 | 34 | 0.000000e+00 | 1.964812e-11 |
| **29** | Lock | 30 | 3.011 | 0 | 0 | 17 | 0.000000e+00 | 9.824059e-12 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 3 | 0.000000e+00 | 1.733657e-12 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 3 | 0.000000e+00 | 1.733657e-12 |

Port - 1,1,4

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 127518.0 | 0.000000e+00 | 7.305966e-08 |
| **0** | Lock | 0 | 0.0 | 0 | 0 | 116 | 0.000000e+00 | 6.703475e-11 |
| **1** | Lock | 16 | 0.0 | 0 | 0 | 48 | 0.000000e+00 | 2.773852e-11 |
| **2** | Lock | 1 | 6.023 | 0 | 0 | 3 | 0.000000e+00 | 1.733657e-12 |
| **3** | Lock | 17 | 3.011 | 0 | 0 | 3 | 0.000000e+00 | 1.733657e-12 |
| **4** | Lock | 18 | 9.035 | 0 | 0 | 241 | 0.000000e+00 | 1.392705e-10 |
| **5** | Lock | 2 | 6.023 | 0 | 0 | 88 | 0.000000e+00 | 5.085395e-11 |
| **6** | Lock | 19 | 6.023 | 0 | 0 | 15 | 0.000000e+00 | 8.668287e-12 |
| **7** | Lock | 3 | 6.023 | 0 | 0 | 5 | 0.000000e+00 | 2.889429e-12 |
| **8** | Lock | 20 | 3.011 | 0 | 0 | 327 | 0.000000e+00 | 1.889687e-10 |
| **9** | Lock | 4 | 3.011 | 0 | 0 | 691 | 0.000000e+00 | 3.993191e-10 |
| **10** | Lock | 21 | 6.023 | 0 | 0 | 69 | 0.000000e+00 | 3.987412e-11 |
| **11** | Lock | 5 | 9.035 | 0 | 0 | 136 | 0.000000e+00 | 7.859247e-11 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 30 | 0.000000e+00 | 1.733657e-11 |
| **13** | Lock | 22 | 6.023 | 0 | 0 | 55 | 0.000000e+00 | 3.178372e-11 |
| **14** | Lock | 7 | 6.023 | 0 | 0 | 2 | 0.000000e+00 | 1.155772e-12 |
| **15** | Lock | 23 | 3.011 | 0 | 0 | 4 | 0.000000e+00 | 2.293339e-12 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 41050 | 0.000000e+00 | 2.353539e-08 |
| **17** | Lock | 8 | 3.011 | 0 | 0 | 41202 | 0.000000e+00 | 2.362254e-08 |
| **18** | Lock | 25 | 6.023 | 0 | 0 | 13073 | 0.000000e+00 | 7.495205e-09 |
| **19** | Lock | 9 | 3.011 | 0 | 0 | 10524 | 0.000000e+00 | 6.033775e-09 |
| **20** | Lock | 10 | 0.0 | 0 | 0 | 6281 | 0.000000e+00 | 3.601116e-09 |
| **21** | Lock | 26 | 3.011 | 0 | 0 | 9153 | 0.000000e+00 | 5.207784e-09 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 853 | 0.000000e+00 | 4.853316e-10 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 1355 | 0.000000e+00 | 7.709547e-10 |
| **24** | Lock | 12 | 3.011 | 0 | 0 | 752 | 0.000000e+00 | 4.278656e-10 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 401 | 0.000000e+00 | 2.281571e-10 |
| **26** | Lock | 13 | 0.0 | 0 | 0 | 123 | 0.000000e+00 | 6.998334e-11 |
| **27** | Lock | 29 | 0.0 | 0 | 0 | 62 | 0.000000e+00 | 3.527615e-11 |
| **28** | Lock | 30 | 0.0 | 0 | 0 | 352 | 0.000000e+00 | 2.002775e-10 |
| **29** | Lock | 14 | 0.0 | 0 | 0 | 415 | 0.000000e+00 | 2.361226e-10 |
| **30** | Lock | 31 | 3.011 | 0 | 0 | 34 | 0.000000e+00 | 1.934499e-11 |
| **31** | Lock | 15 | 3.011 | 0 | 0 | 55 | 0.000000e+00 | 3.129336e-11 |

# L2 Traffic Test Summary

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Frame Size** | **Tx Count** | **Rx Count** | **Loss Count** | **Loss %** |
| **64** | 143335260336 | 143335260336 | 0 | 0.0 |
| **128** | 81173154569 | 81173154569 | 0 | 0.0 |
| **256** | 43539727698 | 43539727698 | 0 | 0.0 |
| **512** | 22658428354 | 22658428354 | 0 | 0.0 |
| **1024** | 11563724979 | 11563724979 | 0 | 0.0 |
| **1280** | 9293850177 | 9293850177 | 0 | 0.0 |
| **1518** | 7846404233 | 7846404233 | 0 | 0.0 |

# Port Statistics - 64

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,3** | **Port 1,1,4** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.199525382 | 00:01:00.202093305 |
| framesSent | 71666101644 | 71669158692 |
| framesReceived | 71666101644 | 71669158692 |
| bitsSent | 36693044041728 | 36694609250304 |
| bitsReceived | 36694609250304 | 36693044041728 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 3287 | 113349 |
| fecMaxSymbolErrors | 1 | 2 |
| fecCorrectedCodewords | 3287 | 112765 |
| fecTotalCodewords | 9865232928 | 9864170960 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 6.124822e-11 | 2.112313e-09 |
| fecMaxSymbolErrorsBin0 | 9865229641 | 9864058195 |
| fecMaxSymbolErrorsBin1 | 3287 | 112181 |
| fecMaxSymbolErrorsBin2 | 0 | 584 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48159620304768 | 48161674641024 |
| l1BitsReceived | 48161674641024 | 48159620304768 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2224364 | 85897118441 |
| averageLatency | 2224370 | 7381181 |
| maxLatency | 2224376 | 85897118453 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |

# Port Statistics - 128

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,3** | **Port 1,1,4** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.066841304 | 00:01:00.069427624 |
| framesSent | 40585703528 | 40587451041 |
| framesReceived | 40585703528 | 40587451041 |
| bitsSent | 41559760412672 | 41561549865984 |
| bitsReceived | 41561549865984 | 41559760412672 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 3315 | 113853 |
| fecMaxSymbolErrors | 1 | 2 |
| fecCorrectedCodewords | 3315 | 113209 |
| fecTotalCodewords | 9851243216 | 9850588592 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 6.185767e-11 | 2.124630e-09 |
| fecMaxSymbolErrorsBin0 | 9851239901 | 9850475383 |
| fecMaxSymbolErrorsBin1 | 3315 | 112565 |
| fecMaxSymbolErrorsBin2 | 0 | 644 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48053472977152 | 48055542032544 |
| l1BitsReceived | 48055542032544 | 48053472977152 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2224364 | 85897118441 |
| averageLatency | 2224370 | 46497187 |
| maxLatency | 2224376 | 85897118453 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |

# Port Statistics - 256

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,3** | **Port 1,1,4** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.083602668 | 00:01:00.086045787 |
| framesSent | 21769421255 | 21770306443 |
| framesReceived | 21769421255 | 21770306443 |
| bitsSent | 44583774730240 | 44585587595264 |
| bitsReceived | 44585587595264 | 44583774730240 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 3393 | 121288 |
| fecMaxSymbolErrors | 1 | 2 |
| fecCorrectedCodewords | 3393 | 120631 |
| fecTotalCodewords | 9850746240 | 9850434560 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 6.331634e-11 | 2.263412e-09 |
| fecMaxSymbolErrorsBin0 | 9850742847 | 9850313929 |
| fecMaxSymbolErrorsBin1 | 3393 | 119974 |
| fecMaxSymbolErrorsBin2 | 0 | 657 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48066882131040 | 48068836626144 |
| l1BitsReceived | 48068836626144 | 48066882131040 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2224364 | 85897118441 |
| averageLatency | 2224370 | 8764422 |
| maxLatency | 2224376 | 85897118453 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |

# Port Statistics - 512

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,3** | **Port 1,1,4** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.269367464 | 00:01:00.273471384 |
| framesSent | 11328828470 | 11329599884 |
| framesReceived | 11328828470 | 11329599884 |
| bitsSent | 46402881413120 | 46406041124864 |
| bitsReceived | 46406041124864 | 46402881413120 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 3402 | 121526 |
| fecMaxSymbolErrors | 1 | 2 |
| fecCorrectedCodewords | 3402 | 120914 |
| fecTotalCodewords | 9880319360 | 9877821360 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 6.329427e-11 | 2.261565e-09 |
| fecMaxSymbolErrorsBin0 | 9880315958 | 9877700446 |
| fecMaxSymbolErrorsBin1 | 3402 | 120302 |
| fecMaxSymbolErrorsBin2 | 0 | 612 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48215493968320 | 48218777106304 |
| l1BitsReceived | 48218777106304 | 48215493968320 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2224364 | 85897118441 |
| averageLatency | 2224370 | 50888025 |
| maxLatency | 2224376 | 85897118453 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |

# Port Statistics - 1024

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,3** | **Port 1,1,4** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.359986836 | 00:01:00.365301951 |
| framesSent | 5781607934 | 5782117045 |
| framesReceived | 5781607934 | 5782117045 |
| bitsSent | 47362932195328 | 47367102832640 |
| bitsReceived | 47367102832640 | 47362932195328 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 3416 | 122151 |
| fecMaxSymbolErrors | 1 | 2 |
| fecCorrectedCodewords | 3416 | 121494 |
| fecTotalCodewords | 10000640256 | 9999246432 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 6.279010e-11 | 2.245592e-09 |
| fecMaxSymbolErrorsBin0 | 10000636840 | 9999124938 |
| fecMaxSymbolErrorsBin1 | 3416 | 120837 |
| fecMaxSymbolErrorsBin2 | 0 | 657 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48287989464768 | 48292241559840 |
| l1BitsReceived | 48292241559840 | 48287989464768 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2224364 | 85897118441 |
| averageLatency | 2224370 | 86548148 |
| maxLatency | 2224376 | 85897118453 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |

# Port Statistics - 1280

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,3** | **Port 1,1,4** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.407602676 | 00:01:00.412449633 |
| framesSent | 4646738667 | 4647111510 |
| framesReceived | 4646738667 | 4647111510 |
| bitsSent | 47582603950080 | 47586421862400 |
| bitsReceived | 47586421862400 | 47582603950080 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 3394 | 121987 |
| fecMaxSymbolErrors | 2 | 2 |
| fecCorrectedCodewords | 3393 | 121333 |
| fecTotalCodewords | 9919743984 | 9919339168 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 6.289447e-11 | 2.260643e-09 |
| fecMaxSymbolErrorsBin0 | 9919740591 | 9919217835 |
| fecMaxSymbolErrorsBin1 | 3392 | 120679 |
| fecMaxSymbolErrorsBin2 | 1 | 654 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48326082136800 | 48329959704000 |
| l1BitsReceived | 48329959704000 | 48326082136800 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2224364 | 85897118441 |
| averageLatency | 2224370 | 269618911 |
| maxLatency | 2224376 | 85897118453 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |

# Port Statistics - 1518

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,3** | **Port 1,1,4** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.327701555 | 00:01:00.349995558 |
| framesSent | 3922477344 | 3923926889 |
| framesReceived | 3922477344 | 3923926889 |
| bitsSent | 47634564865536 | 47652168140016 |
| bitsReceived | 47652168140016 | 47634564865536 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 3312 | 121790 |
| fecMaxSymbolErrors | 1 | 2 |
| fecCorrectedCodewords | 3312 | 121128 |
| fecTotalCodewords | 9910458784 | 9909414976 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 6.143243e-11 | 2.259252e-09 |
| fecMaxSymbolErrorsBin0 | 9910455472 | 9909293848 |
| fecMaxSymbolErrorsBin1 | 3312 | 120466 |
| fecMaxSymbolErrorsBin2 | 0 | 662 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48262161240576 | 48279996442256 |
| l1BitsReceived | 48279996442256 | 48262161240576 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2224364 | 85897118441 |
| averageLatency | 2224370 | 398810068 |
| maxLatency | 2224376 | 85897118453 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |