

Keysight Test Report

**PASS**

|  |  |
| --- | --- |
| Report Header: | Keysight Inc. |
| Report Date: | 2025/04/23 11:07:28 |
| Type: | Framed |

# SUMMARY

|  |  |
| --- | --- |
| IxOS Version | 10.00.1000.17 |
| Test Start Time | 2025/04/23 11:03:18 |
| Test End Time | 2025/04/23 11:07:28 |
| Test Duration | 00:01:00 |
| Pass/Fail Verdict | PASS |
| Module Type | 1,1,1 - 100GBASE-DR 1,1,2 - 100GBASE-DR |
| Module Version | 1,1,1 - CMIS 5.0 1,1,2 - CMIS 5.0 |
| Serial Number | 1,1,1 - UPC5000002 1,1,2 - UPC5000001 |

# Transceiver DOM (Digital Optical Monitoring) - 128

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **1,1,1** |  |  |  |  |  |  |  |  |  |  |
| **Manufactuer** | Eoptolink | **Model** | EOLO-138HG-5H-SM | **Mfg Revision** | 01 |  |  |  |  |  |
| **Type** | 100GBASE-DR | **SN** | UPC5000002 | **Firmware Revision** | 3.0 |  |  |  |  |  |
| **MSA** | CMIS 5.0 | **Date Code(YYMMDDLL)** | 231208 | **Hardware Revision** | 1.0 |  |  |  |  |  |
| **Media Tech** | 1310 nm DFB | **Media Connector** | MPO 1x16 |  |  |  |  |  |  |  |
| **Cable Lenth** | 0.0 m | **Identifier Type** | OSFP |  |  |  |  |  |  |  |
| **Reported Power Class** | 8 | **Reported Max Power** | 18.000000 W |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Modlue** | **High Alarm** | **High Warn** | **Low Warn** | **Low Alarm** |  |  |  |  |  |  |
| **Temperature** | 78 C | 73 C | -3 C | -8 C |  |  |  |  |  |  |
| **Supply Voltage** | 3.630 V | 3.465 V | 3.135 V | 2.970 V |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Lane Limits** | **High Alarm** | **High Warn** | **Low Warn** | **Low Alarm** |  |  |  |  |  |  |
| **Tx Optical Power** | 6.00 dBm | 5.00 dBm | -3.90 dBm | -4.90 dBm |  |  |  |  |  |  |
| **Rx Optical Power** | 6.50 dBm | 5.50 dBm | -7.40 dBm | -8.40 dBm |  |  |  |  |  |  |
| **Tx Bias Current** | 6.50 dBm | 5.50 dBm | -7.40 dBm | -8.40 dBm |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Host Lane** | **Port** | **Data Path State** | **Tx LOS** | **Tx CDR LOL** | **Media Lane** | **Tx Optical Power** | **Tx Bias Cureent** | **Rx Optical Power** | **Rx LOS** | **Rx CDR LOL** |
| 1 | 1.1 | Activated (4) | No | No | 1 | 0.53 dBm | 169.320 mA | 0.19 dBm | No | No |
| 2 | 1.1 | Activated (4) | No | No | 2 | 0.92 dBm | 169.192 mA | 0.29 dBm | No | No |
| 3 | 1.1 | Activated (4) | No | No | 3 | 0.81 dBm | 166.864 mA | 0.92 dBm | No | No |
| 4 | 1.1 | Activated (4) | No | No | 4 | 1.21 dBm | 166.992 mA | 1.58 dBm | No | No |
| 5 | 1.1 | Activated (4) | No | No | 5 | 0.85 dBm | 178.016 mA | -0.18 dBm | No | No |
| 6 | 1.1 | Activated (4) | No | No | 6 | 1.35 dBm | 178.016 mA | -1.11 dBm | No | No |
| 7 | 1.1 | Activated (4) | No | No | 7 | 1.50 dBm | 159.272 mA | 2.62 dBm | No | No |
| 8 | 1.1 | Activated (4) | No | No | 8 | 0.83 dBm | 160.864 mA | 1.90 dBm | No | No |

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **1,1,2** |  |  |  |  |  |  |  |  |  |  |
| **Manufactuer** | Eoptolink | **Model** | EOLO-138HG-5H-SM | **Mfg Revision** | 01 |  |  |  |  |  |
| **Type** | 100GBASE-DR | **SN** | UPC5000001 | **Firmware Revision** | 3.0 |  |  |  |  |  |
| **MSA** | CMIS 5.0 | **Date Code(YYMMDDLL)** | 231208 | **Hardware Revision** | 1.0 |  |  |  |  |  |
| **Media Tech** | 1310 nm DFB | **Media Connector** | MPO 1x16 |  |  |  |  |  |  |  |
| **Cable Lenth** | 0.0 m | **Identifier Type** | OSFP |  |  |  |  |  |  |  |
| **Reported Power Class** | 8 | **Reported Max Power** | 18.000000 W |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Modlue** | **High Alarm** | **High Warn** | **Low Warn** | **Low Alarm** |  |  |  |  |  |  |
| **Temperature** | 78 C | 73 C | -3 C | -8 C |  |  |  |  |  |  |
| **Supply Voltage** | 3.630 V | 3.465 V | 3.135 V | 2.970 V |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Lane Limits** | **High Alarm** | **High Warn** | **Low Warn** | **Low Alarm** |  |  |  |  |  |  |
| **Tx Optical Power** | 6.00 dBm | 5.00 dBm | -3.90 dBm | -4.90 dBm |  |  |  |  |  |  |
| **Rx Optical Power** | 6.50 dBm | 5.50 dBm | -7.40 dBm | -8.40 dBm |  |  |  |  |  |  |
| **Tx Bias Current** | 6.50 dBm | 5.50 dBm | -7.40 dBm | -8.40 dBm |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |
| **Host Lane** | **Port** | **Data Path State** | **Tx LOS** | **Tx CDR LOL** | **Media Lane** | **Tx Optical Power** | **Tx Bias Cureent** | **Rx Optical Power** | **Rx LOS** | **Rx CDR LOL** |
| 1 | 2.1 | Activated (4) | No | No | 1 | 1.11 dBm | 200.424 mA | 1.72 dBm | No | No |
| 2 | 2.1 | Activated (4) | No | No | 2 | 1.61 dBm | 200.424 mA | 1.64 dBm | No | No |
| 3 | 2.1 | Activated (4) | No | No | 3 | 1.44 dBm | 202.128 mA | 1.26 dBm | No | No |
| 4 | 2.1 | Activated (4) | No | No | 4 | 2.49 dBm | 201.400 mA | 0.90 dBm | No | No |
| 5 | 2.1 | Activated (4) | No | No | 5 | -0.23 dBm | 139.744 mA | 1.28 dBm | No | No |
| 6 | 2.1 | Activated (4) | No | No | 6 | -0.82 dBm | 139.872 mA | 0.82 dBm | No | No |
| 7 | 2.1 | Activated (4) | No | No | 7 | 1.71 dBm | 189.456 mA | 2.32 dBm | No | No |
| 8 | 2.1 | Activated (4) | No | No | 8 | 1.80 dBm | 189.944 mA | 0.85 dBm | No | No |

# Transceiver DOM (Digital Optical Monitoring) - 64

No DOM data available.

# CMIS Applicatio Select

|  |  |
| --- | --- |
| 1,1,1 |  |
| Current AppSel | 1 |

Available Applications

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **App** | **Host Side** | | | | | | **Line Side Media** | | | | |
|  | **Interface** | **Lane Speed (G bit/s)** | **Modulation** | **Lane Groups** | **Lanes** | **ID (HEex)** | **Interface** | **Lane Speed (G bit/s)** | **Lane Groups** | **Lanes** | **ID (Hex)** |
| 1 | 100GAUI-1-S C2M | 106 | PAM4 | 8 | 1 | 75 | 100GBASE-DR | 106 | 8 | 1 | 20 |
| 2 | 50GAUI-1 C2M | 53 | PAM4 | 8 | 1 | 10 | Undefined |  | 8 | 1 | 0 |
| 3 | 25GAUI C2M | 26 | NRZ | 8 | 1 | 5 | Undefined |  | 8 | 1 | 0 |

Preview of Auto selected applications

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Host Port Mode** | **Mod** | **Lane Groups** | **Lanes** | **AppSel** | **Link** | **Host Electrical** | **Lane Groups** | **Lanes** | **Note** |
| 800G-R8 | PAM4 | 1 | 8 | 1 | M | 100GAUI-1-S C2M | 1 | 8 | First app matching host lane speed for host id: 0x51 (800G S C2M 8x53.125 PAM4) |
| 400G-R4 | PAM4 | 2 | 4 | 1 | M | 100GAUI-1-S C2M | 2 | 4 | First app matching host lane speed for host id: 0x4f (400GAUI-4-S C2M 4x53.125 PAM4) |
| 200G-R2 | PAM4 | 4 | 2 | 1 | M | 100GAUI-1-S C2M | 4 | 2 | First app matching host lane speed for host id: 0x4d (200GAUI-2-S C2M 2x53.125 PAM4) |
| 100G-R | PAM4 | 8 | 1 | 1 | Y | 100GAUI-1-S C2M | 8 | 1 | First app matching compatible electrical mode: 0x4b (100GAUI-1-S C2M 1x53.125 PAM4) |
| 400G-R8 | PAM4 | 1 | 8 | 2 | M | 50GAUI-1 C2M | 1 | 8 | First app matching host lane speed for host id: 0x11 (400GAUI-8 C2M 8x26.5625 PAM4) |
| 200G-R4 | PAM4 | 2 | 4 | 2 | M | 50GAUI-1 C2M | 2 | 4 | First app matching host lane speed for host id: 0xf (200GAUI-4 C2M 4x26.5625 PAM4) |
| 100G-R2 | PAM4 | 4 | 2 | 2 | M | 50GAUI-1 C2M | 4 | 2 | First app matching host lane speed for host id: 0xd (100GAUI-2 C2M 2x26.5625 PAM4) |
| 50G-R | PAM4 | 8 | 1 | 2 | Y | 50GAUI-1 C2M | 8 | 1 | First app matching compatible electrical mode: 0xa (50GAUI-1 C2M 1x26.5625 PAM4) |
| 200G-R8 | NRZ | 1 | 8 | 1 | N | 100GAUI-1-S C2M | 1 | 8 | No matching electrical mode |
| 100G-R4 | NRZ | 2 | 4 | 3 | M | 25GAUI C2M | 2 | 4 | First app matching host lane speed for host id: 0xb (CAUI-4 C2M 4x25.78125 NRZ) |
| 50G-R2 | NRZ | 4 | 2 | 3 | M | 25GAUI C2M | 4 | 2 | First app matching host lane speed for host id: 0x43 (50GBASE-CR2 with RS 2x25.78125 NRZ) |
| 25G-R | NRZ | 8 | 1 | 3 | Y | 25GAUI C2M | 8 | 1 | First app matching compatible electrical mode: 0x5 (25GAUI C2M 1x25.78125 NRZ) |

|  |  |
| --- | --- |
| 1,1,2 |  |
| Current AppSel | 1 |

Available Applications

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **App** | **Host Side** | | | | | | **Line Side Media** | | | | |
|  | **Interface** | **Lane Speed (G bit/s)** | **Modulation** | **Lane Groups** | **Lanes** | **ID (HEex)** | **Interface** | **Lane Speed (G bit/s)** | **Lane Groups** | **Lanes** | **ID (Hex)** |
| 1 | 100GAUI-1-S C2M | 106 | PAM4 | 8 | 1 | 75 | 100GBASE-DR | 106 | 8 | 1 | 20 |
| 2 | 50GAUI-1 C2M | 53 | PAM4 | 8 | 1 | 10 | Undefined |  | 8 | 1 | 0 |
| 3 | 25GAUI C2M | 26 | NRZ | 8 | 1 | 5 | Undefined |  | 8 | 1 | 0 |

Preview of Auto selected applications

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Host Port Mode** | **Mod** | **Lane Groups** | **Lanes** | **AppSel** | **Link** | **Host Electrical** | **Lane Groups** | **Lanes** | **Note** |
| 800G-R8 | PAM4 | 1 | 8 | 1 | M | 100GAUI-1-S C2M | 1 | 8 | First app matching host lane speed for host id: 0x51 (800G S C2M 8x53.125 PAM4) |
| 400G-R4 | PAM4 | 2 | 4 | 1 | M | 100GAUI-1-S C2M | 2 | 4 | First app matching host lane speed for host id: 0x4f (400GAUI-4-S C2M 4x53.125 PAM4) |
| 200G-R2 | PAM4 | 4 | 2 | 1 | M | 100GAUI-1-S C2M | 4 | 2 | First app matching host lane speed for host id: 0x4d (200GAUI-2-S C2M 2x53.125 PAM4) |
| 100G-R | PAM4 | 8 | 1 | 1 | Y | 100GAUI-1-S C2M | 8 | 1 | First app matching compatible electrical mode: 0x4b (100GAUI-1-S C2M 1x53.125 PAM4) |
| 400G-R8 | PAM4 | 1 | 8 | 2 | M | 50GAUI-1 C2M | 1 | 8 | First app matching host lane speed for host id: 0x11 (400GAUI-8 C2M 8x26.5625 PAM4) |
| 200G-R4 | PAM4 | 2 | 4 | 2 | M | 50GAUI-1 C2M | 2 | 4 | First app matching host lane speed for host id: 0xf (200GAUI-4 C2M 4x26.5625 PAM4) |
| 100G-R2 | PAM4 | 4 | 2 | 2 | M | 50GAUI-1 C2M | 4 | 2 | First app matching host lane speed for host id: 0xd (100GAUI-2 C2M 2x26.5625 PAM4) |
| 50G-R | PAM4 | 8 | 1 | 2 | Y | 50GAUI-1 C2M | 8 | 1 | First app matching compatible electrical mode: 0xa (50GAUI-1 C2M 1x26.5625 PAM4) |
| 200G-R8 | NRZ | 1 | 8 | 1 | N | 100GAUI-1-S C2M | 1 | 8 | No matching electrical mode |
| 100G-R4 | NRZ | 2 | 4 | 3 | M | 25GAUI C2M | 2 | 4 | First app matching host lane speed for host id: 0xb (CAUI-4 C2M 4x25.78125 NRZ) |
| 50G-R2 | NRZ | 4 | 2 | 3 | M | 25GAUI C2M | 4 | 2 | First app matching host lane speed for host id: 0x43 (50GBASE-CR2 with RS 2x25.78125 NRZ) |
| 25G-R | NRZ | 8 | 1 | 3 | Y | 25GAUI C2M | 8 | 1 | First app matching compatible electrical mode: 0x5 (25GAUI C2M 1x25.78125 NRZ) |

# BERT Result Summary

No BERT Result Summary data available.

# BERT Statistics

No BERT Statistics data available.

# FEC Result Summary - 128

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,1** | **1,1,2** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# FEC Result Summary - 64

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,1** | **1,1,2** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | PASS | PASS |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | PASS | PASS |

# PCS Lane Statistics - 128

Port - 1,1,1

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 438.0 | 0.000000e+00 | 2.540467e-10 |
| **0** | Lock | 0 | 3.011 | 0 | 0 | 2 | 0.000000e+00 | 1.178426e-12 |
| **1** | Lock | 16 | 3.011 | 0 | 0 | 2 | 0.000000e+00 | 1.168296e-12 |
| **2** | Lock | 1 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **3** | Lock | 17 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 8 | 0.000000e+00 | 4.673184e-12 |
| **5** | Lock | 18 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.841480e-13 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **8** | Lock | 4 | 0.0 | 0 | 0 | 5 | 0.000000e+00 | 2.920740e-12 |
| **9** | Lock | 20 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **10** | Lock | 5 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **11** | Lock | 21 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **12** | Lock | 6 | 0.0 | 0 | 0 | 7 | 0.000000e+00 | 4.058699e-12 |
| **13** | Lock | 22 | 0.0 | 0 | 0 | 9 | 0.000000e+00 | 5.218328e-12 |
| **14** | Lock | 7 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 1.159628e-12 |
| **15** | Lock | 23 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 8 | 0.000000e+00 | 4.638513e-12 |
| **17** | Lock | 8 | 0.0 | 0 | 0 | 5 | 0.000000e+00 | 2.899071e-12 |
| **18** | Lock | 25 | 6.023 | 0 | 0 | 1 | 0.000000e+00 | 5.798142e-13 |
| **19** | Lock | 9 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **20** | Lock | 26 | 9.035 | 0 | 0 | 92 | 0.000000e+00 | 5.334290e-11 |
| **21** | Lock | 10 | 9.035 | 0 | 0 | 271 | 0.000000e+00 | 1.571296e-10 |
| **22** | Lock | 27 | 3.011 | 0 | 0 | 2 | 0.000000e+00 | 1.159628e-12 |
| **23** | Lock | 11 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.798142e-13 |
| **24** | Lock | 12 | 9.035 | 0 | 0 | 9 | 0.000000e+00 | 5.218328e-12 |
| **25** | Lock | 28 | 9.035 | 0 | 0 | 9 | 0.000000e+00 | 5.218328e-12 |
| **26** | Lock | 13 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **27** | Lock | 29 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **28** | Lock | 30 | 3.011 | 0 | 0 | 4 | 0.000000e+00 | 2.319257e-12 |
| **29** | Lock | 14 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **30** | Lock | 31 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **31** | Lock | 15 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |

Port - 1,1,2

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 12.047 | 0.0 | 0.0 | 524.0 | 0.000000e+00 | 2.998541e-10 |
| **0** | Lock | 0 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **1** | Lock | 16 | 3.011 | 0 | 0 | 3 | 0.000000e+00 | 1.725686e-12 |
| **2** | Lock | 1 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **3** | Lock | 17 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **4** | Lock | 2 | 6.023 | 0 | 0 | 6 | 0.000000e+00 | 3.451371e-12 |
| **5** | Lock | 18 | 6.023 | 0 | 0 | 4 | 0.000000e+00 | 2.300914e-12 |
| **6** | Lock | 3 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **7** | Lock | 19 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **8** | Lock | 20 | 0.0 | 0 | 0 | 28 | 0.000000e+00 | 1.610640e-11 |
| **9** | Lock | 4 | 0.0 | 0 | 0 | 8 | 0.000000e+00 | 4.601828e-12 |
| **10** | Lock | 21 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.752286e-13 |
| **11** | Lock | 5 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 58 | 0.000000e+00 | 3.336326e-11 |
| **13** | Lock | 6 | 3.011 | 0 | 0 | 63 | 0.000000e+00 | 3.623940e-11 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 8 | 0.000000e+00 | 4.566346e-12 |
| **15** | Lock | 7 | 6.023 | 0 | 0 | 5 | 0.000000e+00 | 2.853966e-12 |
| **16** | Lock | 8 | 6.023 | 0 | 0 | 5 | 0.000000e+00 | 2.853966e-12 |
| **17** | Lock | 24 | 6.023 | 0 | 0 | 3 | 0.000000e+00 | 1.712380e-12 |
| **18** | Lock | 9 | 9.035 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **19** | Lock | 25 | 9.035 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **20** | Lock | 26 | 12.047 | 0 | 0 | 45 | 0.000000e+00 | 2.568570e-11 |
| **21** | Lock | 10 | 12.047 | 0 | 0 | 238 | 0.000000e+00 | 1.358488e-10 |
| **22** | Lock | 27 | 9.035 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **23** | Lock | 11 | 9.035 | 0 | 0 | 1 | 0.000000e+00 | 5.707933e-13 |
| **24** | Lock | 12 | 9.035 | 0 | 0 | 11 | 0.000000e+00 | 6.278726e-12 |
| **25** | Lock | 28 | 9.035 | 0 | 0 | 20 | 0.000000e+00 | 1.141587e-11 |
| **26** | Lock | 13 | 9.035 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **27** | Lock | 29 | 9.035 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **28** | Lock | 14 | 6.023 | 0 | 0 | 8 | 0.000000e+00 | 4.566346e-12 |
| **29** | Lock | 30 | 6.023 | 0 | 0 | 9 | 0.000000e+00 | 5.137139e-12 |
| **30** | Lock | 15 | 9.035 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **31** | Lock | 31 | 9.035 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |

# PCS Lane Statistics - 64

Port - 1,1,1

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 9.035 | 0.0 | 0.0 | 466.0 | 0.000000e+00 | 2.719919e-10 |
| **0** | Lock | 0 | 3.011 | 0 | 0 | 6 | 0.000000e+00 | 3.528204e-12 |
| **1** | Lock | 16 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.880340e-13 |
| **2** | Lock | 1 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **3** | Lock | 17 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 4 | 0.000000e+00 | 2.352136e-12 |
| **5** | Lock | 18 | 3.011 | 0 | 0 | 3 | 0.000000e+00 | 1.764102e-12 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **8** | Lock | 4 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.880340e-13 |
| **9** | Lock | 20 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 5.835285e-13 |
| **10** | Lock | 5 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **11** | Lock | 21 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **12** | Lock | 6 | 0.0 | 0 | 0 | 7 | 0.000000e+00 | 4.084700e-12 |
| **13** | Lock | 22 | 0.0 | 0 | 0 | 11 | 0.000000e+00 | 6.418814e-12 |
| **14** | Lock | 7 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 23 | 0.0 | 0 | 0 | 3 | 0.000000e+00 | 1.750586e-12 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 16 | 0.000000e+00 | 9.336456e-12 |
| **17** | Lock | 8 | 0.0 | 0 | 0 | 6 | 0.000000e+00 | 3.501171e-12 |
| **18** | Lock | 25 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **19** | Lock | 9 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **20** | Lock | 26 | 9.035 | 0 | 0 | 101 | 0.000000e+00 | 5.893638e-11 |
| **21** | Lock | 10 | 9.035 | 0 | 0 | 286 | 0.000000e+00 | 1.668892e-10 |
| **22** | Lock | 27 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **23** | Lock | 11 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **24** | Lock | 12 | 9.035 | 0 | 0 | 12 | 0.000000e+00 | 7.002342e-12 |
| **25** | Lock | 28 | 9.035 | 0 | 0 | 8 | 0.000000e+00 | 4.668228e-12 |
| **26** | Lock | 13 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **27** | Lock | 29 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **28** | Lock | 30 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **29** | Lock | 14 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **30** | Lock | 31 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **31** | Lock | 15 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |

Port - 1,1,2

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 12.047 | 0.0 | 0.0 | 466.0 | 0.000000e+00 | 2.678248e-10 |
| **0** | Lock | 0 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **1** | Lock | 16 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.789222e-13 |
| **2** | Lock | 1 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **3** | Lock | 17 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.789222e-13 |
| **4** | Lock | 2 | 6.023 | 0 | 0 | 2 | 0.000000e+00 | 1.157844e-12 |
| **5** | Lock | 18 | 6.023 | 0 | 0 | 3 | 0.000000e+00 | 1.736767e-12 |
| **6** | Lock | 3 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 5.789222e-13 |
| **7** | Lock | 19 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **8** | Lock | 20 | 0.0 | 0 | 0 | 18 | 0.000000e+00 | 1.042060e-11 |
| **9** | Lock | 4 | 0.0 | 0 | 0 | 10 | 0.000000e+00 | 5.744936e-12 |
| **10** | Lock | 21 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **11** | Lock | 5 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **12** | Lock | 22 | 3.011 | 0 | 0 | 50 | 0.000000e+00 | 2.872468e-11 |
| **13** | Lock | 6 | 3.011 | 0 | 0 | 42 | 0.000000e+00 | 2.412873e-11 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 12 | 0.000000e+00 | 6.893923e-12 |
| **15** | Lock | 7 | 6.023 | 0 | 0 | 5 | 0.000000e+00 | 2.872468e-12 |
| **16** | Lock | 8 | 6.023 | 0 | 0 | 1 | 0.000000e+00 | 5.744936e-13 |
| **17** | Lock | 24 | 6.023 | 0 | 0 | 7 | 0.000000e+00 | 4.021455e-12 |
| **18** | Lock | 9 | 9.035 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **19** | Lock | 25 | 9.035 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **20** | Lock | 26 | 12.047 | 0 | 0 | 49 | 0.000000e+00 | 2.815019e-11 |
| **21** | Lock | 10 | 12.047 | 0 | 0 | 230 | 0.000000e+00 | 1.321335e-10 |
| **22** | Lock | 27 | 9.035 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **23** | Lock | 11 | 9.035 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **24** | Lock | 12 | 9.035 | 0 | 0 | 6 | 0.000000e+00 | 3.446962e-12 |
| **25** | Lock | 28 | 9.035 | 0 | 0 | 7 | 0.000000e+00 | 4.021455e-12 |
| **26** | Lock | 13 | 9.035 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **27** | Lock | 29 | 9.035 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **28** | Lock | 14 | 6.023 | 0 | 0 | 6 | 0.000000e+00 | 3.446962e-12 |
| **29** | Lock | 30 | 6.023 | 0 | 0 | 14 | 0.000000e+00 | 8.042911e-12 |
| **30** | Lock | 15 | 9.035 | 0 | 0 | 1 | 0.000000e+00 | 5.701248e-13 |
| **31** | Lock | 31 | 9.035 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |

# L2 Traffic Test Summary

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Frame Size** | **Tx Count** | **Rx Count** | **Loss Count** | **Loss %** |
| **128** | 81751909164 | 81751909164 | 0 | 0.0 |
| **64** | 143810063575 | 143810063575 | 0 | 0.0 |

# Port Statistics - 128

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,1** | **Port 1,1,2** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.494900748 | 00:01:00.497924982 |
| framesSent | 40874932880 | 40876976284 |
| framesReceived | 40874932880 | 40876976284 |
| bitsSent | 41855931269120 | 41858023714816 |
| bitsReceived | 41858023714816 | 41855931269120 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 432 | 501 |
| fecMaxSymbolErrors | 1 | 1 |
| fecCorrectedCodewords | 432 | 501 |
| fecTotalCodewords | 9927587776 | 9925066688 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 7.999100e-12 | 9.279090e-12 |
| fecMaxSymbolErrorsBin0 | 9927587344 | 9925066187 |
| fecMaxSymbolErrorsBin1 | 432 | 501 |
| fecMaxSymbolErrorsBin2 | 0 | 0 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48395920529920 | 48398339920256 |
| l1BitsReceived | 48398339920256 | 48395920529920 |
| transceiverTemp | 54 | 55 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 3.2536999999999998 | 3.2416 |
| minLatency | 2232563 | 85897110480 |
| averageLatency | 2232570 | 56590612 |
| maxLatency | 2232575 | 85897110491 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |

# Port Statistics - 64

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,1** | **Port 1,1,2** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:01:00.395153310 | 00:01:00.405300104 |
| framesSent | 71898992027 | 71911071548 |
| framesReceived | 71898992027 | 71911071548 |
| bitsSent | 36812283917824 | 36818468632576 |
| bitsReceived | 36818468632576 | 36812283917824 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 458 | 445 |
| fecMaxSymbolErrors | 1 | 1 |
| fecCorrectedCodewords | 458 | 445 |
| fecTotalCodewords | 9888647216 | 9888321104 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 8.513923e-12 | 8.272534e-12 |
| fecMaxSymbolErrorsBin0 | 9888646758 | 9888320659 |
| fecMaxSymbolErrorsBin1 | 458 | 445 |
| fecMaxSymbolErrorsBin2 | 0 | 0 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 48316122642144 | 48324240080256 |
| l1BitsReceived | 48324240080256 | 48316122642144 |
| transceiverTemp | 55 | 55 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 3.2488999999999999 | 3.2462 |
| minLatency | 2232563 | 85897110480 |
| averageLatency | 2232570 | 8860995 |
| maxLatency | 2232575 | 85897110491 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |