|  |  |
| --- | --- |
|  |  |

Keysight Test Report

**PASS**

|  |  |
| --- | --- |
| Report Header: | Keysight Inc. |
| Report Date: | 2025/06/22 20:54:55 |
| Type: | Framed |

# SUMMARY

|  |  |
| --- | --- |
| IxOS Version | 10.00.1000.17 |
| Test Start Time | 2025/06/22 20:49:41 |
| Test End Time | 2025/06/22 20:54:54 |
| Test Duration | 00:00:30 |
| Pass/Fail Verdict | PASS |
| Module Type | 1,1,1 - PassiveCopper 1,1,3 - PassiveCopper |
| Module Version | 1,1,1 - CMIS 5.0 1,1,3 - CMIS 5.0 |
| Serial Number | 1,1,1 - APF23350014Y55 1,1,3 - APF23350014Y55 |

# Transceiver DOM (Digital Optical Monitoring) - 128

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **1,1,1** | | | | | | | | | | |
| **Manufacturer** | | Amphenol | | **Model** | | NJMYE6-0001 | | **Mfg Revision** | | 6 |
| **Type** | | PassiveCopper | | **SN** | | APF23350014Y55 | | **Firmware Revision** | |  |
| **MSA** | | CMIS 5.0 | | **Date Code(YYMMDDLL)** | | 230916 | | **Hardware Revision** | |  |
| **Media Tech** | | Copper cable unequalized | | **Media Connector** | | No separable connector | |  | |  |
| **Cable Lenth** | | 1.0 m | | **Identifier Type** | | QSFP-DD | |  | |  |
| **Reported Power Class** | | 1 | | **Reported Max Power** | | 0.250000 W | |  | |  |
|  | |  | |  | |  | |  | |  |
| **Modlue** | | **High Alarm** | | **High Warn** | | **Low Warn** | | **Low Alarm** | |  |
| **Temperature** | | -0.001 C | | -0.001 C | | -0.001 C | | -0.001 C | |  |
| **Supply Voltage** | | -0.000 V | | -0.000 V | | -0.000 V | | -0.000 V | |  |
|  | |  | |  | |  | |  | |  |
| **Lane Limits** | | **High Alarm** | | **High Warn** | | **Low Warn** | | **Low Alarm** | |  |
| **Tx Optical Power** | | -40.00 dBm | | -40.00 dBm | | -40.00 dBm | | -40.00 dBm | |  |
| **Rx Optical Power** | | -40.00 dBm | | -40.00 dBm | | -40.00 dBm | | -40.00 dBm | |  |
| **Tx Bias Current** | | -40.00 dBm | | -40.00 dBm | | -40.00 dBm | | -40.00 dBm | |  |
|  | |  | |  | |  | |  | |  |
| **Host Lane** | **Port** | **Data Path State** | **Tx LOS** | **Tx CDR LOL** | **Media Lane** | **Tx Optical Power** | **Tx Bias Cureent** | **Rx Optical Power** | **Rx LOS** | **Rx CDR LOL** |
| 1 | 1.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 2 | 1.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 3 | 1.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 4 | 1.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 5 | 1.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 6 | 1.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 7 | 1.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 8 | 1.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **1,1,3** | | | | | | | | | | |
| **Manufacturer** | | Amphenol | | **Model** | | NJMYE6-0001 | | **Mfg Revision** | | 6 |
| **Type** | | PassiveCopper | | **SN** | | APF23350014Y55 | | **Firmware Revision** | |  |
| **MSA** | | CMIS 5.0 | | **Date Code(YYMMDDLL)** | | 230916 | | **Hardware Revision** | |  |
| **Media Tech** | | Copper cable unequalized | | **Media Connector** | | No separable connector | |  | |  |
| **Cable Lenth** | | 1.0 m | | **Identifier Type** | | OSFP | |  | |  |
| **Reported Power Class** | | 1 | | **Reported Max Power** | | 0.250000 W | |  | |  |
|  | |  | |  | |  | |  | |  |
| **Modlue** | | **High Alarm** | | **High Warn** | | **Low Warn** | | **Low Alarm** | |  |
| **Temperature** | | -0.001 C | | -0.001 C | | -0.001 C | | -0.001 C | |  |
| **Supply Voltage** | | -0.000 V | | -0.000 V | | -0.000 V | | -0.000 V | |  |
|  | |  | |  | |  | |  | |  |
| **Lane Limits** | | **High Alarm** | | **High Warn** | | **Low Warn** | | **Low Alarm** | |  |
| **Tx Optical Power** | | -40.00 dBm | | -40.00 dBm | | -40.00 dBm | | -40.00 dBm | |  |
| **Rx Optical Power** | | -40.00 dBm | | -40.00 dBm | | -40.00 dBm | | -40.00 dBm | |  |
| **Tx Bias Current** | | -40.00 dBm | | -40.00 dBm | | -40.00 dBm | | -40.00 dBm | |  |
|  | |  | |  | |  | |  | |  |
| **Host Lane** | **Port** | **Data Path State** | **Tx LOS** | **Tx CDR LOL** | **Media Lane** | **Tx Optical Power** | **Tx Bias Cureent** | **Rx Optical Power** | **Rx LOS** | **Rx CDR LOL** |
| 1 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 2 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 3 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 4 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 5 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 6 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 7 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |
| 8 | 3.1 | 0 | No | No | 0 | -40.00 dBm | 0.000 mA | -40.00 dBm | No | No |

# CMIS Applicatio Select

|  |  |
| --- | --- |
| 1,1,1 |  |
| Current AppSel | 0 |

Available Applications

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **App** | **Host Side** | | | | | | **Line Side Media** | | | | |
|  | **Interface** | **Lane Speed (G bit/s)** | **Modulation** | **Lane Groups** | **Lanes** | **ID (HEex)** | **Interface** | **Lane Speed (G bit/s)** | **Lane Groups** | **Lanes** | **ID (Hex)** |
| 1 | 800GBASE-CR8 | 106 | PAM4 | 1 | 8 | 73 | PassiveCopper |  | 0 | 8 | 1 |
| 2 | 400GBASE-CR4 | 106 | PAM4 | 2 | 4 | 72 | PassiveCopper |  | 0 | 8 | 1 |
| 3 | 400GBASE-CR8 | 53 | PAM4 | 1 | 8 | 29 | PassiveCopper |  | 0 | 8 | 1 |
| 4 | 200GBASE-CR4 | 53 | PAM4 | 2 | 4 | 28 | PassiveCopper |  | 0 | 8 | 1 |
| 5 | 100GBASE-CR4 | 26 | NRZ | 2 | 4 | 26 | PassiveCopper |  | 0 | 8 | 1 |
| 6 | 40GBASE-CR4 | 10 | NRZ | 2 | 4 | 23 | PassiveCopper |  | 0 | 8 | 1 |

Preview of Auto selected applications

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Host Port Mode** | **Mod** | **Lane Groups** | **Lanes** | **AppSel** | **Link** | **Host Electrical** | **Lane Groups** | **Lanes** | **Note** |

|  |  |
| --- | --- |
| 1,1,3 |  |
| Current AppSel | 0 |

Available Applications

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **App** | **Host Side** | | | | | | **Line Side Media** | | | | |
|  | **Interface** | **Lane Speed (G bit/s)** | **Modulation** | **Lane Groups** | **Lanes** | **ID (HEex)** | **Interface** | **Lane Speed (G bit/s)** | **Lane Groups** | **Lanes** | **ID (Hex)** |
| 1 | 800GBASE-CR8 | 106 | PAM4 | 1 | 8 | 73 | PassiveCopper |  | 0 | 8 | 1 |
| 2 | 400GBASE-CR4 | 106 | PAM4 | 2 | 4 | 72 | PassiveCopper |  | 0 | 8 | 1 |
| 3 | 400GBASE-CR8 | 53 | PAM4 | 1 | 8 | 29 | PassiveCopper |  | 0 | 8 | 1 |
| 4 | 200GBASE-CR4 | 53 | PAM4 | 2 | 4 | 28 | PassiveCopper |  | 0 | 8 | 1 |
| 5 | 100GBASE-CR4 | 26 | NRZ | 2 | 4 | 26 | PassiveCopper |  | 0 | 8 | 1 |
| 6 | 40GBASE-CR4 | 10 | NRZ | 2 | 4 | 23 | PassiveCopper |  | 0 | 8 | 1 |

Preview of Auto selected applications

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Host Port Mode** | **Mod** | **Lane Groups** | **Lanes** | **AppSel** | **Link** | **Host Electrical** | **Lane Groups** | **Lanes** | **Note** |

# BERT Result Summary

# BERT Statistics

# FEC Result Summary - 128

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,1** | **1,1,3** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | **PASS** | **PASS** |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | **PASS** | **PASS** |

# FEC Result Summary - 256

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,1** | **1,1,3** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | **PASS** | **PASS** |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | **PASS** | **PASS** |

# FEC Result Summary - 512

|  |  |  |
| --- | --- | --- |
| **Frame Loss Ratio** | **1,1,1** | **1,1,3** |
| **Pre-FEC Standard** | 2.400000e-04 | 2.400000e-04 |
| **Pre-FEC Pass/Fail Verdict** | **PASS** | **PASS** |
| **Post-FEC Standard** | 9.200000e-13 | 9.200000e-13 |
| **Post-FEC Pass/Fail Verdict** | **PASS** | **PASS** |

# PCS Lane Statistics - 128

Port - 1,1,1

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 134.0 | 0.000000e+00 | 1.479101e-10 |
| **0** | Lock | 0 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 1.115501e-12 |
| **1** | Lock | 16 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **2** | Lock | 1 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **3** | Lock | 17 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **4** | Lock | 2 | 0.0 | 0 | 0 | 25 | 0.000000e+00 | 2.788752e-11 |
| **5** | Lock | 18 | 0.0 | 0 | 0 | 8 | 0.000000e+00 | 8.924007e-12 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 1.115501e-12 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **8** | Lock | 4 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **9** | Lock | 20 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **10** | Lock | 5 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 1.115501e-12 |
| **13** | Lock | 22 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **14** | Lock | 7 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 23 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 20 | 0.000000e+00 | 2.199022e-11 |
| **17** | Lock | 8 | 0.0 | 0 | 0 | 15 | 0.000000e+00 | 1.649267e-11 |
| **18** | Lock | 25 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **20** | Lock | 10 | 6.023 | 0 | 0 | 2 | 0.000000e+00 | 2.199022e-12 |
| **21** | Lock | 26 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **22** | Lock | 11 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **24** | Lock | 12 | 3.011 | 0 | 0 | 21 | 0.000000e+00 | 2.308973e-11 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 36 | 0.000000e+00 | 3.958240e-11 |
| **26** | Lock | 13 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 1.099511e-12 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 1.099511e-12 |
| **28** | Lock | 14 | 3.011 | 0 | 0 | 2 | 0.000000e+00 | 2.199022e-12 |
| **29** | Lock | 30 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 413.0 | 0.000000e+00 | 4.409380e-10 |
| **0** | Lock | 0 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **1** | Lock | 16 | 6.023 | 0 | 0 | 1 | 0.000000e+00 | 1.083686e-12 |
| **2** | Lock | 1 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **3** | Lock | 17 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 5 | 0.000000e+00 | 5.418432e-12 |
| **5** | Lock | 18 | 6.023 | 0 | 0 | 2 | 0.000000e+00 | 2.167373e-12 |
| **6** | Lock | 3 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **7** | Lock | 19 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **8** | Lock | 4 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **9** | Lock | 20 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **10** | Lock | 5 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **12** | Lock | 22 | 6.023 | 0 | 0 | 5 | 0.000000e+00 | 5.418432e-12 |
| **13** | Lock | 6 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 7 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **16** | Lock | 8 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **17** | Lock | 24 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **18** | Lock | 9 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **19** | Lock | 25 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **20** | Lock | 10 | 3.011 | 0 | 0 | 308 | 0.000000e+00 | 3.286746e-10 |
| **21** | Lock | 26 | 6.023 | 0 | 0 | 58 | 0.000000e+00 | 6.189327e-11 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 6 | 0.000000e+00 | 6.402752e-12 |
| **23** | Lock | 27 | 6.023 | 0 | 0 | 4 | 0.000000e+00 | 4.268501e-12 |
| **24** | Lock | 28 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **25** | Lock | 12 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **26** | Lock | 29 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **27** | Lock | 13 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **28** | Lock | 14 | 3.011 | 0 | 0 | 11 | 0.000000e+00 | 1.173838e-11 |
| **29** | Lock | 30 | 6.023 | 0 | 0 | 12 | 0.000000e+00 | 1.280550e-11 |
| **30** | Lock | 15 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **31** | Lock | 31 | 6.023 | 0 | 0 | 1 | 0.000000e+00 | 1.067125e-12 |

# PCS Lane Statistics - 256

Port - 1,1,1

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 154.0 | 0.000000e+00 | 1.685800e-10 |
| **0** | Lock | 0 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 1.118749e-12 |
| **1** | Lock | 16 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **2** | Lock | 1 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **3** | Lock | 17 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **4** | Lock | 2 | 0.0 | 0 | 0 | 23 | 0.000000e+00 | 2.534740e-11 |
| **5** | Lock | 18 | 0.0 | 0 | 0 | 8 | 0.000000e+00 | 8.816486e-12 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 1.102061e-12 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 1.102061e-12 |
| **8** | Lock | 4 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **9** | Lock | 20 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **10** | Lock | 5 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **13** | Lock | 22 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **14** | Lock | 7 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 23 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 22 | 0.000000e+00 | 2.424534e-11 |
| **17** | Lock | 8 | 0.0 | 0 | 0 | 24 | 0.000000e+00 | 2.644946e-11 |
| **18** | Lock | 25 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 1.086466e-12 |
| **20** | Lock | 10 | 6.023 | 0 | 0 | 2 | 0.000000e+00 | 2.172931e-12 |
| **21** | Lock | 26 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **22** | Lock | 11 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 1.086466e-12 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **24** | Lock | 12 | 3.011 | 0 | 0 | 28 | 0.000000e+00 | 3.042103e-11 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 40 | 0.000000e+00 | 4.345862e-11 |
| **26** | Lock | 13 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **28** | Lock | 14 | 3.011 | 0 | 0 | 2 | 0.000000e+00 | 2.172931e-12 |
| **29** | Lock | 30 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 399.0 | 0.000000e+00 | 4.253197e-10 |
| **0** | Lock | 0 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 1.086466e-12 |
| **1** | Lock | 16 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **2** | Lock | 1 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **3** | Lock | 17 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 6 | 0.000000e+00 | 6.418993e-12 |
| **5** | Lock | 18 | 6.023 | 0 | 0 | 1 | 0.000000e+00 | 1.069832e-12 |
| **6** | Lock | 3 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **7** | Lock | 19 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **8** | Lock | 4 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **9** | Lock | 20 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **10** | Lock | 5 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **12** | Lock | 22 | 6.023 | 0 | 0 | 4 | 0.000000e+00 | 4.279329e-12 |
| **13** | Lock | 6 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 1.069832e-12 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 7 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **16** | Lock | 8 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **17** | Lock | 24 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **18** | Lock | 9 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **19** | Lock | 25 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **20** | Lock | 10 | 3.011 | 0 | 0 | 282 | 0.000000e+00 | 3.016927e-10 |
| **21** | Lock | 26 | 6.023 | 0 | 0 | 71 | 0.000000e+00 | 7.489312e-11 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 4 | 0.000000e+00 | 4.219331e-12 |
| **23** | Lock | 27 | 6.023 | 0 | 0 | 3 | 0.000000e+00 | 3.164498e-12 |
| **24** | Lock | 28 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **25** | Lock | 12 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **26** | Lock | 29 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **27** | Lock | 13 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **28** | Lock | 14 | 3.011 | 0 | 0 | 10 | 0.000000e+00 | 1.054833e-11 |
| **29** | Lock | 30 | 6.023 | 0 | 0 | 16 | 0.000000e+00 | 1.687732e-11 |
| **30** | Lock | 15 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **31** | Lock | 31 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |

# PCS Lane Statistics - 512

Port - 1,1,1

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 158.0 | 0.000000e+00 | 1.739071e-10 |
| **0** | Lock | 0 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 2.233589e-12 |
| **1** | Lock | 16 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 1.116794e-12 |
| **2** | Lock | 1 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **3** | Lock | 17 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **4** | Lock | 2 | 0.0 | 0 | 0 | 27 | 0.000000e+00 | 3.015345e-11 |
| **5** | Lock | 18 | 0.0 | 0 | 0 | 9 | 0.000000e+00 | 1.005115e-11 |
| **6** | Lock | 3 | 0.0 | 0 | 0 | 2 | 0.000000e+00 | 2.233589e-12 |
| **7** | Lock | 19 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 1.116794e-12 |
| **8** | Lock | 4 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **9** | Lock | 20 | 6.023 | 0 | 0 | 2 | 0.000000e+00 | 2.233589e-12 |
| **10** | Lock | 5 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **12** | Lock | 6 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 1.100749e-12 |
| **13** | Lock | 22 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 1.100749e-12 |
| **14** | Lock | 7 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 23 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **16** | Lock | 24 | 3.011 | 0 | 0 | 14 | 0.000000e+00 | 1.541048e-11 |
| **17** | Lock | 8 | 0.0 | 0 | 0 | 25 | 0.000000e+00 | 2.751872e-11 |
| **18** | Lock | 25 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **19** | Lock | 9 | 0.0 | 0 | 0 | 1 | 0.000000e+00 | 1.100749e-12 |
| **20** | Lock | 10 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **21** | Lock | 26 | 6.023 | 0 | 0 | 1 | 0.000000e+00 | 1.100749e-12 |
| **22** | Lock | 11 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **23** | Lock | 27 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **24** | Lock | 12 | 3.011 | 0 | 0 | 29 | 0.000000e+00 | 3.192171e-11 |
| **25** | Lock | 28 | 3.011 | 0 | 0 | 38 | 0.000000e+00 | 4.117952e-11 |
| **26** | Lock | 13 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 1.083672e-12 |
| **27** | Lock | 29 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 1.083672e-12 |
| **28** | Lock | 14 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 1.083672e-12 |
| **29** | Lock | 30 | 3.011 | 0 | 0 | 1 | 0.000000e+00 | 1.083672e-12 |
| **30** | Lock | 15 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **31** | Lock | 31 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |

Port - 1,1,3

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Physical Lane** | **PCS Lane Marker Lock** | **PCS Lane Marker Map** | **Relative Lane Skew (ns)** | **PCS Lane Marker Error Count** | **FEC Symbol Error Count** | **FEC Correct Bit Count** | **FEC Symbol Error** | **FEC Correct Bit Rate** |
| **Totals** | Lock | all | 6.023 | 0.0 | 0.0 | 374.0 | 0.000000e+00 | 3.992231e-10 |
| **0** | Lock | 0 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **1** | Lock | 16 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **2** | Lock | 1 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **3** | Lock | 17 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **4** | Lock | 2 | 3.011 | 0 | 0 | 2 | 0.000000e+00 | 2.167343e-12 |
| **5** | Lock | 18 | 6.023 | 0 | 0 | 1 | 0.000000e+00 | 1.083672e-12 |
| **6** | Lock | 3 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **7** | Lock | 19 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **8** | Lock | 4 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **9** | Lock | 20 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **10** | Lock | 5 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **11** | Lock | 21 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **12** | Lock | 22 | 6.023 | 0 | 0 | 3 | 0.000000e+00 | 3.203942e-12 |
| **13** | Lock | 6 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **14** | Lock | 23 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **15** | Lock | 7 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **16** | Lock | 8 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **17** | Lock | 24 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **18** | Lock | 9 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **19** | Lock | 25 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **20** | Lock | 10 | 3.011 | 0 | 0 | 272 | 0.000000e+00 | 2.904907e-10 |
| **21** | Lock | 26 | 6.023 | 0 | 0 | 56 | 0.000000e+00 | 5.980691e-11 |
| **22** | Lock | 11 | 3.011 | 0 | 0 | 11 | 0.000000e+00 | 1.174779e-11 |
| **23** | Lock | 27 | 6.023 | 0 | 0 | 4 | 0.000000e+00 | 4.271922e-12 |
| **24** | Lock | 28 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **25** | Lock | 12 | 0.0 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **26** | Lock | 29 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **27** | Lock | 13 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **28** | Lock | 14 | 3.011 | 0 | 0 | 8 | 0.000000e+00 | 8.543844e-12 |
| **29** | Lock | 30 | 6.023 | 0 | 0 | 17 | 0.000000e+00 | 1.790698e-11 |
| **30** | Lock | 15 | 3.011 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |
| **31** | Lock | 31 | 6.023 | 0 | 0 | 0 | 0.000000e+00 | 0.000000e+00 |

# L2 Traffic Test Summary

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Frame Size** | **Tx Count** | **Rx Count** | **Loss Count** | **Loss %** |
| **128** | 40571235976 | 40571235976 | 0 | 0.0 |
| **256** | 22021093319 | 22021093319 | 0 | 0.0 |
| **512** | 11388055904 | 11388055904 | 0 | 0.0 |

# Port Statistics - 128

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,1** | **Port 1,1,3** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:00:30.021039230 | 00:00:30.024390187 |
| framesSent | 20284485908 | 20286750068 |
| framesReceived | 20284485908 | 20286750068 |
| fragments | 0 | 0 |
| undersize | 0 | 0 |
| oversizeAndCrcErrors | 0 | 0 |
| vlanTaggedFramesRx | 0 | 0 |
| flowControlFrames | 0 | 0 |
| bitsSent | 20771313569792 | 20773632069632 |
| bitsReceived | 20773632069632 | 20771313569792 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 128 | 377 |
| fecMaxSymbolErrors | 1 | 1 |
| fecCorrectedCodewords | 128 | 377 |
| fecTotalCodewords | 5148889760 | 5148256176 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 4.569803e-12 | 1.346115e-11 |
| fecMaxSymbolErrorsBin0 | 5148889632 | 5148255799 |
| fecMaxSymbolErrorsBin1 | 128 | 377 |
| fecMaxSymbolErrorsBin2 | 0 | 0 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 24016831315072 | 24019512080512 |
| l1BitsReceived | 24019512080512 | 24016831315072 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2865125 | 85896477680 |
| averageLatency | 2865131 | 114382025 |
| maxLatency | 2865136 | 85896477691 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |

# Port Statistics - 256

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,1** | **Port 1,1,3** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:00:30.387014023 | 00:00:30.391203544 |
| framesSent | 11009787688 | 11011305631 |
| framesReceived | 11009787688 | 11011305631 |
| fragments | 0 | 0 |
| undersize | 0 | 0 |
| oversizeAndCrcErrors | 0 | 0 |
| vlanTaggedFramesRx | 0 | 0 |
| flowControlFrames | 0 | 0 |
| bitsSent | 22548045185024 | 22551153932288 |
| bitsReceived | 22551153932288 | 22548045185024 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 148 | 382 |
| fecMaxSymbolErrors | 1 | 1 |
| fecCorrectedCodewords | 148 | 382 |
| fecTotalCodewords | 5196832320 | 5196196472 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 5.235090e-12 | 1.351384e-11 |
| fecMaxSymbolErrorsBin0 | 5196832172 | 5196196090 |
| fecMaxSymbolErrorsBin1 | 148 | 382 |
| fecMaxSymbolErrorsBin2 | 0 | 0 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 24309611215104 | 24312962833248 |
| l1BitsReceived | 24312962833248 | 24309611215104 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2865125 | 85896477680 |
| averageLatency | 2865131 | 71261664 |
| maxLatency | 2865136 | 85896477692 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |

# Port Statistics - 512

|  |  |  |
| --- | --- | --- |
| **Port Statistics** | **Port 1,1,1** | **Port 1,1,3** |
| link | link up | link up |
| lineSpeed | 800000 | 800000 |
| transmitDuration | 00:00:30.289531790 | 00:00:30.294925630 |
| framesSent | 5693521012 | 5694534892 |
| framesReceived | 5693521012 | 5694534892 |
| fragments | 0 | 0 |
| undersize | 0 | 0 |
| oversizeAndCrcErrors | 0 | 0 |
| vlanTaggedFramesRx | 0 | 0 |
| flowControlFrames | 0 | 0 |
| bitsSent | 23320662065152 | 23324814917632 |
| bitsReceived | 23324814917632 | 23320662065152 |
| pcsSyncErrorsReceived | 0 | 0 |
| pcsRemoteFaultsReceived | 0 | 0 |
| pcsLocalFaultsReceived | 0 | 0 |
| fecTotalBitErrors | 150 | 350 |
| fecMaxSymbolErrors | 1 | 1 |
| fecCorrectedCodewords | 150 | 350 |
| fecTotalCodewords | 5182048768 | 5181675408 |
| fecFrameLossRatio | 0.000000e+00 | 0.000000e+00 |
| preFecBer | 5.320971e-12 | 1.241649e-11 |
| fecMaxSymbolErrorsBin0 | 5182048618 | 5181675058 |
| fecMaxSymbolErrorsBin1 | 150 | 350 |
| fecMaxSymbolErrorsBin2 | 0 | 0 |
| fecMaxSymbolErrorsBin3 | 0 | 0 |
| fecMaxSymbolErrorsBin4 | 0 | 0 |
| fecMaxSymbolErrorsBin5 | 0 | 0 |
| fecMaxSymbolErrorsBin6 | 0 | 0 |
| fecMaxSymbolErrorsBin7 | 0 | 0 |
| fecMaxSymbolErrorsBin8 | 0 | 0 |
| fecMaxSymbolErrorsBin9 | 0 | 0 |
| fecMaxSymbolErrorsBin10 | 0 | 0 |
| fecMaxSymbolErrorsBin11 | 0 | 0 |
| fecMaxSymbolErrorsBin12 | 0 | 0 |
| fecMaxSymbolErrorsBin13 | 0 | 0 |
| fecMaxSymbolErrorsBin14 | 0 | 0 |
| fecMaxSymbolErrorsBin15 | 0 | 0 |
| fecUncorrectableCodewords | 0 | 0 |
| fecTranscodingUncorrectableErrors | 0 | 0 |
| l1BitsSent | 24231625427072 | 24235940500352 |
| l1BitsReceived | 24235940500352 | 24231625427072 |
| transceiverTemp | 0 | 0 |
| encoding | PAM4 106G | PAM4 106G |
| fecStatus | KP4-FEC | KP4-FEC |
| transceiverVoltage | 0 | 0 |
| minLatency | 2865125 | 85896477680 |
| averageLatency | 2865131 | 288849588 |
| maxLatency | 2865136 | 85896477692 |
| Loss\_Frames | 0 | 0 |
| Loss% | 0.0 | 0.0 |