|  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Gate no | Propagation delay | | Average Power | | | | Power delay Product | Cell area | No. transistors | No. of DRC | | No. LVS Mismatches | |
| NAND | 143.8E-12 | | 107.7E-9 | | | | 1.587E-17 | 15.125 micrometer | 6 | 0 | | 0 | |
| AND | 165.1E-12 | | 188.0E-9 | | | | 3.103E-17 | 20.135micrometer | 8 | 0 | | 0 | |
| NOR | 40.83E-12 | | | 155.3E-9 | | | 6.341E-18 | 31.014  micrometer | 6 | 0 | | 0 |
| OR | 56.16E-12 | | | 280.4E-9 | | | 1.574E-17 | 36.636  micrometer | 8 | 0 | | 0 |
| xor | |  | | |  |  | |  |  | |  |  |
| xnor | |  | | |  |  | | 33.78 micrometer | 14 | | 0 | 0 |