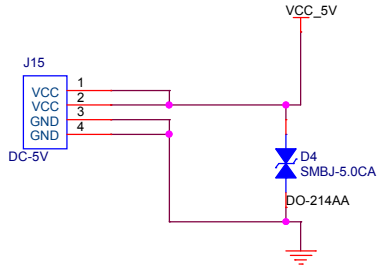


LED Panel Driver Card

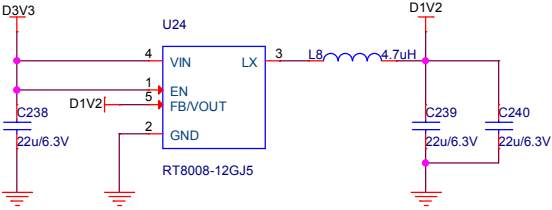
REV	DATE	PAGES	DESCRIPTION
A1	FEB,2015	All	INITIAL REVISION A RELEASE

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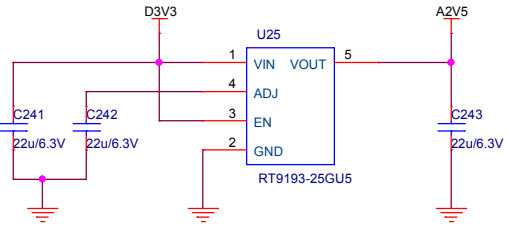
Power



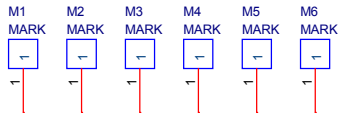
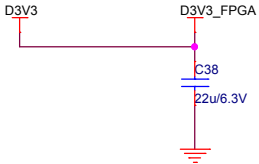
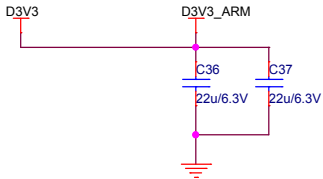
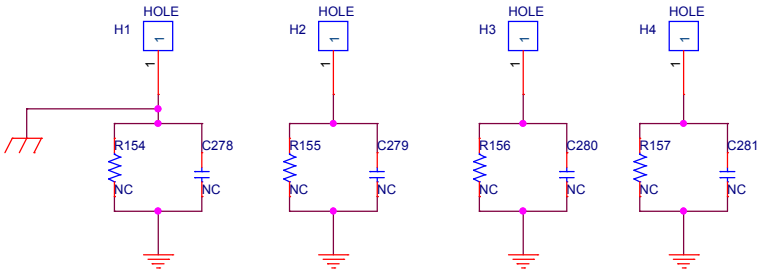
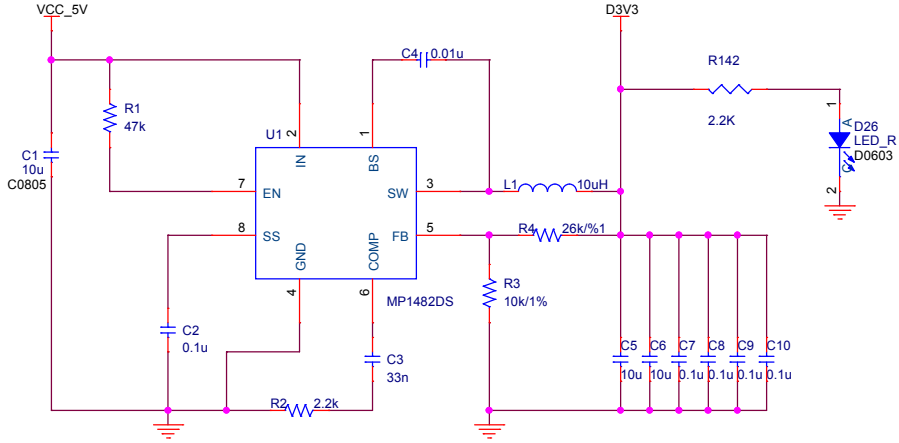
+1.2 V 600mA Power



+2.5 V 300mA LDO

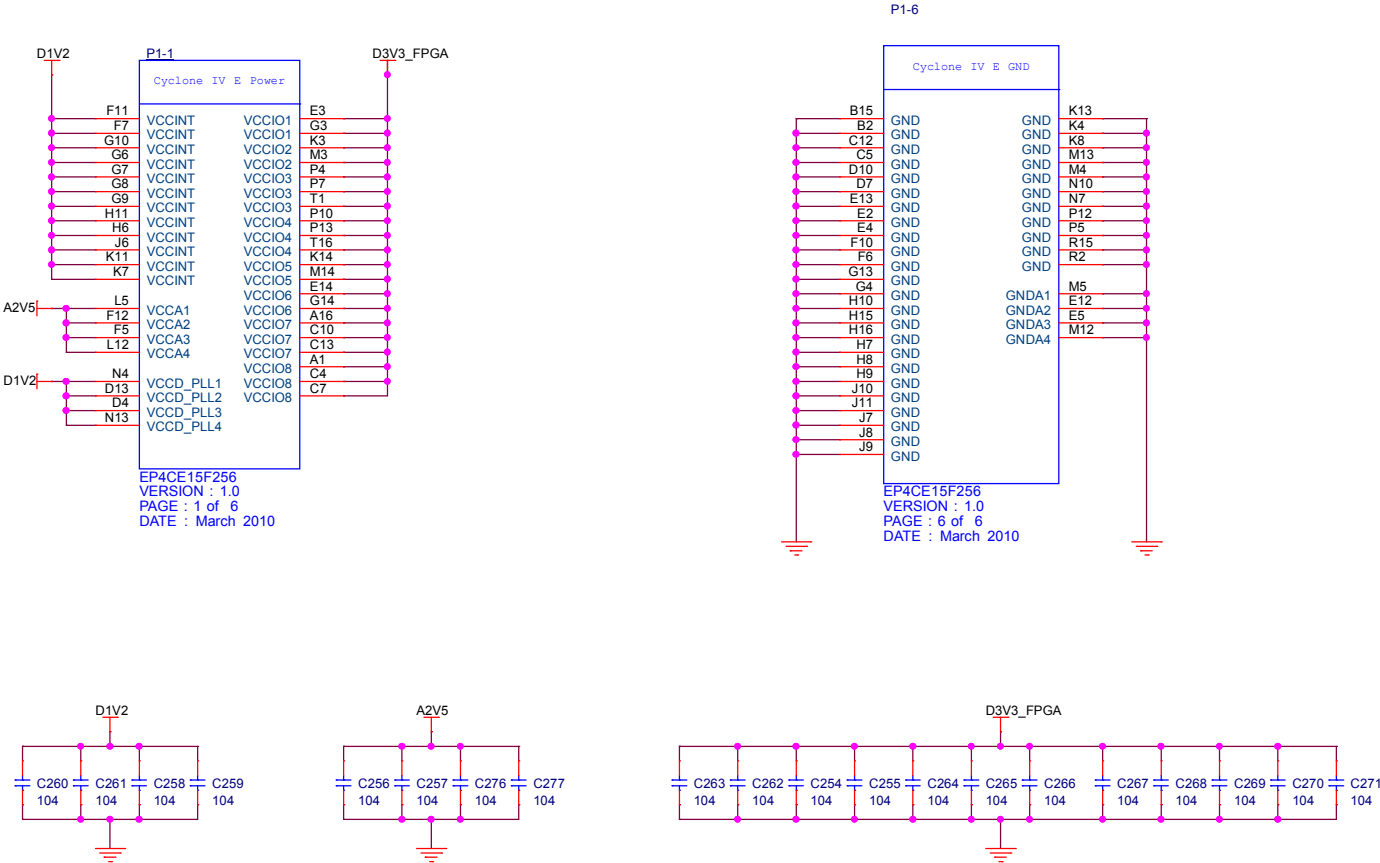


+3.3 V 2A Power

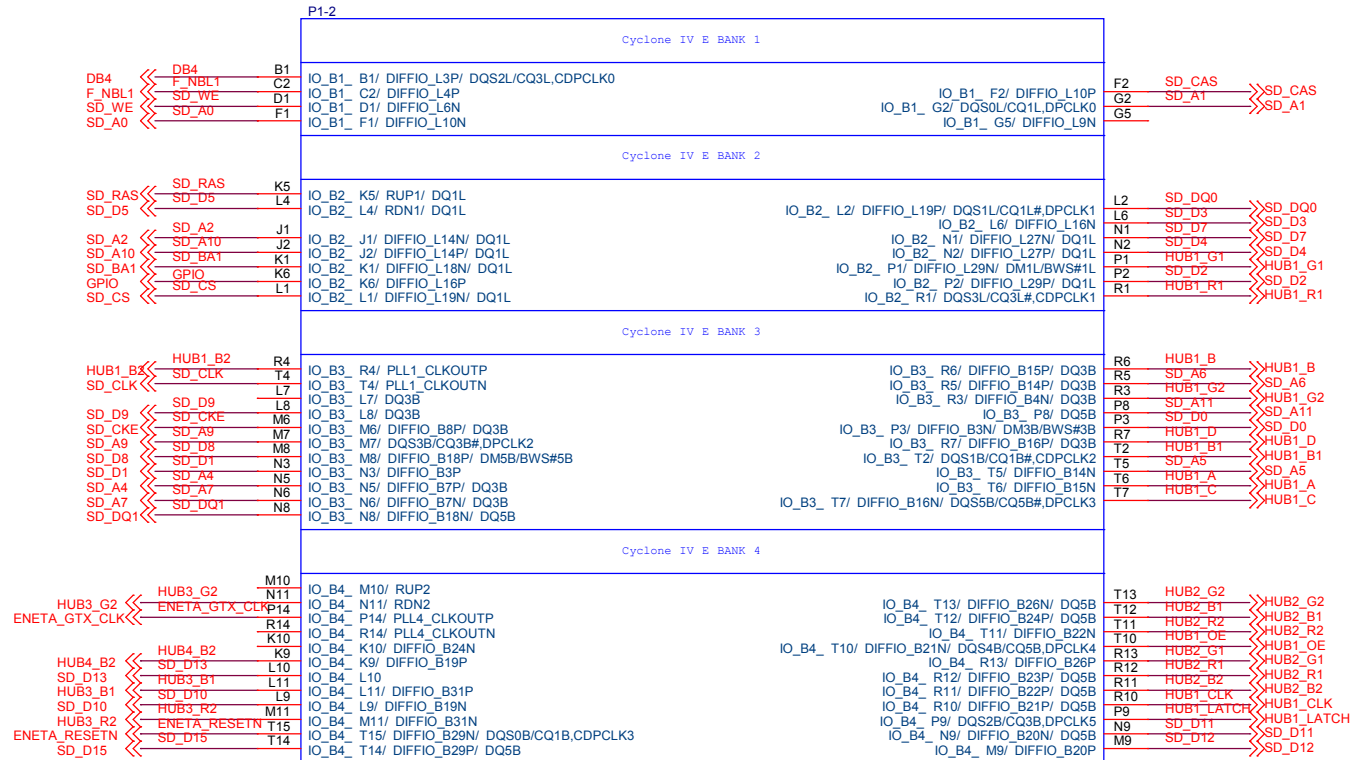


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FPGA Power & GND

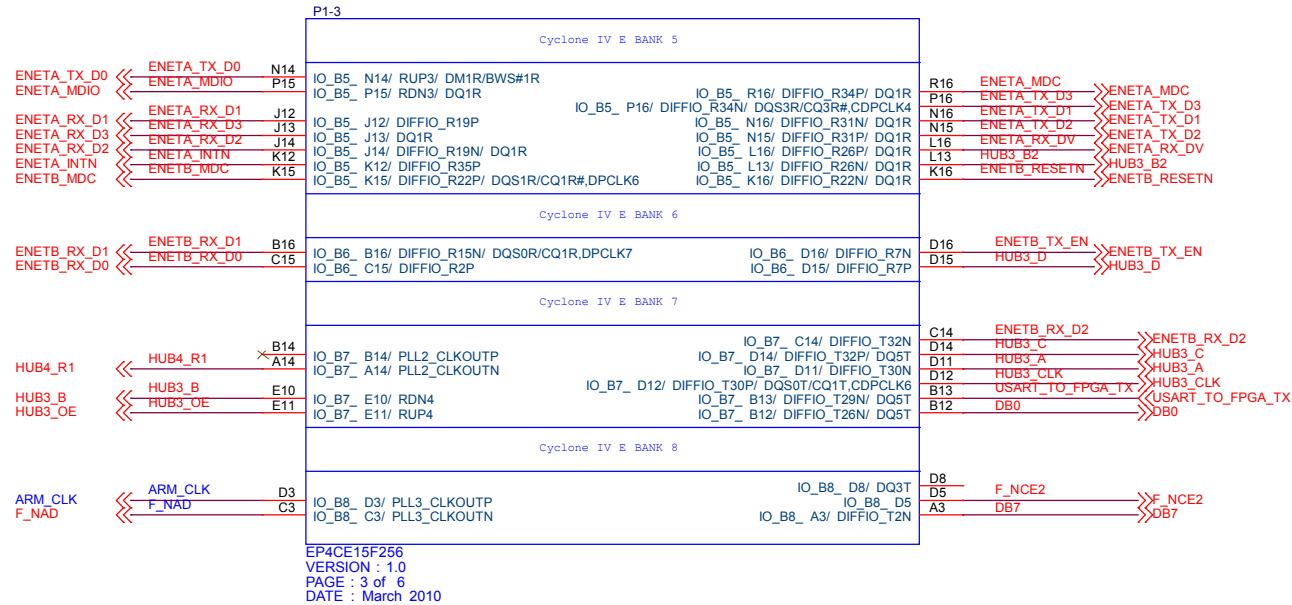


# FPGA BANK 1, 2 , 3, 4



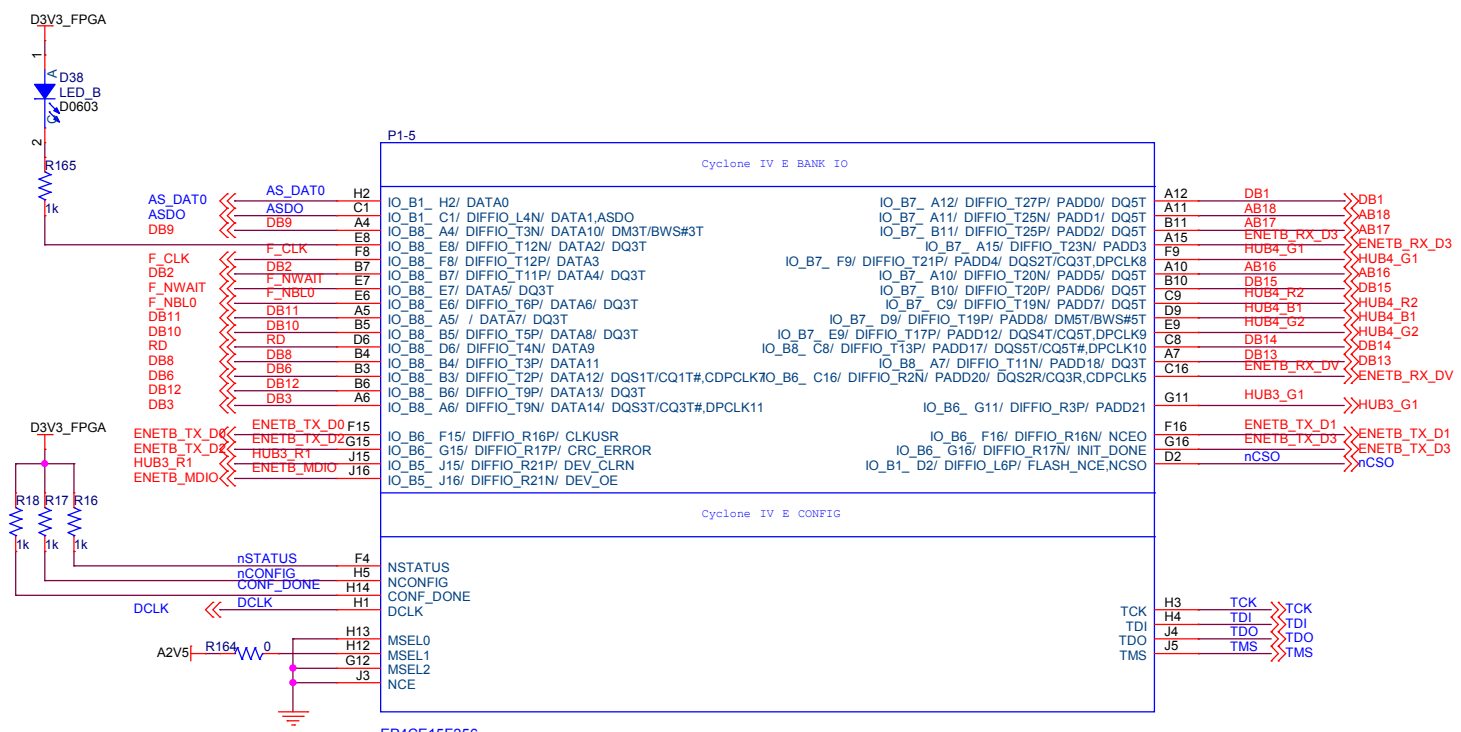
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# FPGA BANK 5, 6 , 7, 8



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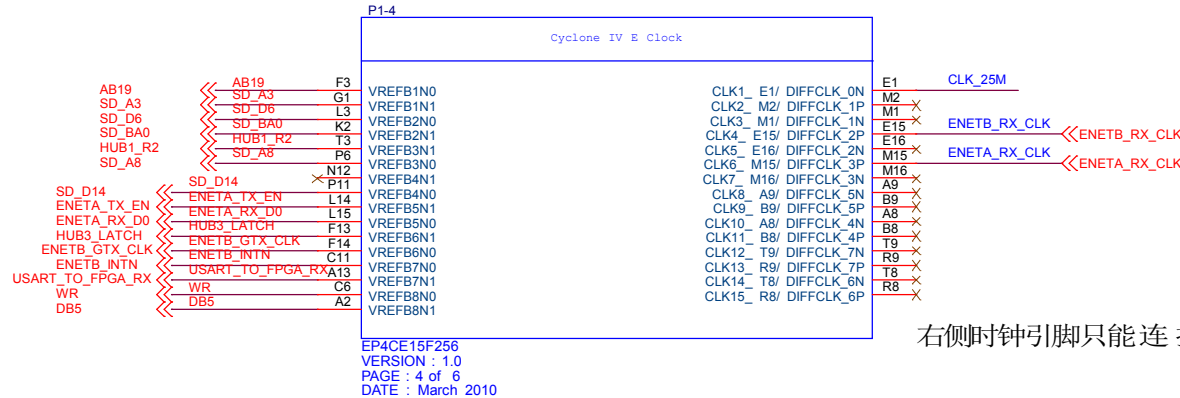
FPGA BANK IO & CONFIG



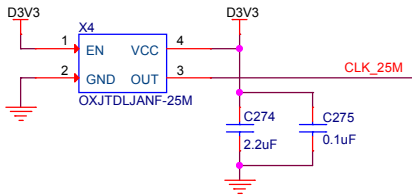
EP4CE15F256  
VERSION : 1.0  
PAGE : 5 of 6  
DATE : March 2010

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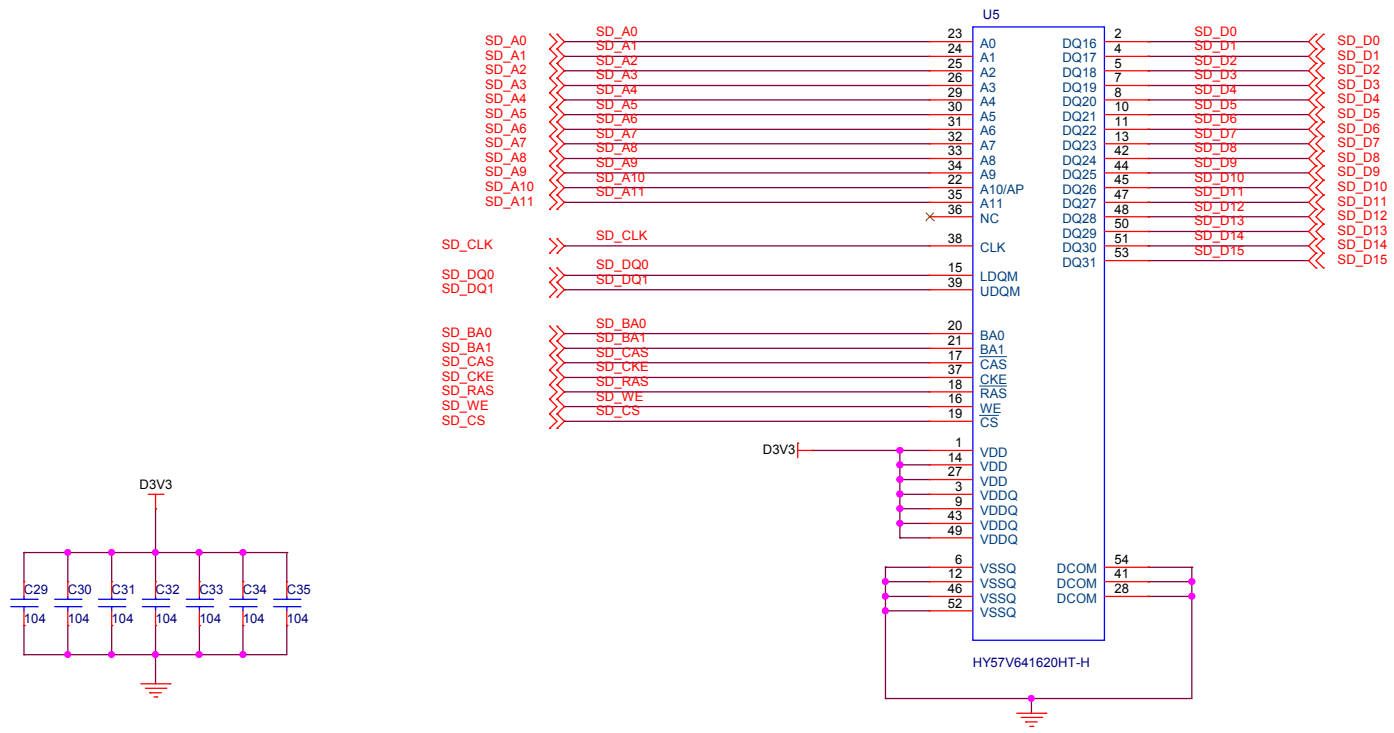
FPGA Clock



25MHz Logic Clock



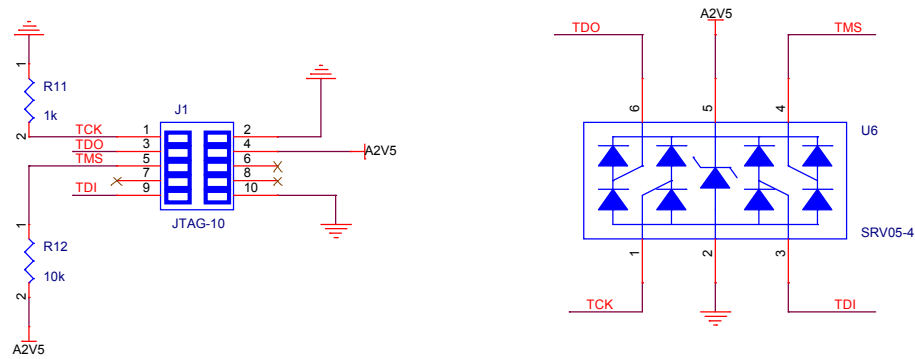
SDRAM





JTAG & EPCS

FPGA JATG



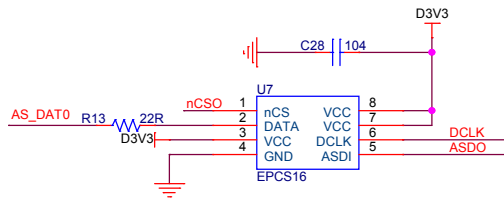
FPGA JTAG



AS CONFIG



AS\_CONFIG



Title LED Panel Driver Card

Size B Document Number NV-EP4CE15-A

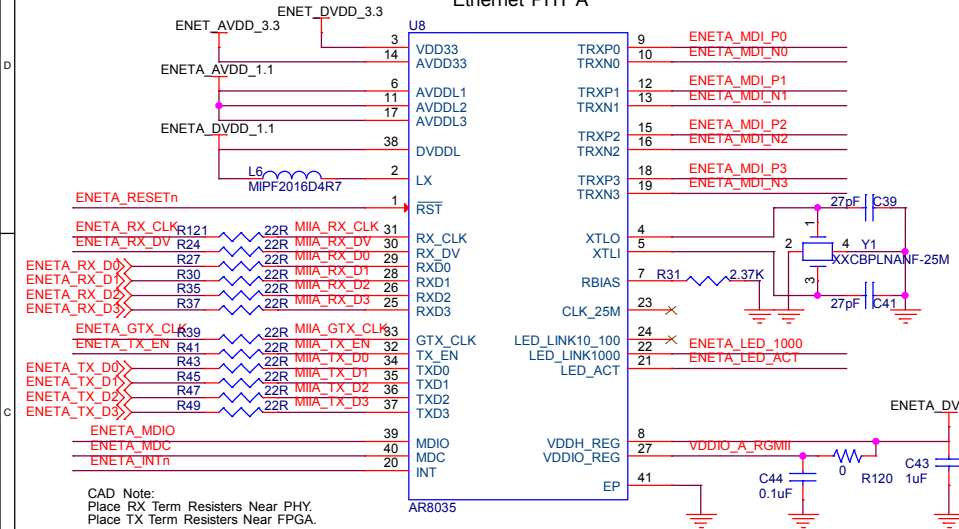
Rev A

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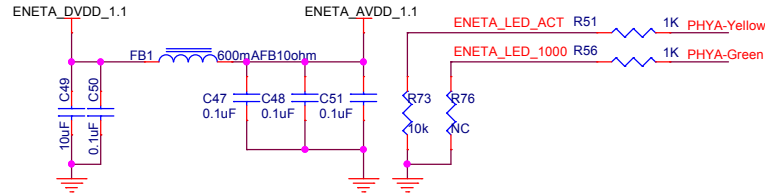
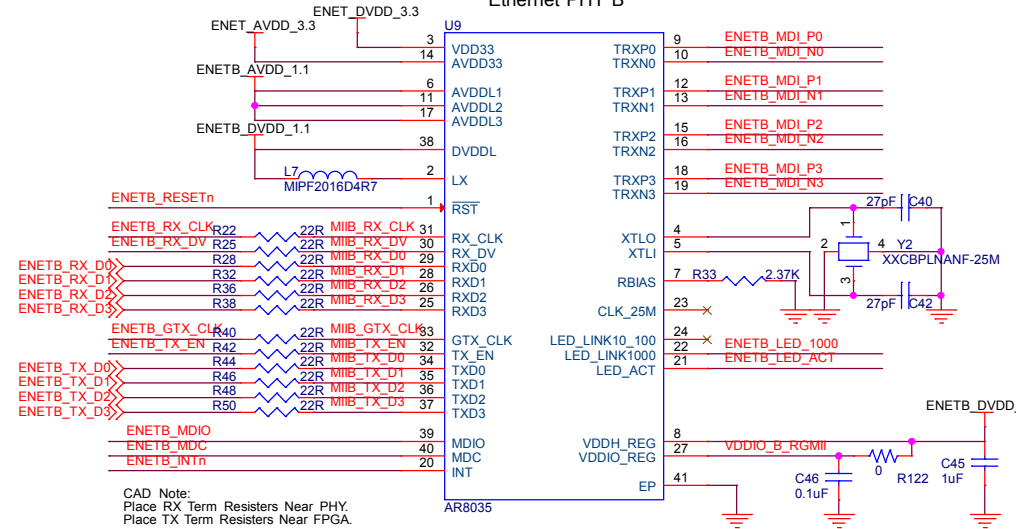
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# 10/100/1000 Ethernet PHY

## Ethernet PHY A

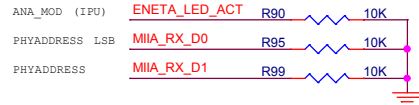


## Ethernet PHY B



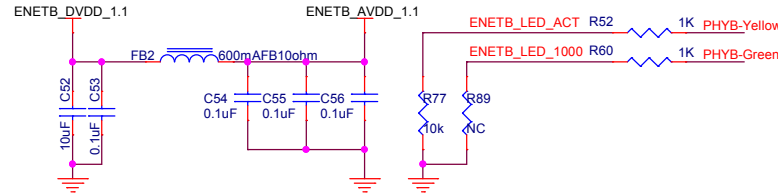
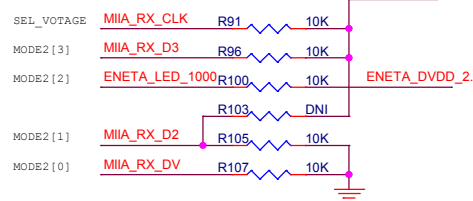
### PHY Address

PHYADDRESS = 00000

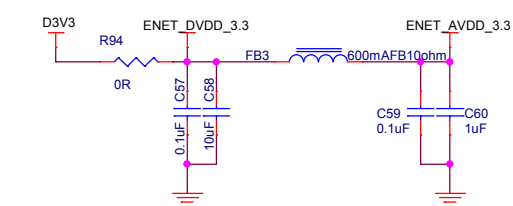


### Bootstrapping MODE SELECTION

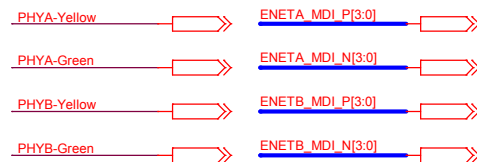
Mode "1100" = 1000 BASE-T, RGMII



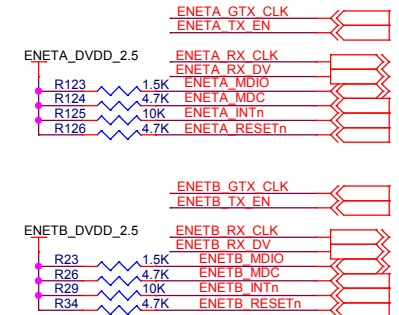
### 3.3V POWER INPUT



### RJ45 Interface



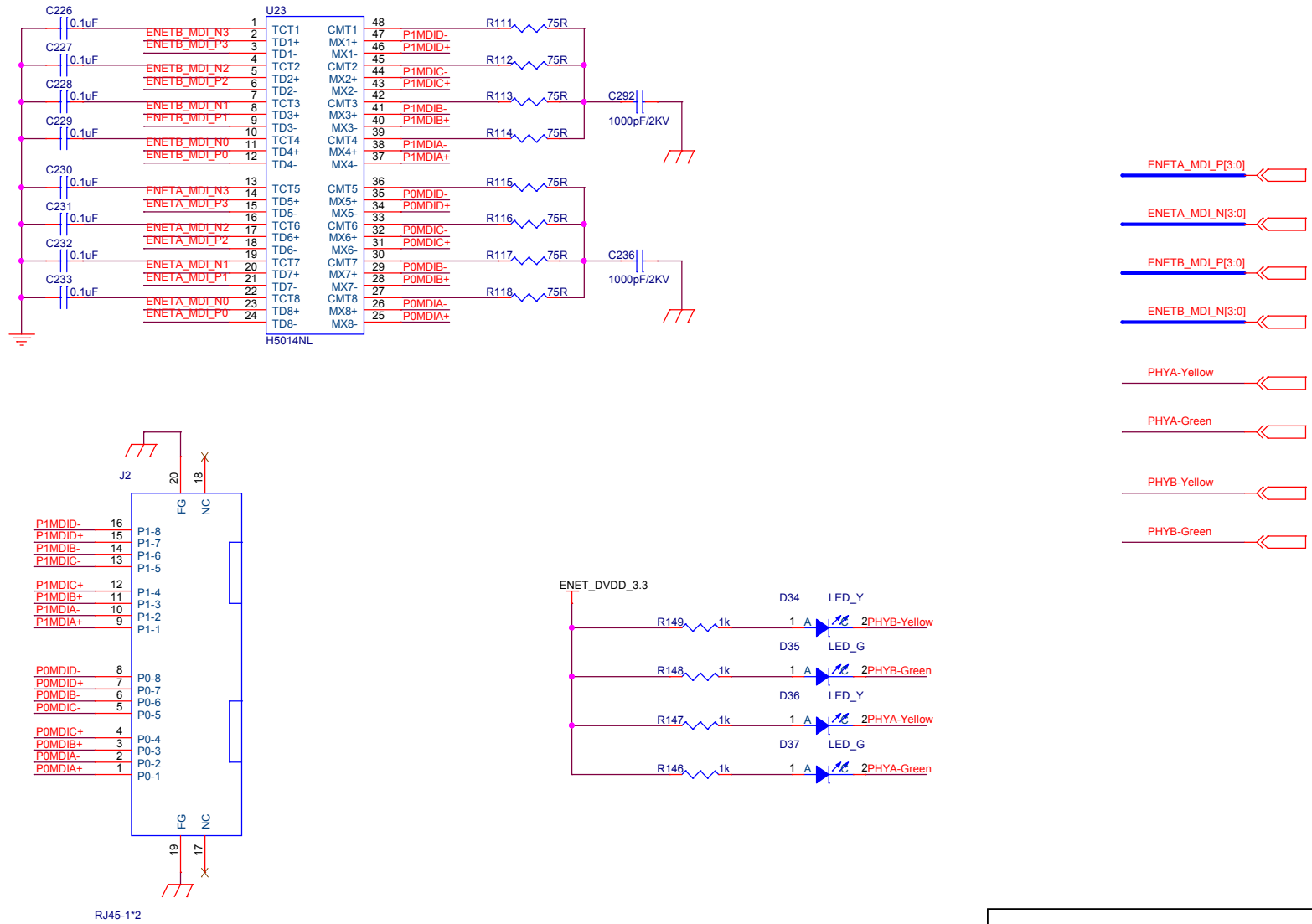
### FPGA Interface



### LED Panel Driver Card

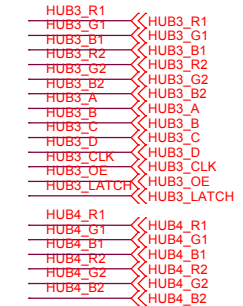
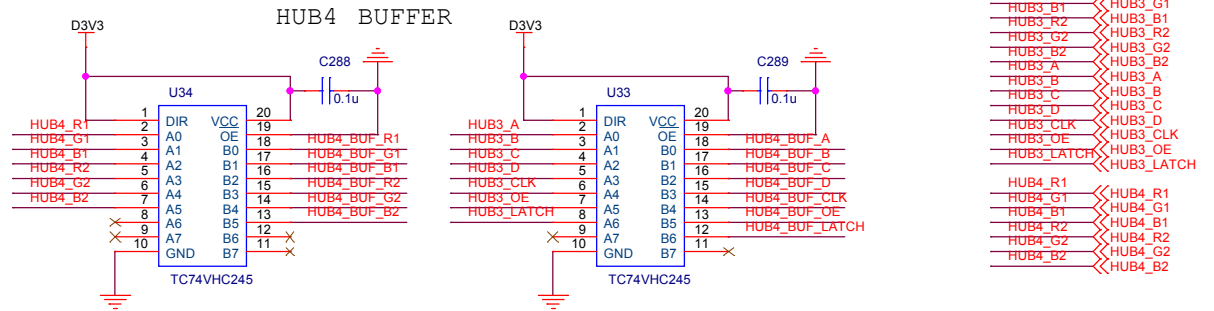
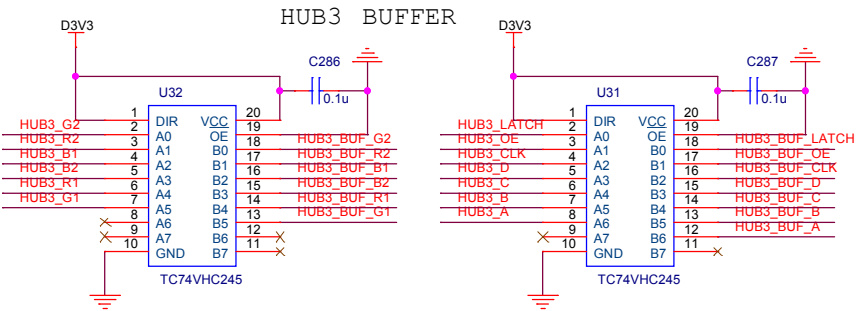
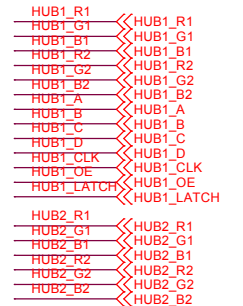
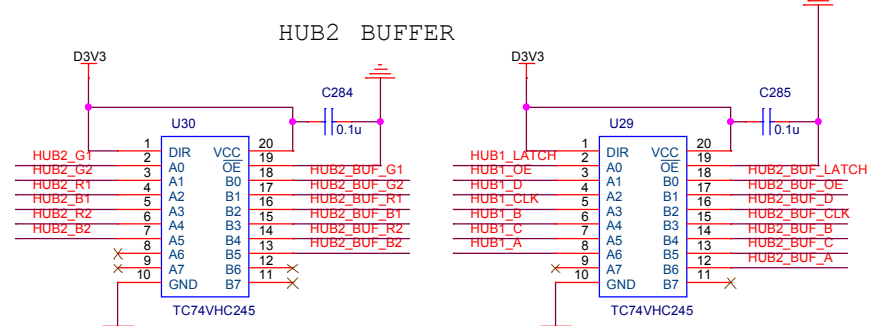
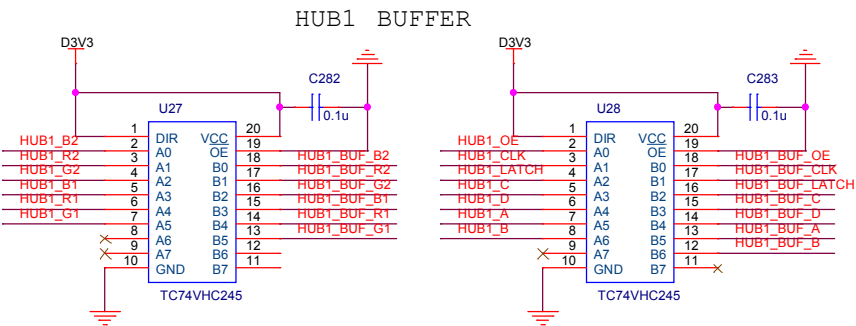
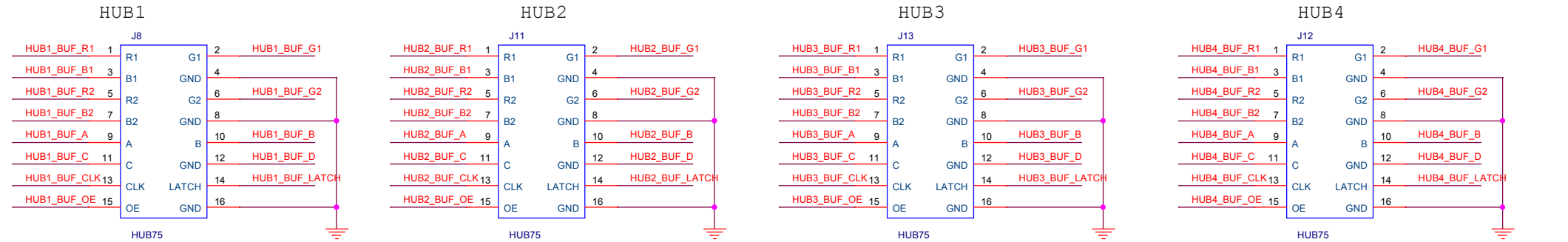
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## RJ45 1\*2



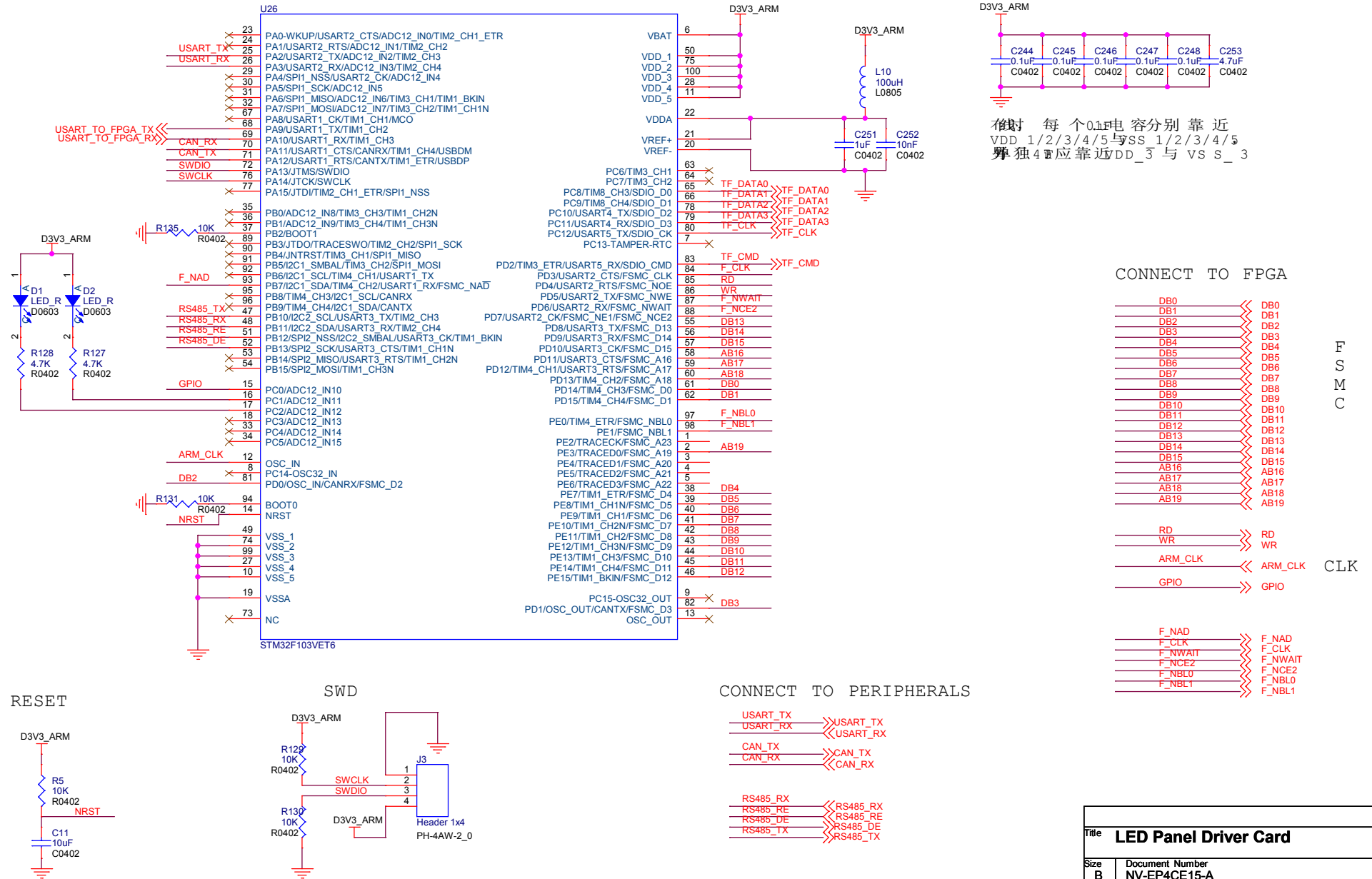
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<b>LED Panel Driver Card</b>			
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# HUB75



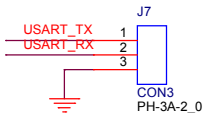
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# STM32

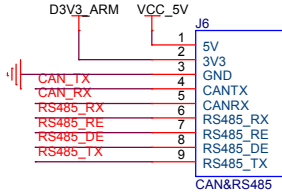


STM32 Peripherals

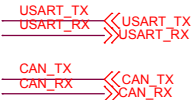
USART



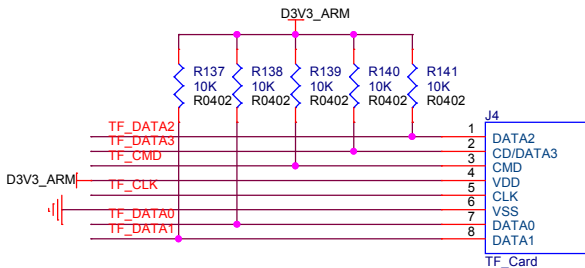
CAN & RS485 Connector



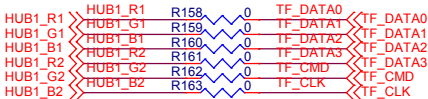
STM32 IO



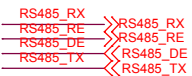
TF-Card



0 欧靠近TF卡槽



电阻左侧引脚可换成其他UB引脚



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