LED Panel Driver Card (Ethenet NC)

REV	DATE	PAGES	DESCRIPTION
A1	MAR,2017	All	INITIAL REVISION A RELEASE
A2	FEB,2016	08 10 11	DELETE COMPONENT RELATED WITH ETHENET

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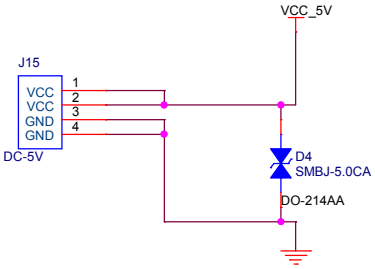
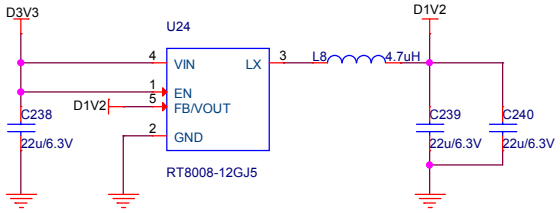
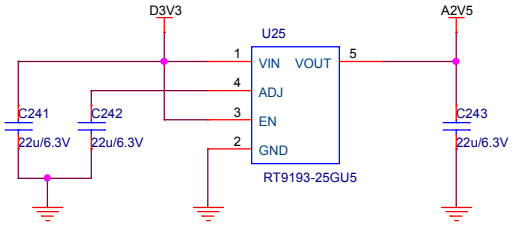
Power



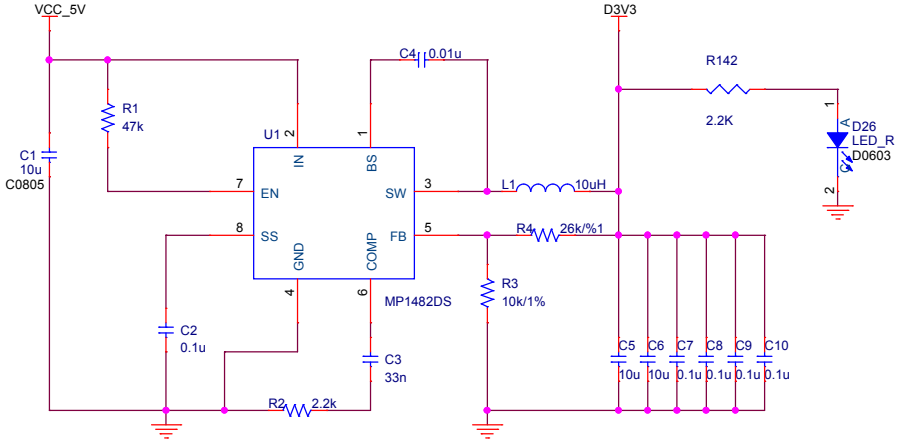
+1.2 V 600mA Power

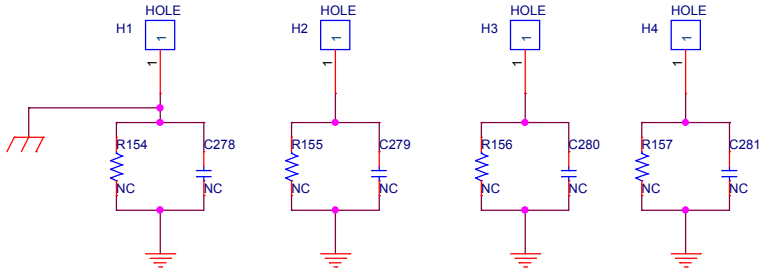


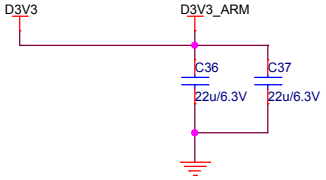
+2.5 V 300mA LDO

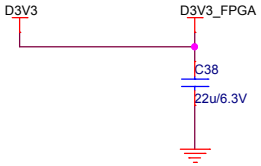


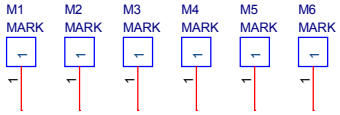
+3.3 V 2A Power











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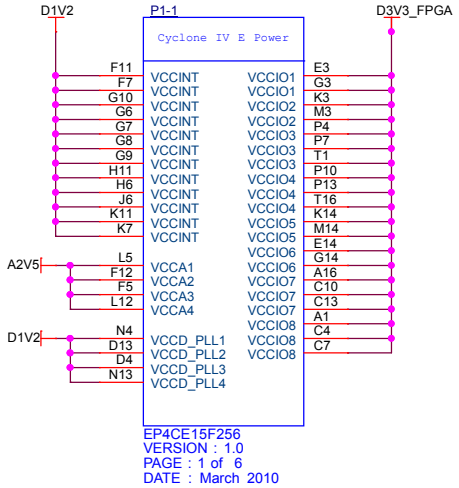
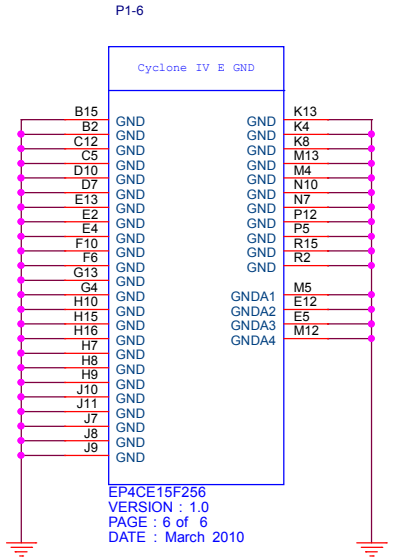
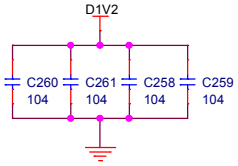
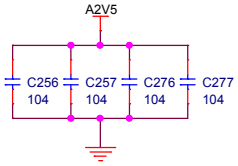
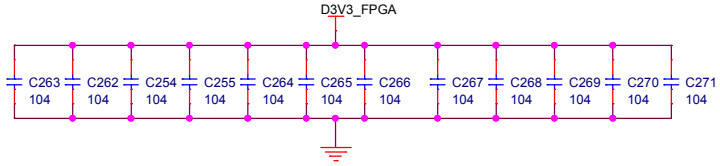
FPGA Power & GND











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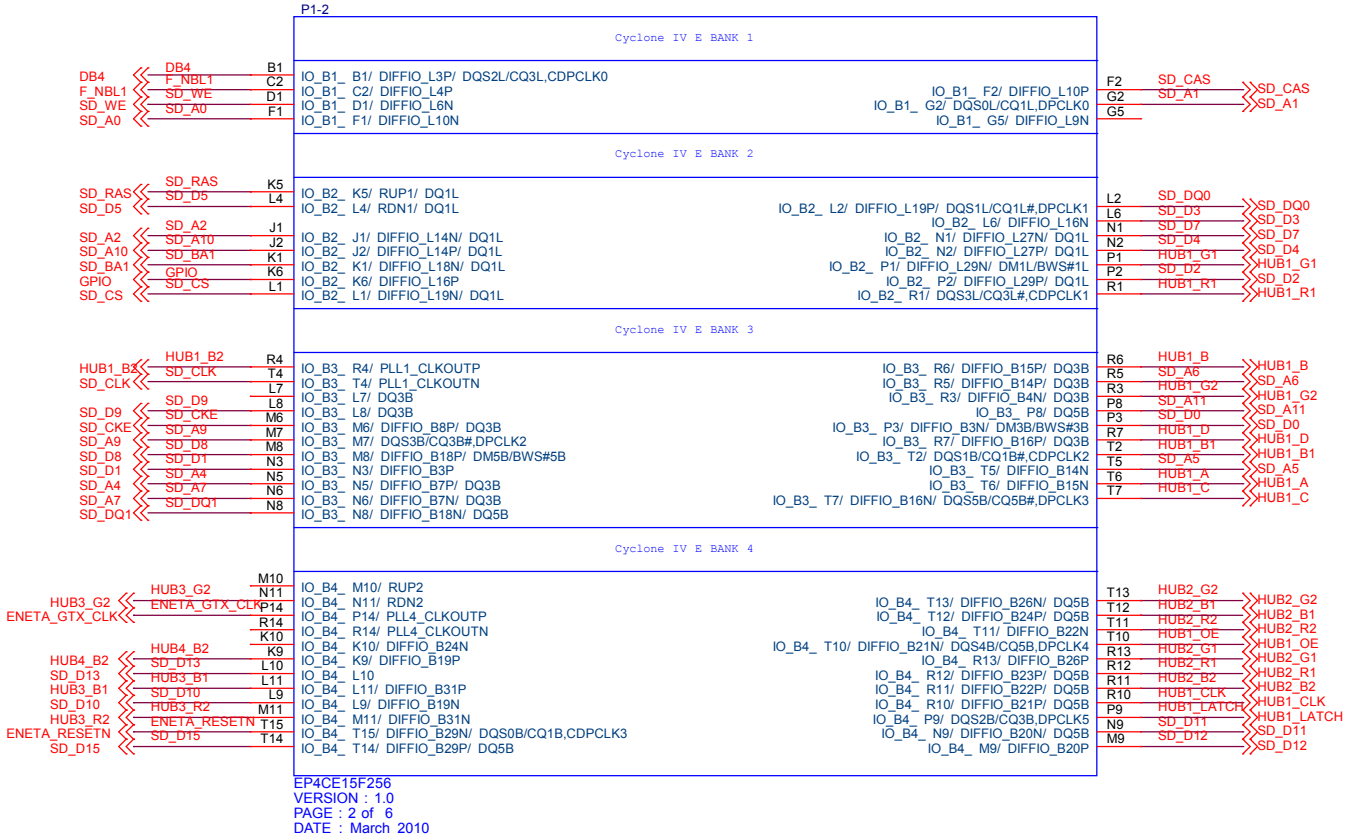
FPGA BANK 1, 2 , 3, 4



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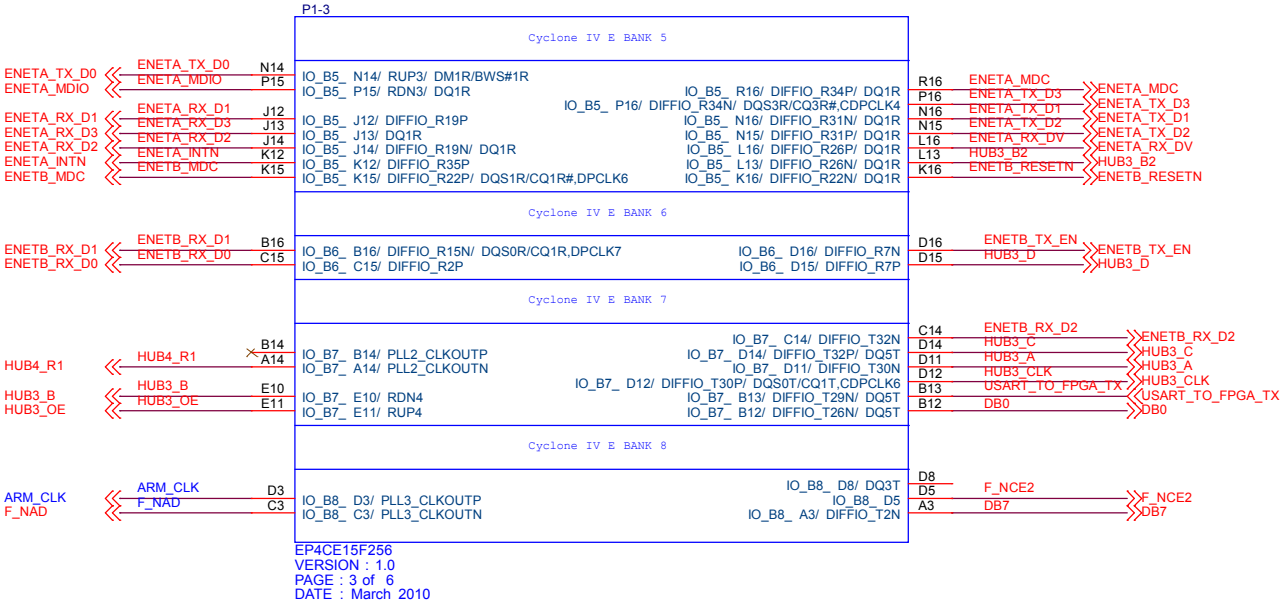
FPGA BANK 5, 6 , 7, 8



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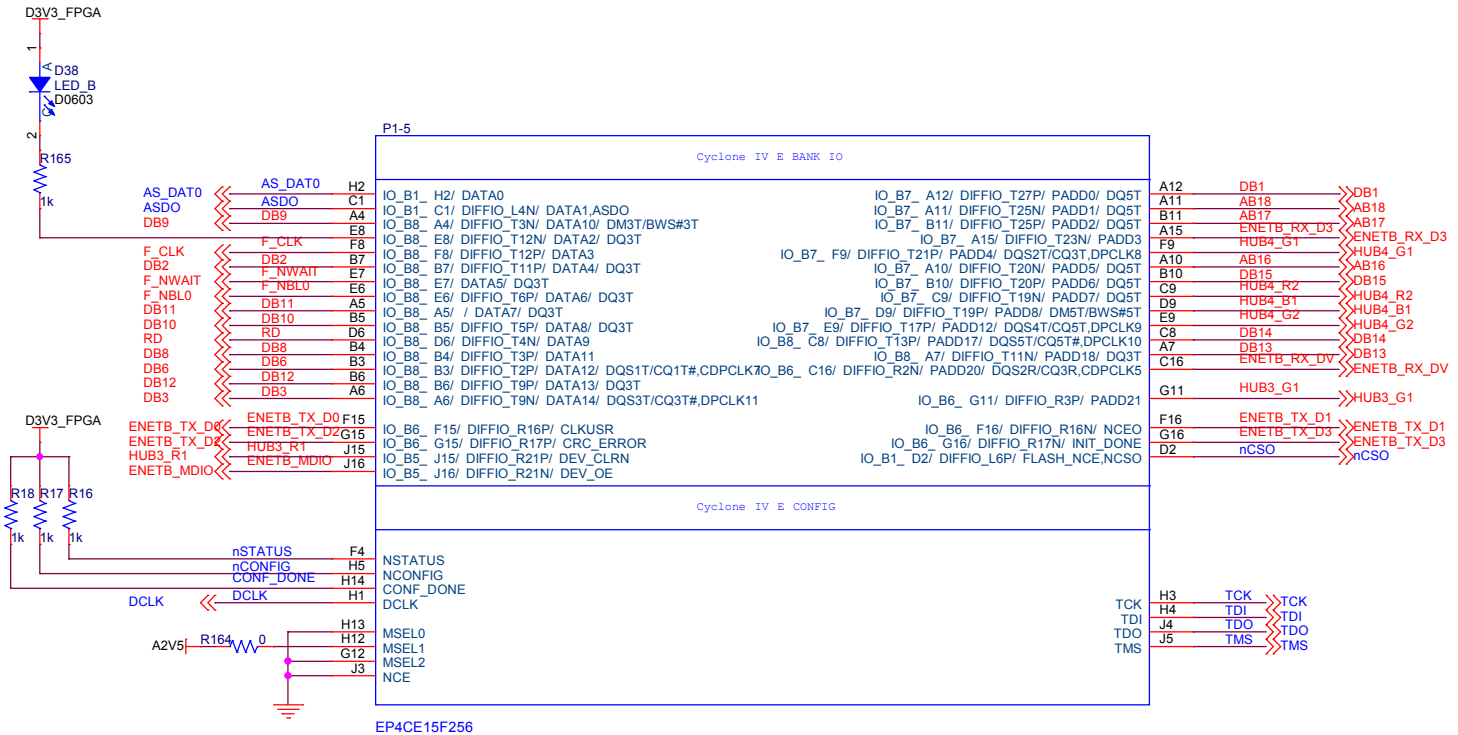
FPGA BANK IO & CONFIG



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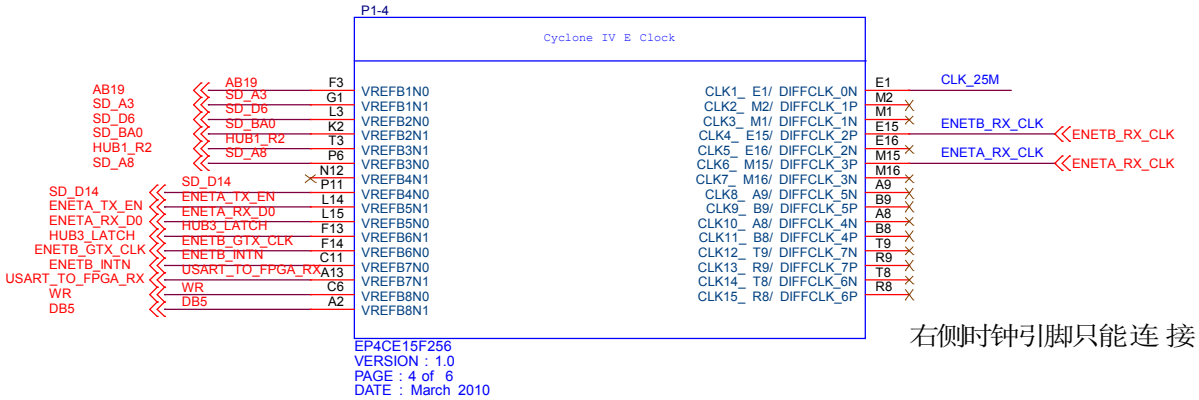
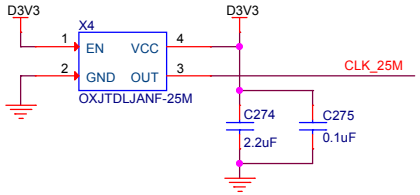
FPGA Clock



右侧时钟引脚只能连 接 输入 信 号

25MHz Logic Clock



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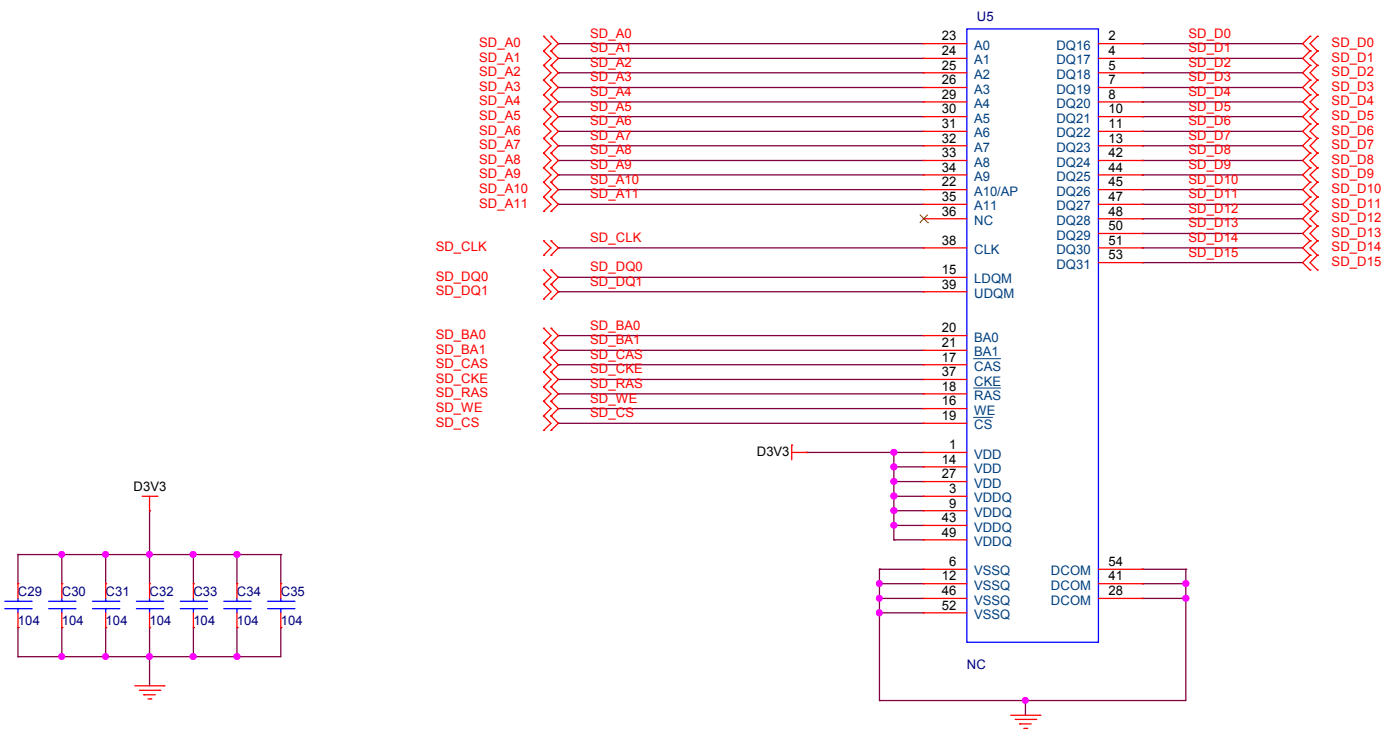
SDRAM



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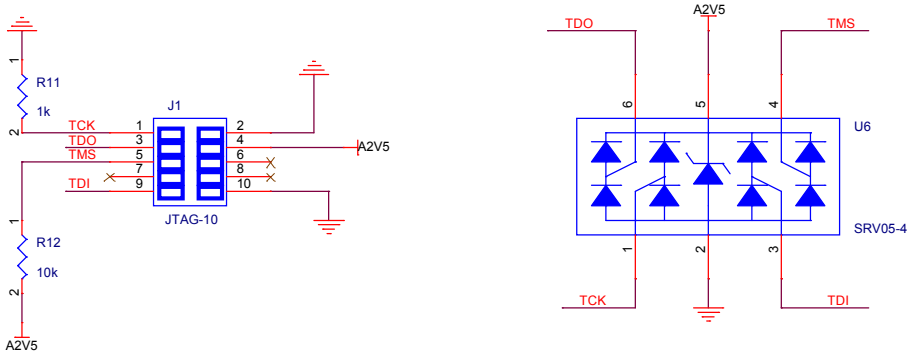
JTAG & EPCS

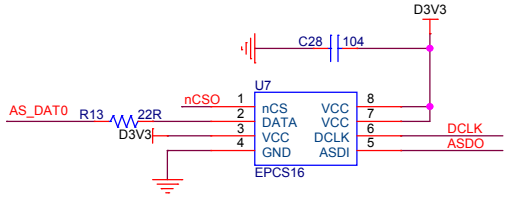
FPGA JATG



FPGA JTAG

AS CONFIG

AS_CONFIG



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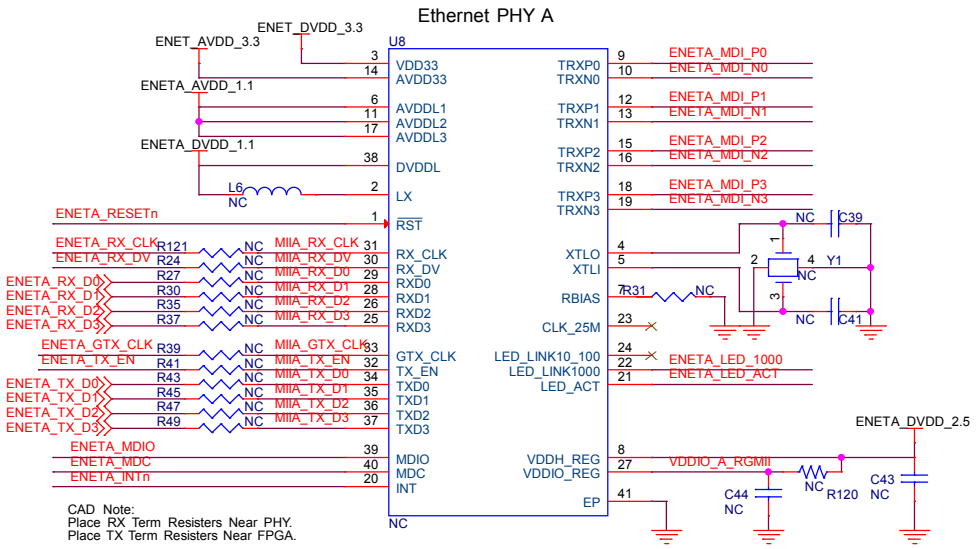
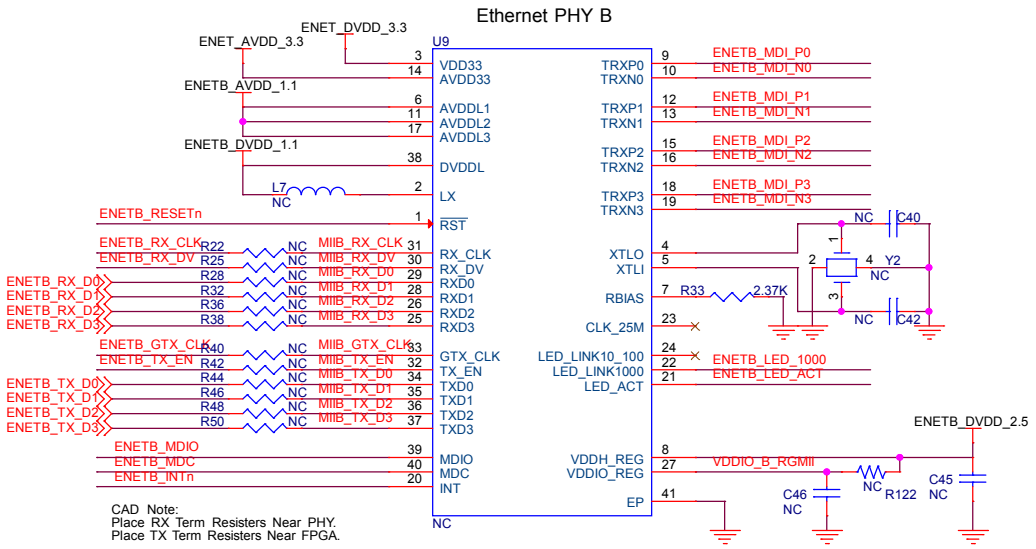
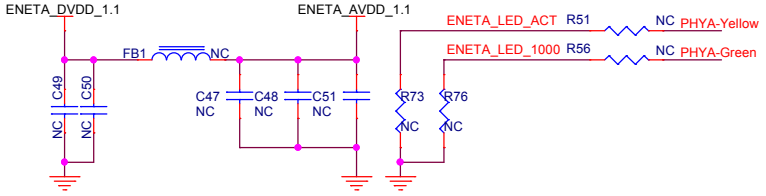
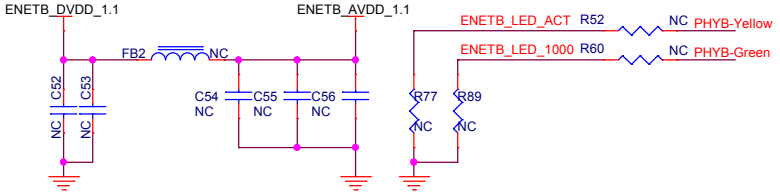
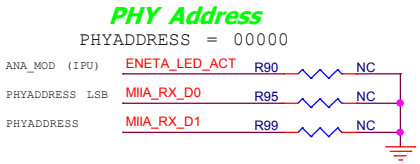
10/100/1000 Ethernet PHY

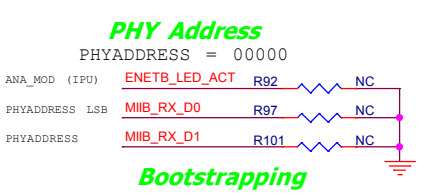


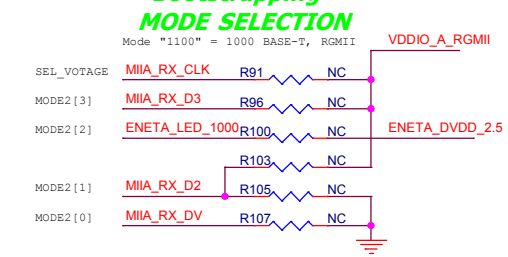


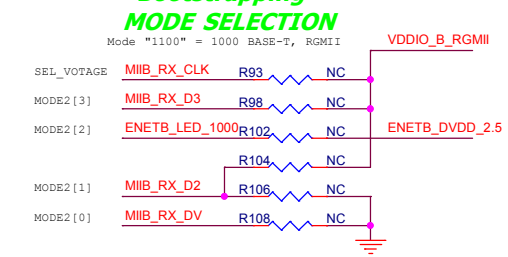


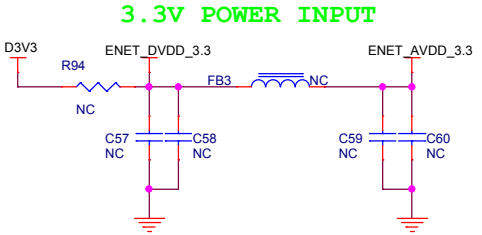


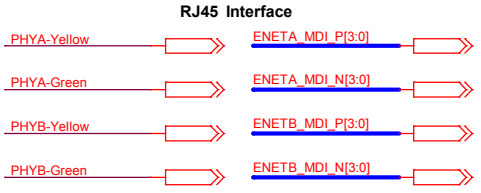


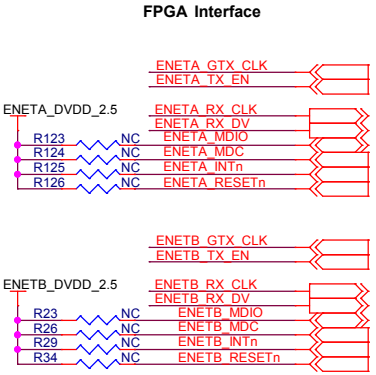












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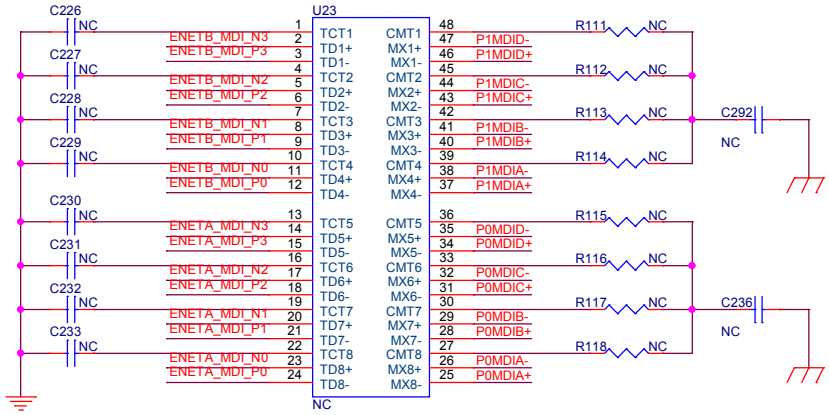
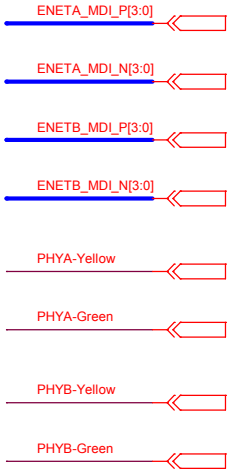
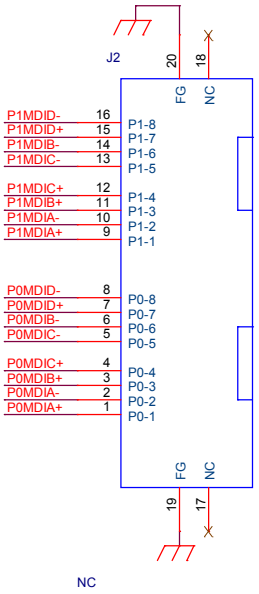
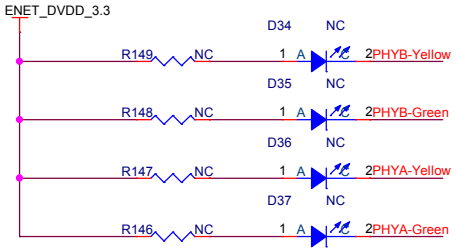
RJ45 1*2









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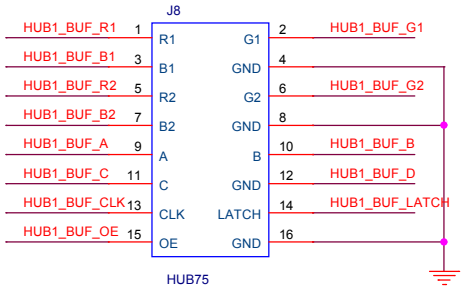
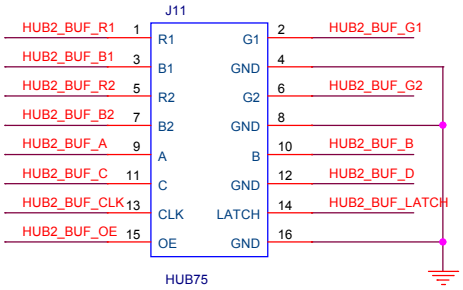
HUB75

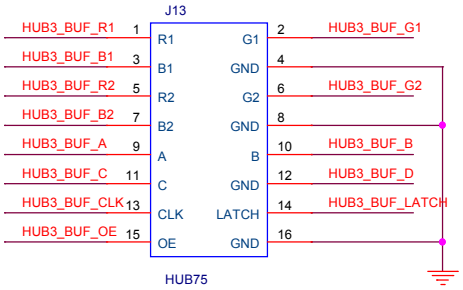
HUB1



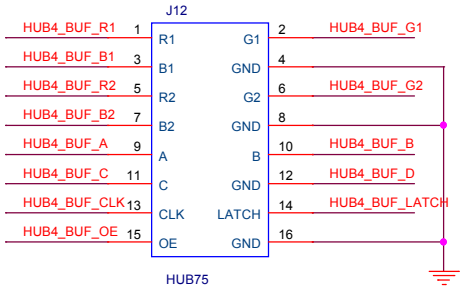
HUB2



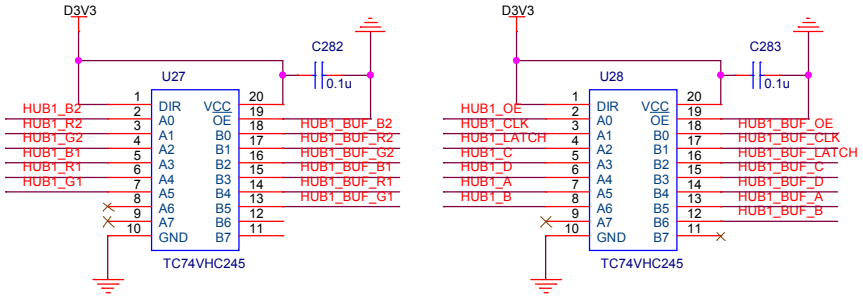
HUB3



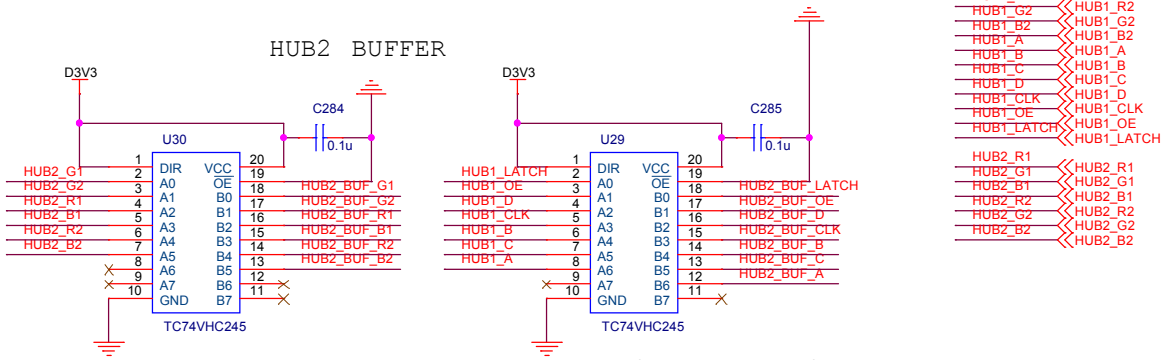
HUB4



HUB1 BUFFER

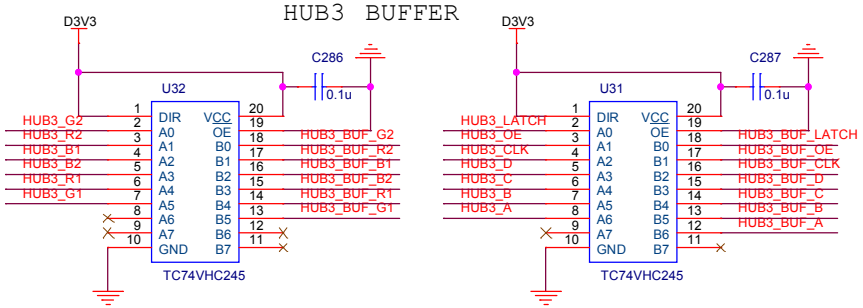


HUB2 BUFFER

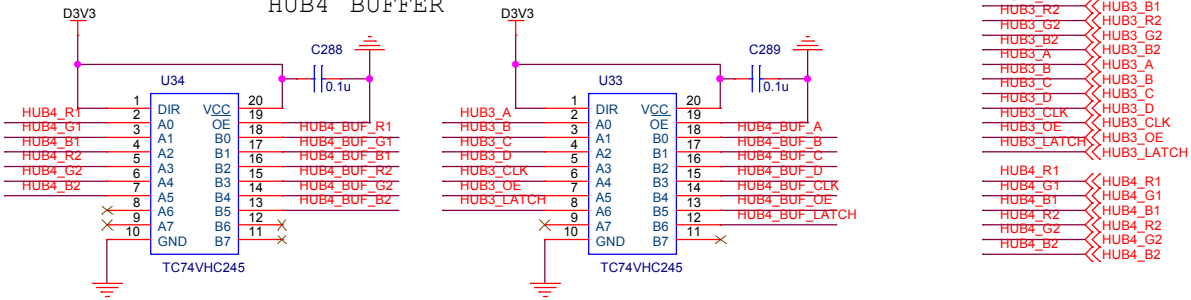


hub2 input control signal same as hub1

HUB3 BUFFER



HUB4 BUFFER



hub4 input control signal same as hub3

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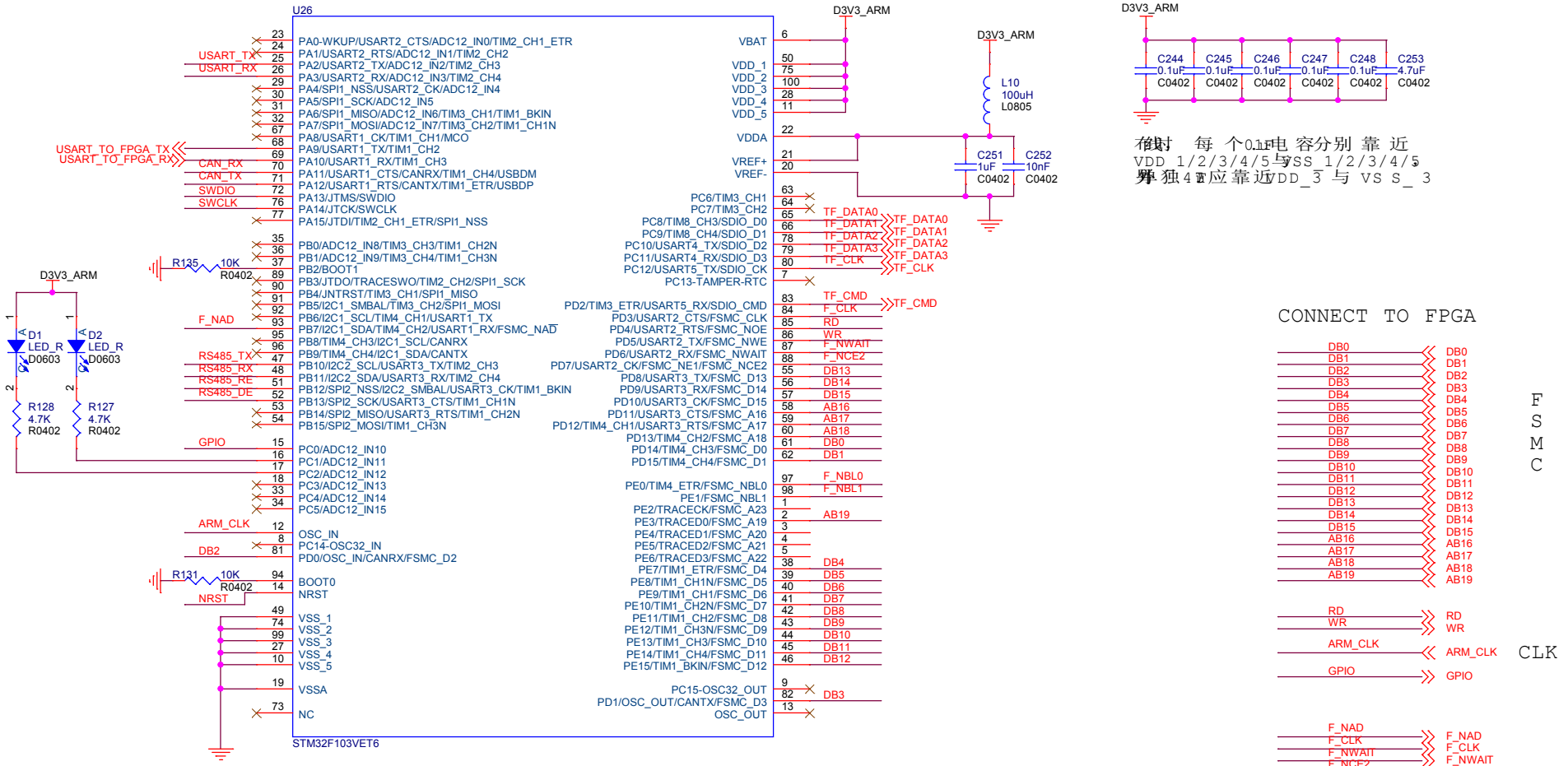
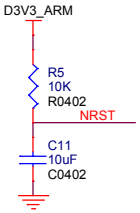
STM32



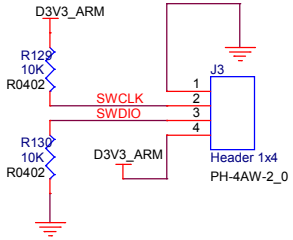
在PCB设计时，每个0.1uF电容分别靠近VDD_1/2/3/4/5与VSS_1/2/3/4/5引脚，4.7uF电容应靠近VDD_3与VSS_3

CONNECT TO FPGA

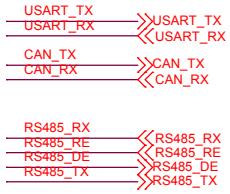
RESET



SWD



CONNECT TO PERIPHERALS



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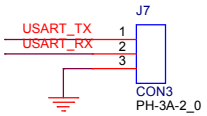
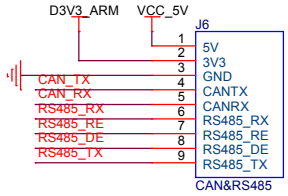
STM32 Peripherals

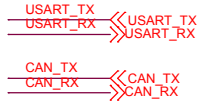
USART



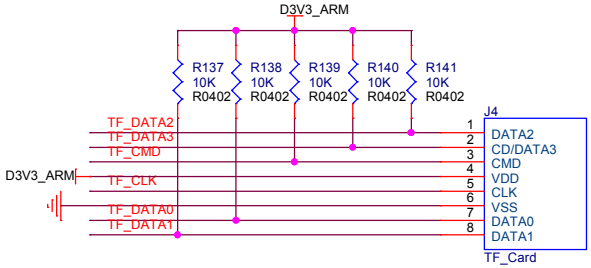
CAN & RS485 Connector



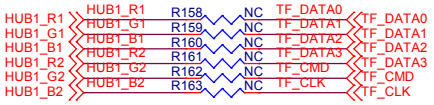
STM32 IO



TF-Card



0 欧靠近 TF 卡槽



电阻旁侧脚可 换成其他 UB 引 脚

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