

ISVLSI 2019 Program Outline

14 July (Sunday)	5:00 PM	-	6:00 PM	Onsite Registration	
	6:00 PM	-	8:00 PM	Welcome Reception/Cocktail WUF Panther Square	
15 July 2019 (Monday)	8:00 AM	-	8:30 AM	Registration & Refreshment (Ballroom Foyer)	
	8:30 AM	-	9:00 AM	Opening Remarks (Ballroom B)	
	9:00 AM	-	9:45 AM	Keynote # 1 (Ballroom B)	
	9:45 AM	-	10:00 AM	Coffee Break	Coffee Break
	10:00 AM	-	11:00 AM	Session 1: Digital Circuits and FPGA based Designs I	Session 02: Special Session: FPGA Accelerator Design and Optimization
	11:00 AM	-	12:00 PM	Session 3: System Design and Security I	Session 04: Special Session: FPGA Accelerator Design and Optimization
	12:00 PM	-	1:15 PM	Lunch and Keynote (Ballroom B)	
	1:15 PM	-	2:35 PM	Session 05: Computer-Aided Design and Verification I	Session 06: Special Session: NVM based Architecture and System
	2:35 PM	-	2:50 PM	Coffee Break	Coffee Break
	2:50 PM	-	4:10 PM	Session 07: Computer-Aided Design and Verification II	Session 08: Circuit, Reliability and Fault Tolerance I
	4:10 PM	-	5:30 PM	Session 9: VLSI for Applied and Future Computing Technology I	Session 10: Special Session: Neuromorphic Computing and Emerging Technologies
	5:30 PM	-	6:00 PM	ISVLSI Committee Meeting (WUC 245C)	
	5:40 PM	-	6:40 PM	Poster / Demo Session & Appetizers (Ballroom A)	
16 July 2019 (Tuesday)	8:30 AM	-	9:00 AM	Refreshment (Ballroom B)	
	9:00 AM	-	9:45 AM	Keynote # 2 (Ballroom B)	
	9:45 AM	-	10:00 AM	Coffee Break	IEEE-CS TCVLSI Meeting (WUC 245C)
	10:00 AM	-	11:00 AM	Session 11: Digital Circuits and FPGA based Designs II	Session 12: Special Session: Explorations in Energy-Efficient Computing for IoT Applications I
	11:00 AM	-	12:00 PM	Session 13: Emerging and Post-CMOS Technologies I	Session 14: Special Session: Explorations in Energy-Efficient Computing for IoT Applications II
	12:00 PM	-	1:00 PM	Lunch and Keynote (Ballroom B)	
	1:00 PM	-	2:00 PM	Session 15: VLSI for Applied and Future Computing Technology II	Session 16: System Design and Security II
	2:00 PM	-	3:20 PM	Session 17: Circuit, Reliability and Fault Tolerance II	Session 18: Special Session: Emerging Technologies and Architectures
	3:20 PM	-	3:40 PM	Coffee Break	Coffee Break
	3:40 PM	-	4:40 PM	Panel Discussion (Ballroom B)	
	4:40 PM	-	11:00 PM	Dinner Cruise and Award Ceremony	
17 July 2019 (Wednesday)	8:30 AM	-	9:00 AM	Refreshment (Ballroom B)	
	9:00 AM	-	9:45 AM	Keynote # 3 (Ballroom B)	
	9:45 AM	-	10:00 AM	Coffee Break	Coffee Break
	10:00 AM	-	11:00 AM	Session 19: System Design and Security III	Session 20: Special Session: Energy-Efficient Machine Learning
	11:00 AM	-	12:20 PM	Session 21: Digital Circuits and FPGA based Designs III	Session 22: Special Session: Botnet of Things: Hardware Insecurity in the IoT Era
	12:20 PM	-	1:00 PM	Lunch	
	1:00 PM	-	2:00 PM	Session 23: Digital Circuits and FPGA based Designs IV	Session 24: Special Session: Secure, Smart, Connected Devices for Emergent Applications
	2:00 PM	-	3:00 PM	Session 25: Emerging and Post-CMOS Technologies II	Session 26: System Design and Security IV
	3:00 PM	-	3:30 PM	Closing Remarks (Ballroom B)	