Lab 2 - Student Work Distribution

- DEC 5x32 Isaac E.
 - Designed the overall schematic for the 5x32DEC
 - Attempted design of generic for 15+ hours, learned a lot
 - Designed the instantiation of the 5x32DEC using 2x4 non-generically
- Testbench Jacob S.
 - Non-txtio simulation (to verify functionality of DEC
 - Txtio simulation testbench to show functionality of the decoder when taking in data stream and outputting results to a .txt
- Generic Decoder- Adrian A.
 - Helped with development of Generic Decoder code.
 - Debugged, reviewed, and modified non-generic 5x32DEC.