Lab 4 - Student Work Distribution

- Isaac E.
  - Implemented overall design for the scaled bcd counters with enable cin's w/ help from teammates in vhdl
  - Helped debugged multiple problems with code
- Jacob S.
  - Seven Seg Controller module, helped with top module and demo implementation
- Adrian A.
  - Revised the components for BCD Counter and Seven Seg Controller
  - Debugged and modified code