

EE 314 DIGITAL CIRCUITS LABORATORY 2022-2023 SPRING TERM PROJECT REPORT

FPGA IMPLEMENTATION OF A 2D STRATEGY GAME

Ahmet Caner Akar

*Electrical and Electronics Engineering Department
Middle East Technical University
Ankara, Turkey
e244228@metu.edu.tr*

Osama Awad

*Electrical and Electronics Engineering Department
Middle East Technical University
Ankara, Turkey
e248849@metu.edu.tr*

İsmail Enes Bülbül

*Electrical and Electronics Engineering Department
Middle East Technical University
Ankara, Turkey
e244263@metu.edu.tr*

Abstract—This document is about the end-term project of EE314 Digital Circuits Laboratory, implementation of a 2D strategy game by using FPGA.

Index Terms—FPGA, Verilog, VGA driver, button debouncing, state-machine

I. INTRODUCTION

II. PROJECT OVERVIEW

A. VGA Module

B. Button Debouncing Module

Example citation [1]

III. CONCLUSION

REFERENCES

[1] “Butterworth filter,” Feb 2019.