Israel Escobar-Camacho

□ +1 312 678 9737 | @ iescobar@andrew.cmu.edu | the linkedin.com/israel-e-c | ♥ Chicago, IL, US | ௸ Spanish & English

EDUCATION

Carnegie Mellon University

Pittsburgh, PA

B.S in Electrical & Computer Engineering M.S in Electrical & Computer Engineering

 $Sep \ 2019 - May \ 2024$

January 2024 - May 2025

• Relevant Coursework: Introduction to Computer Systems, Computer Systems and Hardware-software interfaces, Introduction to Embedded Systems, Functional Programming,

Introduction to Computer Security, Principals of Imperative Computation

• Scholarships: Tarten Scholar, Renaissance Scholar, Chicago Scholar

SKILLS

Languages: C/C++, Bash, Python, SML, Java, Matplotlib, TCL Scripting, SystemVerilog, RISC-V ISA, x86-64 ISA Technologies: Intel Pin, Git/Github, Quartus, GDB, Linux, Windows Vista/8.1/10, MacOS X, UNIX

Work Experience

Intel Hillsboro, OR

Pre-silicon IP Validation Intern

May 2023 - August 2023

- Developed and refactored dynamic Python scripts for new infastructures to support my team efforts
- Self-taught architectural and design knowledge for applying my skills throughout advancing current validation practices

Carnegie Mellon University

Pittsburgh, PA

Teacher Assistant for Principals of Imperative Computation

Summer 2022

- Supported collaborative learning during lab and instruct recitations varying from 4 to 15 students
- Taught topics ranging from data structures, correctness in C, to big-O time efficency
- Held office hours to support students 1-on-1, including review sessions for larger groups

RESEARCH EXPERIENCE

Carengie Mellon Univ. Department of Mechanical Engineering

Pittsburgh, PA

Undergraduate Researcher

September 2023 – Current

- Troubleshooted C-based firmware for flashing a STM32 processor on a Custom PCB design
- Developed communication protocols with UART for our chemical sensor EmStat Pico Module

Carnegie Mellon Univ. Department of Electrical & Computer Engineering

Pittsburgh, PA

Undergraduate Researcher

January 2023 - May 2023

- Dealt with TCL scripting with the Cadence toolset for synthesizing and performance analyzation
- Worked on ISCAS files and SOP simplification/optimizations through python scripting
- Developed Bash and Python scripts for data analysis and automation

PROJECTS

Real-Time Kernel

- Developed a mult-threaded Real Time Operating System for a STM32 processor with enforced fixed priority scheduler
- Utilized GDB to step through out x86-64 instructions for my C code and trace assembly code
- Used MMIO to build upon the UART and I2C Drivers for communication between arduino, LEDs, and other electronics

Branch Predictors

- Implemented branch predictors, meant for computer infrastructures, in C++ code to test efficiency
- Used Intel Pin to run these branch predictors on simulated computer processes given by SPEC2017
- Utilized results, specially picked with Pin, to analyze the efficiency and report our discoveries (making claims with our data collected)