

# PIMT 2021 NSF WORKSHOP ON PROCESSING-IN-MEMORY TECHNOLOGY

## Introduction

PIMT will provide a forum for leading experts in the relevant research thrusts of Processing-In-Memory technology, specifically circuit, architecture, systems, and applications. This will enable researchers to brainstorm the latest research progress and discuss their visions of the critical challenges that need to be addressed in the near future.

## Workshop Committees



Ulf Schlichtmann  
Technical University  
of Munich



Partha Pande  
Washington  
State Univ.



Yiran Chen  
Duke Univ.



Sharon Hu  
University of  
Notre  
Dame

## Workshop Schedule

### Phase 1: Pilot Talks (Online)

To boost the community vitality during the pandemic, the workshop will hold pilot talks online to the public.

- Pilot Talks (Online)  
Sept. 2020~Mar. 2021
- Workshop Application  
Jan. 2021, Notification: Mid-Feb.
- In-Person Workshop  
Mar. 2021

## INFORMATION



<http://www.nsf-pim.com/>  
**Xiang Chen**  
George Mason University  
xchen26@gmu.edu

\*The workshop organization team is carefully monitoring the situation of COVID-19. The exact date of the workshop will be timely updated online.

## Important Dates

### Pilot Talks (Online)



**1<sup>st</sup> Pilot Talk**  
**Dr. Kaushik Roy** Purdue Univ.  
Sept. 18<sup>th</sup>, 2020  
In-Memory Computing based Machine Learning Accelerators: Opportunities and Challenges



**2<sup>nd</sup> Pilot Talk**  
**Dr. Onur Mutlu** ETH Zurich  
Oct. 26<sup>th</sup>, 2020  
Intelligent Architectures for Intelligent Machines



**3<sup>rd</sup> Pilot Talk**  
**Mr. Stephen S. Pawlowski**  
Micron Technology Dec. 10<sup>th</sup>, 2020  
The Challenges and Opportunities of Processing-in-Memory



**4<sup>th</sup> Pilot Talk**  
**Dr. Xian-He Sun**  
Illinois Institute of Technology Jan. 7<sup>th</sup>, 2021  
The Challenges and Opportunities of Processing-in-Memory: A Performance Point of View



**5<sup>th</sup> Pilot Talk**  
**Dr. Ram Krishnamurthy**  
Intel Labs  
Feb. 5<sup>th</sup>, 2021 10AM~11PM EST

High Performance and Energy Efficient  
Circuit Technologies for sub-7nm AI  
Accelerators and In-Memory/  
Near-Memory Computing



\* Please visit our YouTube  
channel for the previous  
talks' video recording.