

PIMT 2021 NSF WORKSHOP ON PROCESSING-IN-MEMORY TECHNOLOGY

Introduction

PIMT will provide a forum for leading experts in the relevant research thrusts of Processing-In-Memory technology, specifically circuit, architecture, systems, and applications. This will enable researchers to brainstorm the latest research progress and discuss their visions of the critical challenges that need to be addressed in the near future.

Workshop Committees



Ulf Schlichtmann
Technical University of Munich



Partha Pande
Washington State Univ



Yiran Chen
Duke Univ.



Sharon Hu
University of
Notre Dame

Workshop Schedule

Phase 1: Pilot Talks (Online)

To boost the community vitality during the pandemic, the workshop will hold pilot talks online to the public.

Phase 2: In-Person Participated Workshop

The workshop will be held at George Mason University, which is structured with Invited Talks, Invited Expert Panels, Group Discussions, and Report Synthesis.

INFORMATION



<http://www.nsf-pim.com/>

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*The workshop organization team is carefully monitoring the situation of COVID-19. The exact date of the workshop will be timely updated online.

PIMT Call for Participation

Pilot Talks (Online)



1st Pilot Talk
Dr. Kaushik Roy Purdue Univ.
Sept. 18th, 2020
In-Memory Computing based Machine Learning Accelerators: Opportunities and Challenges



2nd Pilot Talk
Dr. Onur Mutlu ETH Zurich
Oct. 26th, 2020
Intelligent Architectures for Intelligent Machines



3rd Pilot Talk
Mr. Stephen S. Pawlowski Micron Technology Dec. 10th, 2020
Intelligent Architectures for Intelligent Machines

4th Pilot Talk



Dr. Xian-He Sun
Illinois Institute of Technology
Jan. 7th, 2021 11AM~12PM

The Challenges and Opportunities of Processing-in-Memory: A Performance Point of View



* Please visit our YouTube channel for the previous talks' video recording.