

PIMT 2021 NSF WORKSHOP ON PROCESSING-IN-MEMORY TECHNOLOGY

March 17th~18th2021, Online

Introduction

PIMT will provide a forum for leading experts in the relevant research thrusts of Processing-In-Memory technology, specifically circuit, architecture, systems, and applications. This will enable researchers to brainstorm the latest research progress and discuss their visions of the critical challenges that need to be addressed in the near future.

Workshop Committees



Ulf Schlichtmann
Technical University of Munich



Partha Pande
Washington State Univ.



Yiran Chen
Duke Univ.



Sharon Hu
University of
Notre Dame

Important Dates

- Pilot Talks (Online)
Sept. 2020~Mar. 2021
- Workshop Application
Feb. 21st, Notification: Mar. 5th
- Online Workshop
Mar. 17th~18th 2021

Phase 1: Online Pilot Talks

To boost the community vitality during the pandemic, the workshop will hold pilot talks online to the public.

Phase 2: Online Workshop

The workshop will be held online, which is structured with Invited Talks, Invited Expert Panels, Group Discussions, and Report Synthesis.

INFORMATION



<http://www.nsf-pim.com/>
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*The workshop organization team is carefully monitoring the situation of COVID-19. The exact date of the workshop will be timely updated online.

PIMT Call for Participation

The workshop participation application system is open!
Please submit your application by Feb. 28th 2021!

Pilot Talks (Online)



1st Dr. Kaushik Roy

Purdue University Sept. 18th, 2020
In-Memory Computing based Machine Learning Accelerators: Opportunities and Challenges



2nd Dr. Onur Mutlu

ETH Zurich Oct. 26th, 2020
Intelligent Architectures for Intelligent Machines



3rd Mr. Stephen S. Pawlowski

Micron Technology Dec. 10th, 2020
The Challenges and Opportunities of Processing-in-Memory



4th Dr. Xian-He Sun

Illinois Institute of Technology Jan. 7th, 2021
The Challenges and Opportunities of Processing-in-Memory: A Performance Point of View



5th Dr. Onur Mutlu

Intel Labs Feb. 5th, 2021
High Performance and Energy Efficient Circuit Technologies for sub-7nm AI Accelerators and In-Memory/Near-Memory Computing



6th Pilot Talk

Dr. Qinru Qiu

Syracuse University
Mar. 5th, 2021 11AM~12PM EST



Neuromorphic Computing
on Neuromorphic Processors:
A Non-von Neumann Approach to
Machine Intelligence



* Please visit our YouTube
channel for the previous
talks' video recording.