

# PIMT 2021 NSF WORKSHOP ON PROCESSING-IN-MEMORY TECHNOLOGY

## Introduction

PIMT will provide a forum for leading experts in the relevant research thrusts of Processing-In-Memory technology, specifically circuit, architecture, systems, and applications. This will enable researchers to brainstorm the latest research progress and discuss their visions of the critical challenges that need to be addressed in the near future.

## Workshop Committees



Ulf Schlichtmann  
Technical University  
of Munich



Partha Pande  
Washington  
State Univ



Yiran Chen  
Duke Univ.



Sharon Hu  
University of  
Notre  
Dame

## Workshop Schedule

### Phase 1: Pilot Talks (Online)

To boost the community vitality during the pandemic, the workshop will hold pilot talks online to the public.

### Phase 2: In-Person Participated Workshop

The workshop will be held at George Mason University, which is structured with Invited Talks, Invited Expert Panels, Group Discussions, and Report Synthesis.

## INFORMATION



<http://www.nsf-pim.com/>

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George Mason University  
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\*The workshop organization team is carefully monitoring the situation of COVID-19. The exact date of the workshop will be timely updated online.

## PIMT Call for Participation

### Pilot Talks (Online)



1<sup>st</sup> Pilot Talk  
**Dr. Kaushik Roy** Purdue Univ.  
Sept. 18<sup>th</sup>, 2020  
In-Memory Computing based Machine Learning Accelerators: Opportunities and Challenges



2<sup>nd</sup> Pilot Talk  
**Dr. Onur Mutlu** ETH Zurich  
Oct. 26<sup>th</sup>, 2020  
Intelligent Architectures for Intelligent Machines

### 3<sup>rd</sup> Pilot Talk



**Mr. Stephen S.  
Pawlowski**

Corporate Vice President  
Micron Technology

Dec.10<sup>th</sup>, 2020 11AM~12PM

### The Challenges and Opportunities of Processing-in-Memory

\* Please visit our website for online access instructions.



\* Please visit our YouTube  
channel for the previous  
talks' video recording.