

# 2021 NSF WORKSHOP ON PROCESSING-IN-MEMORY TECHNOLOGY

## Introduction

The Workshop on Processing-In-Memory (PIMT) provides a forum for leading experts in the relevant research thrusts of Processing-In-Memory (PIM) technology, specifically circuit, architecture, systems, and applications. PIMT also enables researchers to brainstorm the latest research progress and discuss their visions of the critical challenges that need to be addressed in the near future.

## Workshop Committees



Ulf Schlichtmann  
Technical University  
of Munich



Partha Pande  
Washington  
State Univ



Yiran Chen  
Duke Univ.



Sharon Hu  
University of  
Notre  
Dame

## Workshop Schedule

### Phase 1: Pilot Talks (Online)

To boost the community vitality during the pandemic, the workshop will hold pilot talks online to the public.

### Phase 2: In-Person Participated Workshop

The workshop will be held at George Mason University, which is structured with Invited Talks, Invited Expert Panels, Group Discussions, and Report Synthesis.

## INFORMATION



<http://www.nsf-pim.com/>

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George Mason University  
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\*The workshop organization team is carefully monitoring the situation of COVID-19. The exact date of the workshop will be timely updated online.

March 2021, George Mason University, Fairfax, VA

## Call for Participation

### 4 Major Thrusts

- PIM Circuits;
- PIM Architecture;
- PIM System;
- PIM Applications.

### 1<sup>st</sup> Pilot Talk (Online)



Dr. Kaushik Roy  
Purdue University

Sept. 18<sup>th</sup>, 2020  
12PM~1PM

In-Memory Computing  
based Machine Learning  
Accelerators: Opportunities  
and Challenges

\* Please visit our website for  
online access instructions.