

# **RESUME**

**SAGAR V. DUSANE**

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## **CAREER OBJECTIVE:**

To work in an organization where my skills and knowledge is utilized to the fullest and where I can have a continuous learning progress which leads to my overall development in order to achieve my career goals.

## **EDUCATIONAL QUALIFICATION:**

### **Academic Profile:**

<b>EXAM</b>	<b>University/Board</b>	<b>YEAR OF PASSING</b>	<b>CLASS</b>	<b>PERCENTAGE</b>
<b>M.B.A.(Marketing)</b>	University Of Pune	2019	First Class	60.10%
<b>B.E.(Electrical)</b>	University Of Pune	2016	First Class	63.47%
<b>H.S.C.</b>	Maharashtra State Board.	2010	Second Class	53.17%
<b>S.S.C.</b>	Maharashtra State Board.	2008	First Class	74.92%

## **WORK EXPERIENCE**

**Company Name:** Narkhede Switchgear Pvt. Ltd, Nashik (Maharashtra)

**Duration :** 1<sup>st</sup> November 2016 to 31<sup>st</sup> July 2018

**Designation:** Quality Engineer

- Incoming, in-process and outgoing Quality Inspection
- BOM checking
- Handling the product quality issues
- Testing of switchgear: Primary current injection, high voltage, Functional tests, Insulation resistance test and Transformer ratio test.
- Maintain quality documents

## OTHER TECHNICAL SKILLS:

<b>Operating System</b>	: Windows 98, 2003, XP, 2007
<b>Software known</b>	: Auto CAD(Electrical)
<b>Packages Known</b>	: MS Office (Word, PowerPoint, Excel)

## TRAININGS AND PLANT VISITS:

- Training at 500kV HVDC Substation, Padghe, Thane. Training conducted on high voltage dc power transmission from date 29/5/2014 to 7/6/2014 .
- In-plant training on HTLS conductor in Apar Industries Ltd. From date 3/12/2015 to 17/12/2015.

## PROJECT:

**TITLE:** Statistical Analysis and Modeling of HTLS Conductor.

**DESCRIPTION:** The project deals with the statistical results that shows high temperature low sag (HTLS) conductors can be used for transmission of nearly doubled electric power in comparison to existing ACSR conductor.

## EXTRACURRICULAR ACTIVITIES:

- Published the paper on subject “**Statistical Analysis & Comparison of HTLS Conductor with Conventional ACSR Conductor**” in the proceedings of International Conference on “Electrical, Electronics and Optimization Techniques(ICEEOT)-2016” with **IEEE catalog “978-1-4673-9939-5/16/\$31.00©2016”**, DMI college of Engineering, Chennai, Tamilnadu, during 3<sup>rd</sup> to 5<sup>th</sup> March 2016.
- Represented the paper on subject “**Transformer Fault Detection Using Sweep Frequency Response Analysis**” at the International Conference on “Innovative Advancements in Engineering and Technology”-IAET from 7<sup>th</sup> to 8<sup>th</sup> March, 2014 organized by the Department of Electronics and Communication Engineering, Jaipur National University, Jaipur, Rajasthan.
- Participated in “**Entrepreneurship Awareness Camp**” Organized by “UDYOGWARDHINI SHIKSHAN SANSTHA”, Nashik.
- Student Coordinator in National Level Event Sandip Drishti 2015 at SIEM, Sandip foundation, Nashik.
- Participated in National Level Technical Festival “AAYAAM-2013”.
- Participated in National Level Technical Symposium Event DESIRE-2014, SIEM, Nashik.
- Participated in National level paper presentation “Electra-14” at Sir Visvesvaraya Institute of Technology, Chincholi, Nashik.

**PERSONAL DETAILS:**

- **Father's Name** : Mr. Vilas P. Dusane
- **Permanent Address** : 7,Raviraj Empire,Samarth Nagar,Dwarka,Nashik
- **Date of Birth** : 27<sup>th</sup> April 1992
- **Gender** : Male
- **Languages Known** : English, Hindi and Marathi.
- **Marital Status** : Unmarried

**DECLARATION :**

I hereby declare that, all the above information is true and correct to the best of my knowledge and belief.

**Place :**

**Date :**

**Sagar V. Dusane**