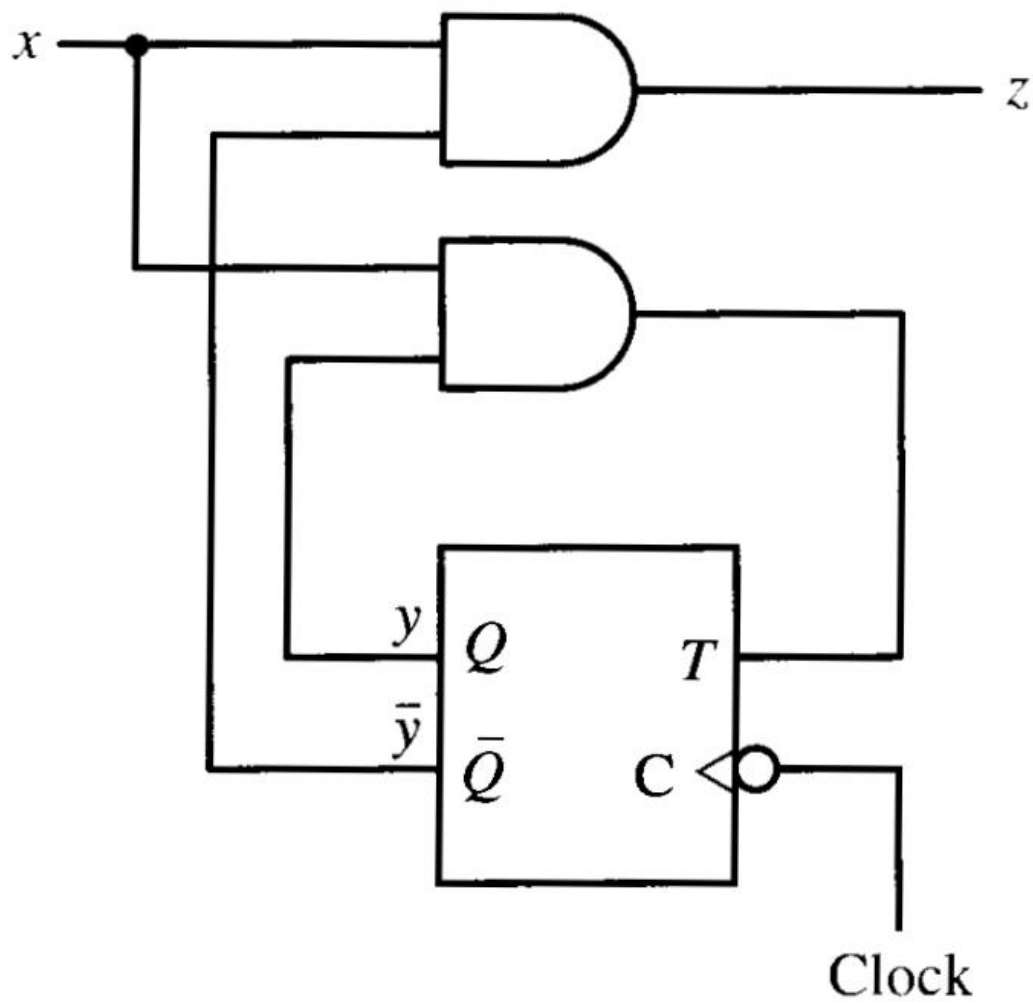For the synchronous sequential circuit of the figure, find:

(a) The state table. Assign A = 0, B= 1.

(b) The state diagram.

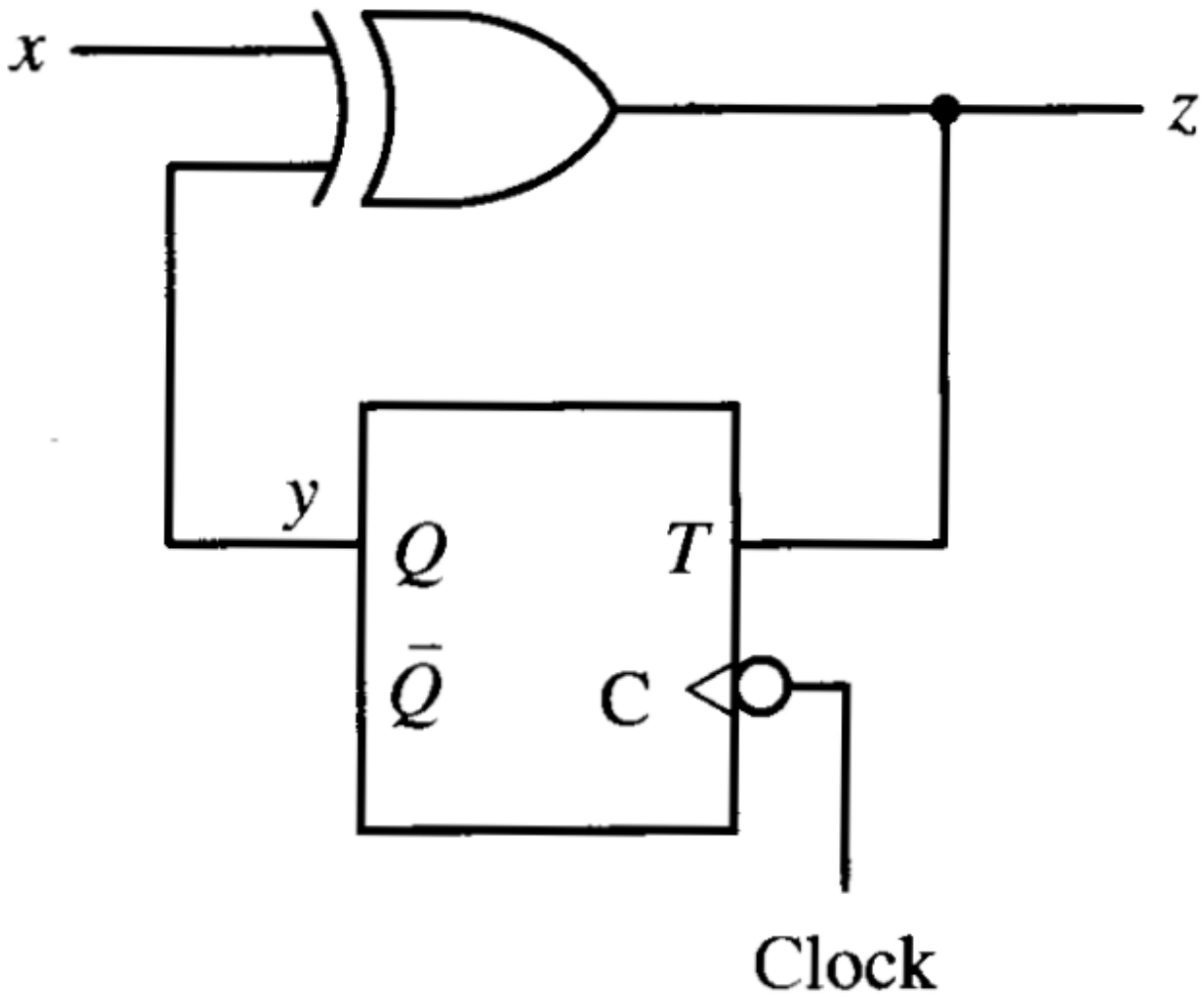(c) The timing diagram for an input sequence x = 00100110 and the starting state $y° = 1$.

- Given the synchronous sequential circuit of the figure with level inputs:

(a) Draw a timing diagram for x = 000101011 and the initial state of the flip flop is equal to 0 $(y^0 = 0)$.

(b) Find the state diagram.

(c) Find the state table.

- For the sequential circuit in the figure, find:

(a) The state table (A =0, B= 1).

(b) The state diagram.

(c) A timing diagram if the starting state is $y^0 = 0$ and x = 001011000.

This circuit is level synchronous.