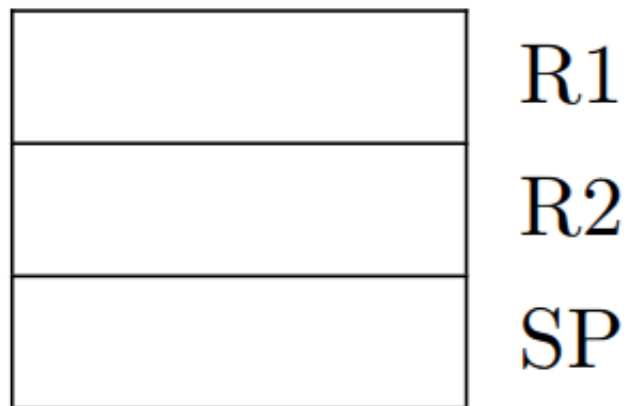


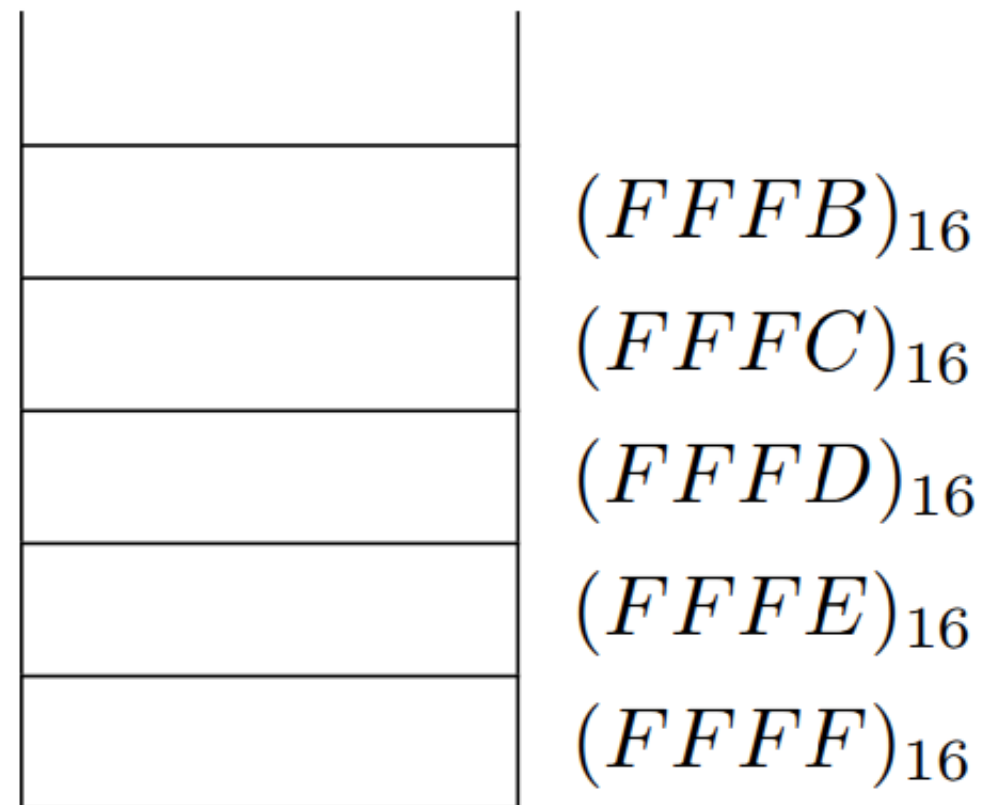
```

SP  $\leftarrow$  (FFFF)16
R1  $\leftarrow$  lsh(R1+1)
push_16(R1)
R1  $\leftarrow$  lsh(R2+1)
push_16(R1)
R1  $\leftarrow$  R1+R2
push_16(R1)
R2  $\leftarrow$  R1+R2
pop_16(R2)
pop_16(R1)
R2  $\leftarrow$  lsh(R1+1)
pop_16(R1)

```



Register



RAM

