MIRROR SWITCH CIRCUIT

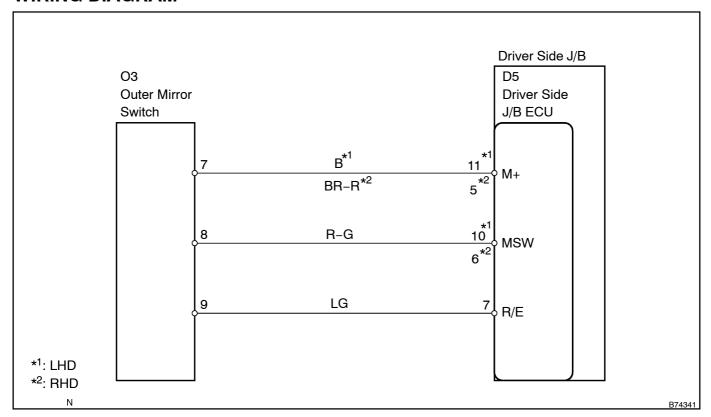
CIRCUIT DESCRIPTION

This circuit detects the conditions of the mirror master switch and the mirror control switch.

The operating condition of the mirror switch is input by the driver side J/B ECU and the input signals are sent to the driver door ECU or the passenger ECU.

Mirror adjustment is controlled by the driver door ECU or the passenger door ECU.

WIRING DIAGRAM



INSPECTION PROCEDURE

1 | READ[YALUE[OF[]NTELLIGENT[]TESTER[]I

- (a) Connect the intelligent tester to the DLC3.
- (b) Turn the ignition switch ON and press the intelligent tester I main switch ON.
- $(c) \begin{tabular}{ll} Select \line \li$

Driverside J/B ECU:

Item	Measurement[ltem/ Display[[Range)	Normal@ondition
Mirr[\$el[\$W[L	Mirror[master[\$witch[\$ignal]]or[LH[mirror/ ON[or[DFF	ON:[\$witch[]s[]n[]eft[position OFF:[\$witch[]s[]n[]original[]or[]ight[]position
Mirr[\$el[\$W[R	Mirror[master[switch[signal[]or[RH[mirror/ ON[or[DFF	ON:[\$witch[]s[]n[]ight[]position OFF:[\$witch[]s[]n[]original[]or[]eft[]position
Mirr[Pos[\$W[]_	Mirror@ontrol[\$witch[\$ignal[[Left)/ ON[or[DFF	ON:[\$witch[]_[]s[DN OFF:[Any[\$witch[except[]_[]s[DN[or[all[\$witches are[DFF
Mirr[Pos[\$W[R	Mirror@ontrol@witch@ignal[[Right)/ ON[or[OFF	ON: Switch Ris ON OFF: Any switch except Ris ON or all switches are OFF
Mirr[Pos[\$W[Up	Mirroriٍontroliٍ\$witchi\$ignal[[Up)/ ON[ori]OFF	ON:[\$witch[]JP[]s[]ON OFF:[Any[\$witch[@xcept[]JP[]s[]DN[]pr[]all[\$witches are[]DFF
Mirr[Pos[\$W[Dwn	Mirror[control[switch[signal[[Down)/ ON[or[DFF	ON:[\$witch[DOWN[]s[DN OFF:[Any[\$witch[except[DOWN[]s[DN[]or[]all switches are OFF

OK:

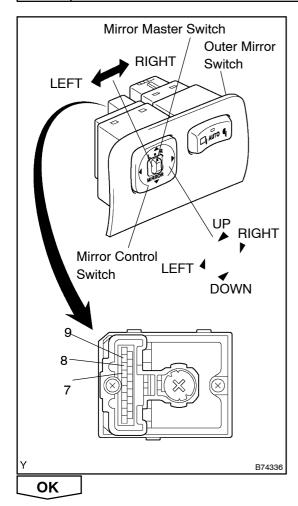
On the tester screen, each item should change between ON and OFF according to the above chart.

NG Go to step 2

OK

PROCEED TO NEXT CIRCUIT INSPECTION SHOWN ON PROBLEM SYMPTOMS TABLE (See page 05-2080)

2 INSPECT MIRROR MASTER SWITCH AND MIRROR CONTROL SWITCH



(a) Measure the resistance between each terminal of the mirror master switch and mirror control switch.

Standard:

Mirror master switch

Tester Condition	Switch Condition	Specified Condition
8 – 9	LEFT	100 Ω
8 – 9 RIGHT 0		0

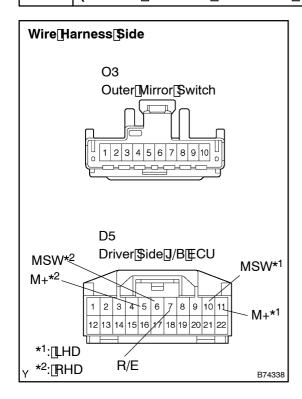
Mirror control switch

Tester Condition	Switch Condition	Specified Condition
7 – 9	UP	Approx. 100 Ω
7 – 9	RIGHT	250 Ω
7 – 9	DOWN	470 Ω
7 – 9	LEFT	800 Ω

NG

REPLACE OUTER MIRROR SWITCH

3 | CHECK[WIRE[HARNESS (OUTER[MIRROR[\$WITCH - [DRIVER[\$IDE]]/B[ECU)]



- (a) Disconnect he O3 witch and D6 ECU connectors.
- (b) Measure the resistance of the wire harness ide to nnectors.

Standard: LHD models

Tester@onnection	Specified[Condition
O3-7 -[D5-11[M+)	Below[] [Ω
O3-8 -[D5-10[]MSW)	Below[] [Ω
O3-9 -[D5-7[JR/E)	Below[] [Ω
O3-7 -[Body[ground	10[k͡k͡k͡k͡k]∱r[ħigher
O3-8 -[Body[ground	10[kthtp://brthigher

RHD models

Tester@onnection	Specified[Condition
O3-7 -[D5-5[]M+)	Below[] [Ω
O3-8 -[D5-6[MSW)	Below[] [Ω
O3-9 -[D5-7[JR/E)	Below[] [Ω
O3-7 -[Body[ground	10[kttopprtthigher
O3-8 -Bodyground	10[k͡ᡌᢩᠪr[ħigher

NG REPLACE OUTER MIRROR SWITCH

OK _

PROCEED TO NEXT CIRCUIT INSPECTION SHOWN ON PROBLEM SYMPTOMS TABLE (See page 05-2080)