DTC	P0604□	INTERNAL CONTROL MODULE RANDOM ACCESS MEMORY (RAM) ERROR
DTC□	P 0606∏	ECM/PCM[PROCESSOR
	•	
DTC□	P0607□	CONTROL[MODULE[PERFORMANCE
DTC	P0657	ACTUATOR[\$UPPLY[YOLTAGE CIRCUIT/OPEN

CIRCUIT DESCRIPTION

The ECM continuously monitors it's internal memory status, internal circuits, and output signals sent to the throttle@ctuator. This self-check[insures@hat@he[ECM[is@functioning@properly.]]f@ny@nalfunction[is@letected, the ECM will set the appropriate DTC and fluminate the MIL.

The [ECM [memory[status]]s [diagnosed [by [internal]] mirroring "[of [inhe [main [CPU [and [inhe [sub [CPU [io [detect Random [Access] Memory [RAM) [errors.] The [two [CPUs] also [perform [continuous] mutual [monitoring.]] The [two [cPUs] also [perform [continuous] mutual [two [cPUs] also [twoECM[]|luminates[]the[]MIL[and[set]a[]DTC[]f: 1)[outputs[]from[]the[]two[]CPUs[are[]different[and[]deviate[]from[]the standards,[2)[]he[signals[sent[]o[]he[]hrottle[actuator[deviate[]rom[]he[]standards,[3)[a[]malfunction[]s[]ound in[]he[]hrottle[actuator[supply[voltage,[and[4])[any[other[ECM[]malfunction[]s[]ound.

DTC No.	DTC Detection Condition	Trouble Area
P0604		
P0606	FOM internal array	ECM
P0607	ECM internal error	ECIVI
P0657		

INSPECTION PROCEDURE

DEDI AGENCALIZA	
REPLACE[ECM[See[page 10-21)	