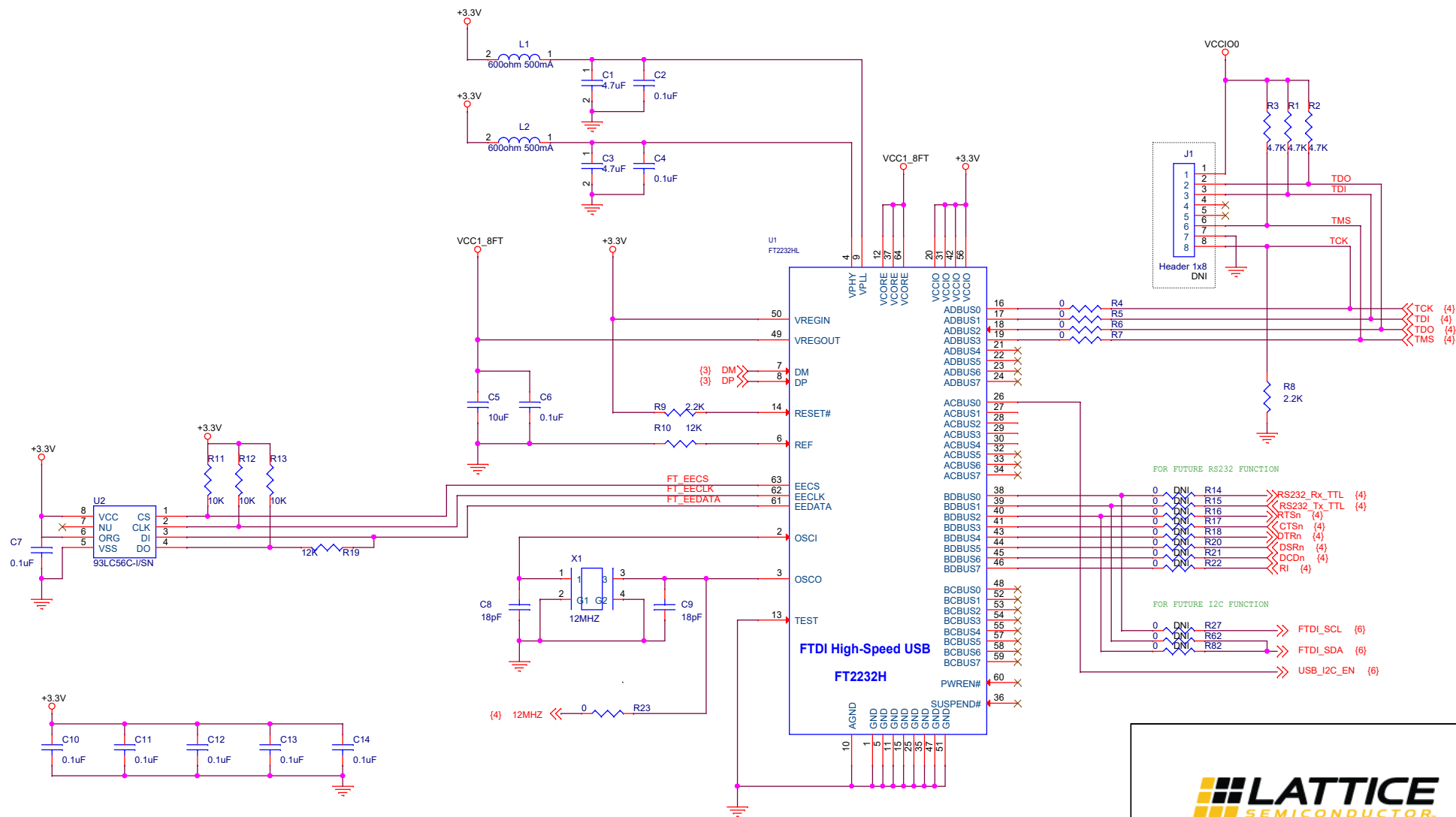


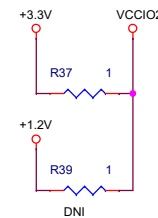
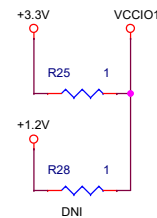
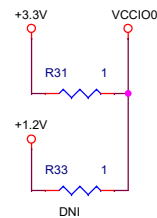
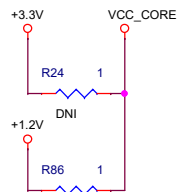
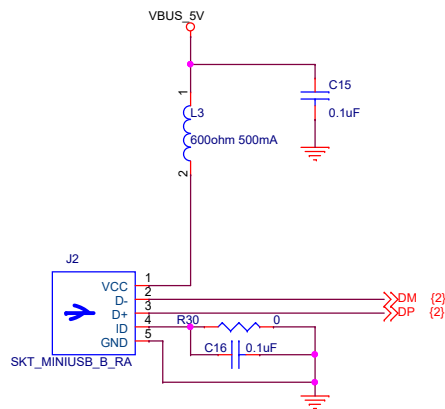
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Title MACHXO3 Starter Kit - BLOCK DIAGRAM		
Size B	Project MACHXO3 Starter Kit - LCMXO3L-6900C	Schematic Rev 1.0
Date: 24-SEPTEMBER-2021	Sheet 1	of 8

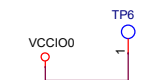
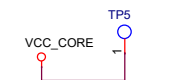


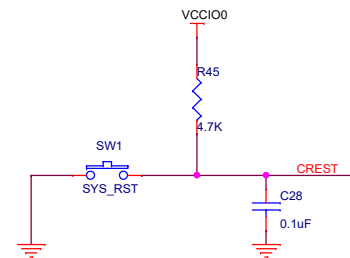
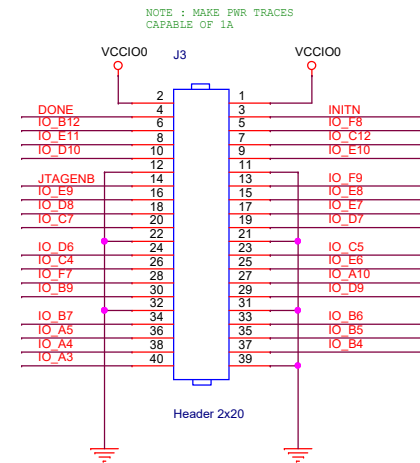
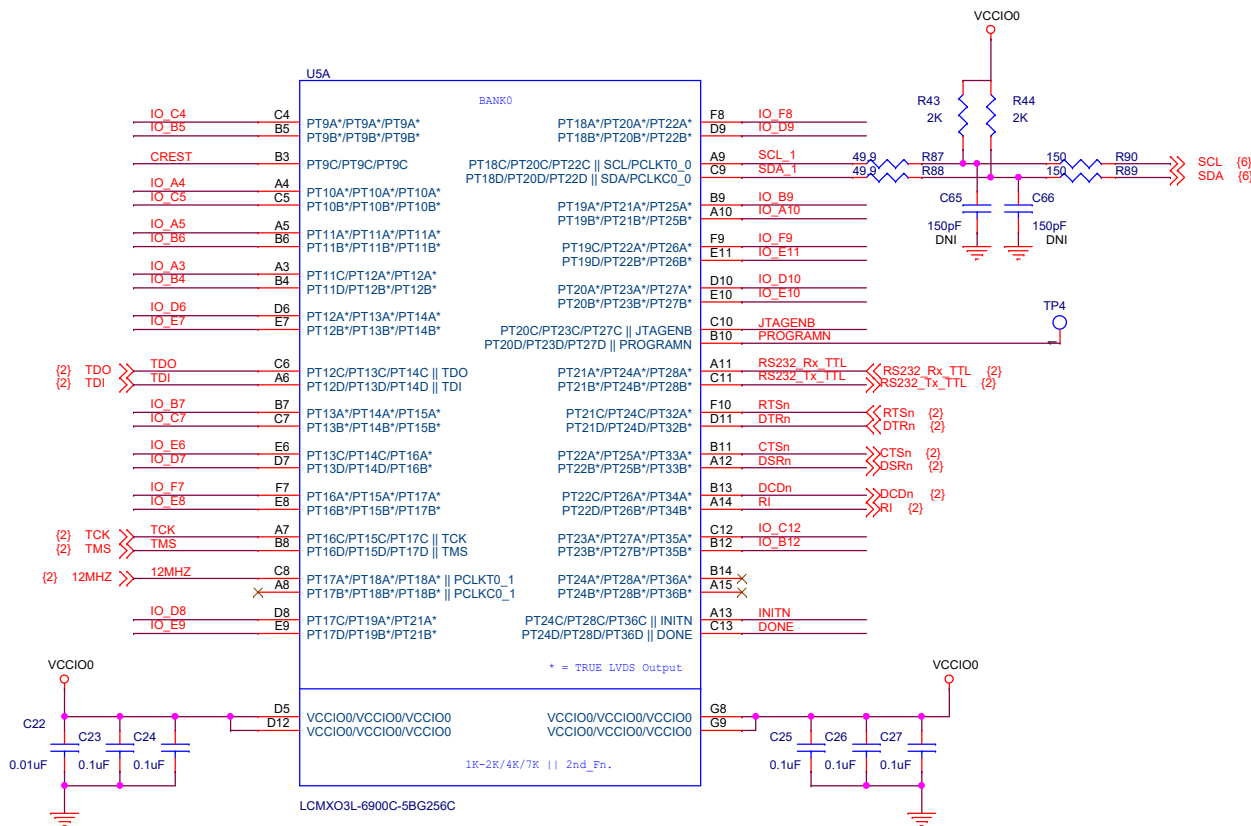
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Title		
USB to JTAG I/F		
Size	Project	Schematic Rev
B	MACHXO3 Starter Kit - LCMXO3L-6900C	1.0
Date:	24-SEPTEMBER-2021	Board Rev
		A
	Sheet	2 of 8



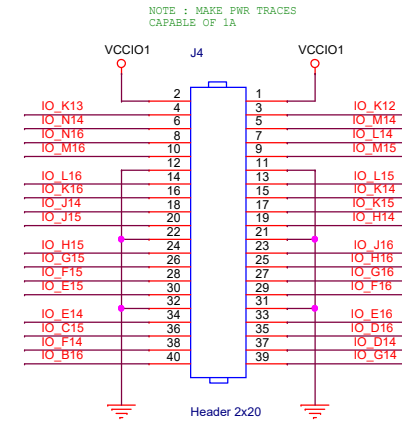
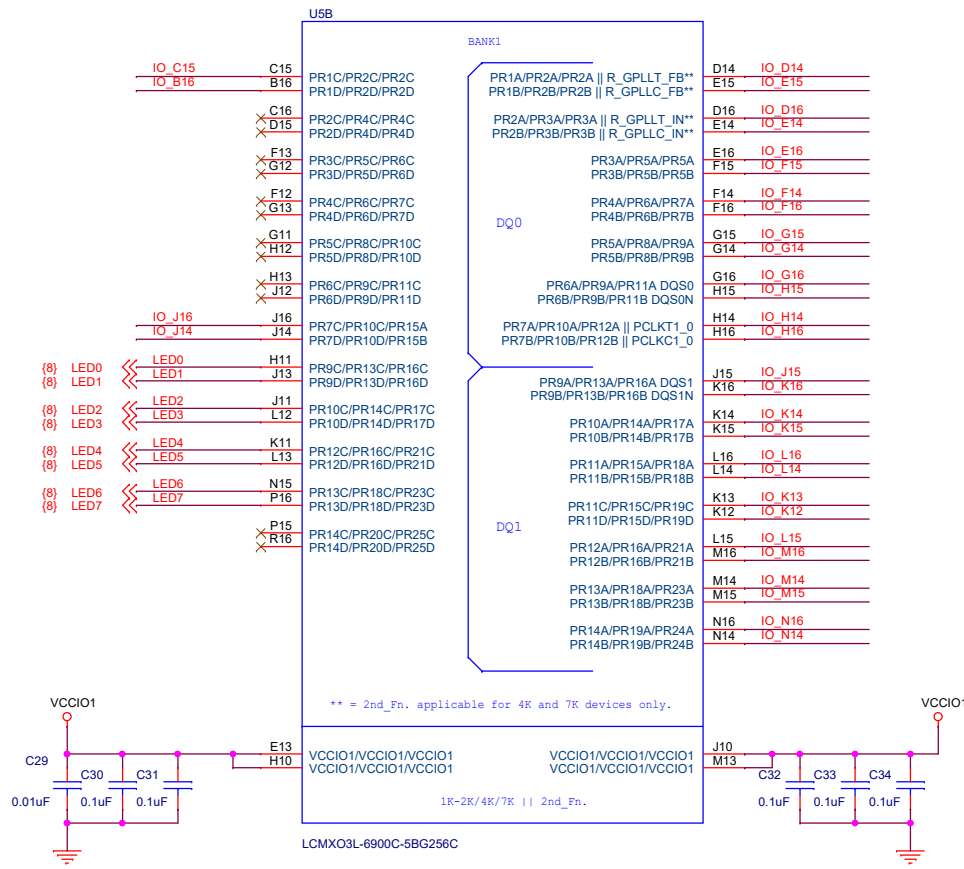
NOTE : Boot from external SPI Flash (U6) requires VCCIO2 set to 3.3V. Use caution when setting VCCIO2 to any other voltage.





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Title BANK0 I/O			
Size B	Project MACHXO3 Starter Kit - LCMXO3L-6900C	Schematic Rev 1.0	Board Rev A
Date: 24-SEPTEMBER-2021	Sheet 4 of 8		



J5

1 · Proto Type Area

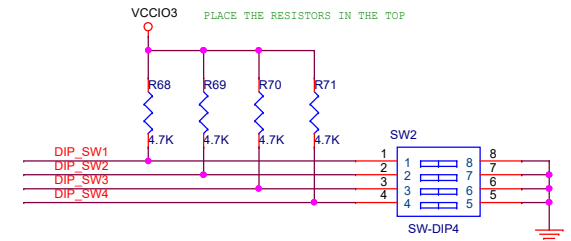
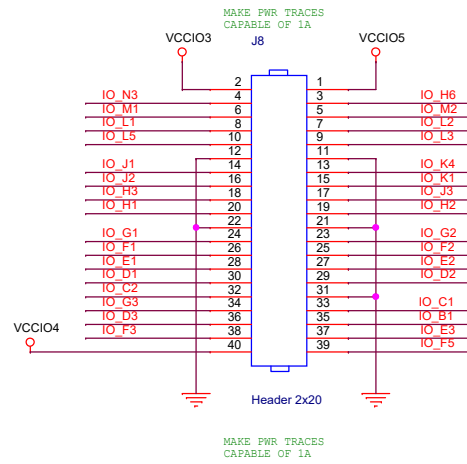
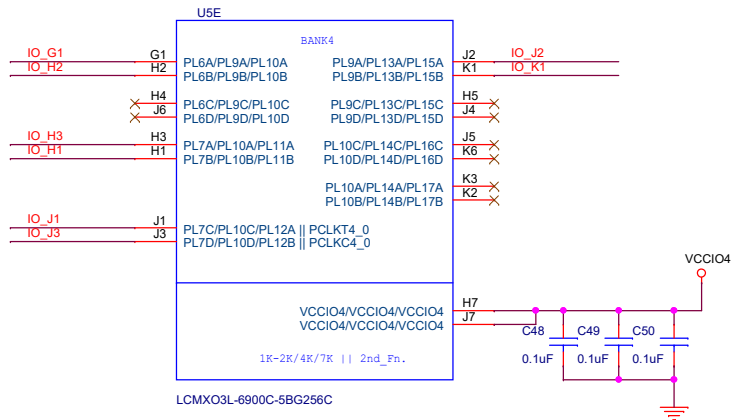
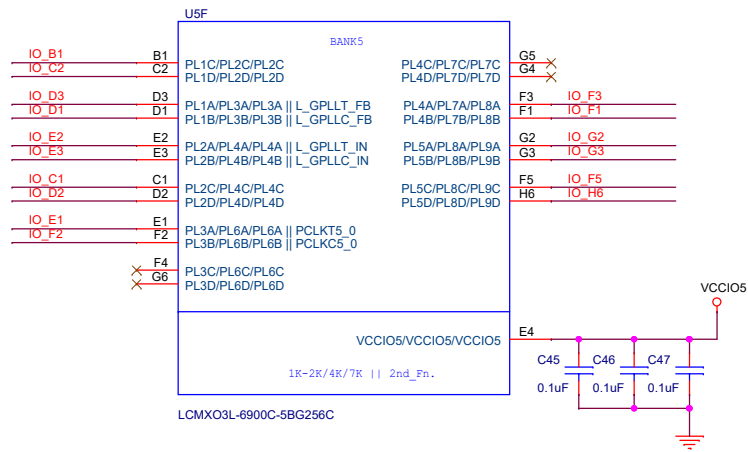
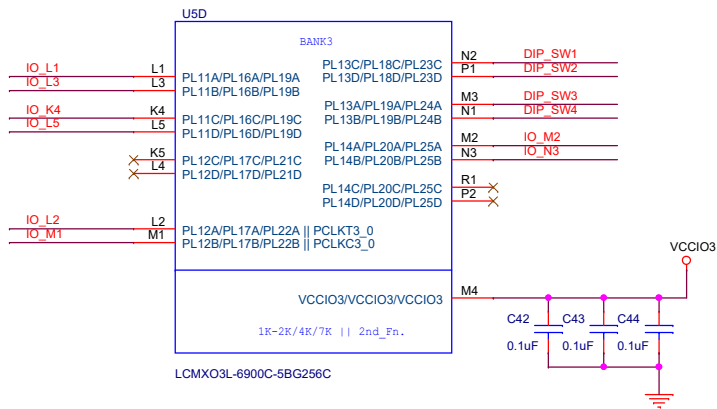
Proto Type Area, Holes on 0.1 inch Centers
DNI

PROTOTYPE AREA
FILL AVAILABLE AREA



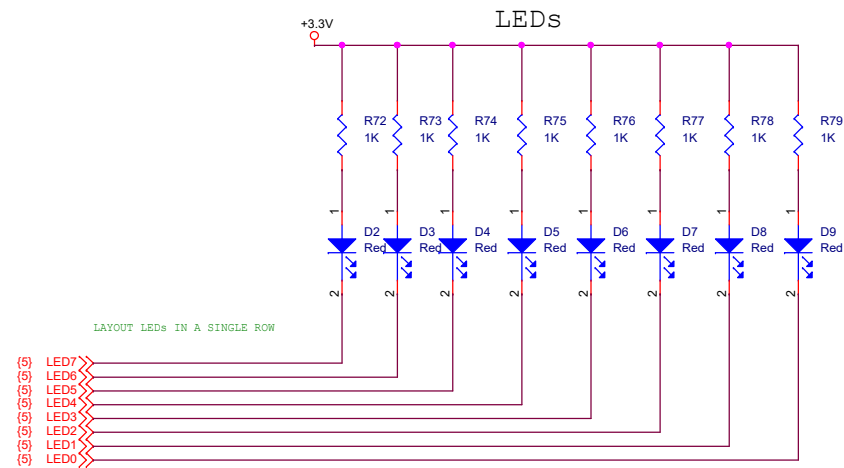
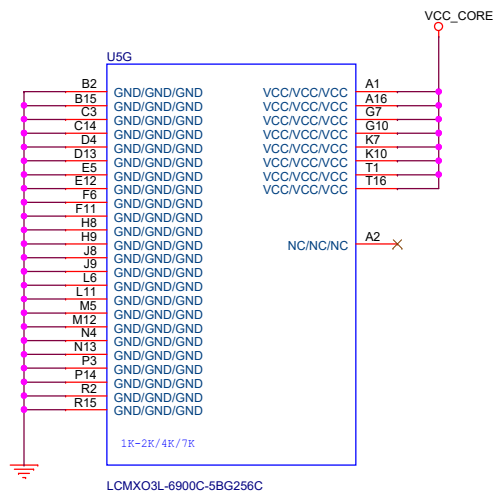
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Title BANK1 I/O		
Size B	Project MACHXO3 Starter Kit - LCMXO3L-6900C	Schematic Rev 1.0
Date: 24-SEPTEMBER-2021	Sheet 5 of 8	Board Rev A

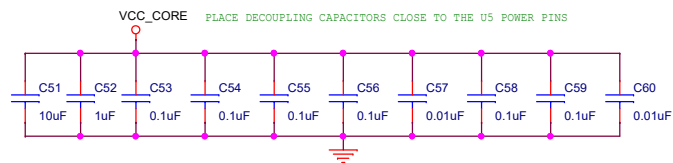


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Title		
BANK3,4,5 I/O		
Size B	Project MACHXO3 Starter Kit - LCMXO3L-6900C	Schematic Rev 1.0
Date: 24-SEPTEMBER-2021	Sheet 7 of 8	Board Rev A



Note : LEDs are controlled by XO3L I/O Bank 1. When VCCI01 is set to a voltage less than 3.3V, observe all I/O overdrive requirements. Refer to Lattice TN1280 "MachXO3L sysIO Usage Guide" for more information.



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Title POWER DECOUPLING AND LED'S		
Size B	Project MACHXO3 Starter Kit - LCMXO3L-6900C	Schematic Rev 1.0
Date: 24-SEPTEMBER-2021	Sheet 8 of 8	Board Rev A