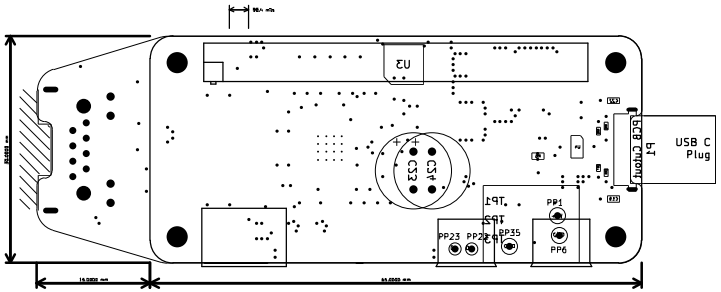

Layer Name	Type	Material	Thickness (mm)	Color	Epsilon R	Loss Tangent
F.Silkscreen	Top Silk Screen	Not specified	0 mm	Not specified	1	0
F.Paste	Top Solder Paste		0 mm		1	0
F.Mask	Top Solder Mask	Not specified	0.01 mm	Not specified	3.3	0
F.Cu	copper		0.035 mm		1	0
Dielectric 1	prepreg	FR4	0.0994 mm	Not specified	4.5	0.02
In1.Cu	copper		0.0152 mm		1	0
Dielectric 2	core	FR4	1.265 mm	Not specified	4.5	0.02
In2.Cu	copper		0.0152 mm		1	0
Dielectric 3	prepreg	FR4	0.0994 mm	Not specified	4.5	0.02
B.Cu	copper		0.035 mm		1	0
B.Mask	Bottom Solder Mask	Not specified	0.01 mm	Not specified	3.3	0
B.Paste	Bottom Solder Paste		0 mm		1	0
B.Silkscreen	Bottom Silk Screen	Not specified	0 mm	Not specified	1	0

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Sheet:
File: pizero-papoe.kicad_pcb

Title: Pi Zero Passive PoE HAT

Size: A4	Date: 2024-12-30	**Rev: R1**
KiCad E.D.A. 8.0.7		Id: 1/1