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IAN	ПА	RT	WI	G

IAN C. HARTWIG			San Francisco, CA		mail@ihartwig.me	
Skills:	EE System Design	BRINGUP & TEST	Software		FABRICATION & CABLING	
Fluent	Orcad / AllegroFunctional SpecsCM, JDM Workflows	Specs I2C, SPI, I2S		ountu le, Docker	AutoCAD, Inventor, Solidworks3D Printing	
Familiar	\overline{v}		C on X86, ARMPandas, SQL, SFDC		CNC Router, LaserMill, Lathe	
Past Use	KiCAD, EagleStackup Sim	Thermal EnvelopeSCPI, GPIB Scripts	■ Bash, Java■ HTML, CSS, JS		Automotive HarnessZuken E3	
Experience:	Industry			Projects		
Present (1½ years) Aug 2018 (2½ years)	compute layout, HVAC i Planned and rolled-out r Drove Gen. 2 HW design production for all cable l Standardized AC & LV E Hardware Engineer Pure Storage, Mountain Vie FlashArray fault-tolerant x81 HW Design Lead on new Modeling of Future Mem	isco, CA en design for new truck serientegration, and contractor Inew Intel Xeon + NVIDIA HER on with review, revision contramess, PCBA, and HPC OC power distribution and E ew, CA 6 server design team	iaison PC platform trol, and CM E-Stops	FRC 5026, E Guide dynar school stude electrical des weeks Design re Debuggin pressure a competitic 2019 Wor	Burlingame, CA mic group of 30+ high ents in mechanical and sign of 150 lb. robot in 6 views, strategy, tutorials g assistance under at local and travel ons rld Championship win with eams 1323, 973, 4201	
July 2016 Aug. 2015	 Design Review & Sample Testing for PCle 3, NVMe, RoCE 2, 10G+ Ethernet, 12G SAS Field Failure statistic tracking of all FlashArray HW for exec. review - Python/Pandas, Salesforce, JIRA 			CMU Robo-Buggy Built a self-guiding, gravity-powered vehicle with the CMU Robotics Club. Embedded SW, HW system		
May. 2015				•	, and power delivery HW.	
Aug. 2014 (1 year) Jan. 2014	 Apple, Cupertino, CA iOS Device Accessories Drove schematic & PCB for internal developer kits in loc with engineers on Battery Case and AirPods 		·	Extended RF Oakland, CA HW and SW USB data. github.com/	ter Toys RFID Entry FID locks at hackerspace in A. Custom microcontroller I with power relays and I hartwig/amtdoor3 I ihartwig/amtdoor2	
Education:	Degree			PROJECTS		
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May 2016 | Carnegie Mellon University

M.S. Electrical & Computer Engineering

- 18-623 Analog Integrated Circuits
- 18-625 Mobile and Server Product Design
- 18-649 Distributed Embedded Systems

May 2015 | Carnegie Mellon University

B.S. Electrical & Computer Engineering

- 18-578 Mechatronic Design
- 18-474 Embedded Control Systems
- 15-410 Operating Systems
- 18-349 Embedded Real-Time Systems

Embedded Systems TA 18-349 & 18-549

- Wrote labs, exams for C on ARM
- Designed Raspberry Pi lab HW
- Built Gitlab check-in workflow github.com/ihartwig/rpi-labio

AB Tech Executive Board

Live audio engineer and electrician for student-run productions co.

- 2-64 ch. Networked Audio
- 200+A 3-phase AC power