IAN C. HARTWIG			San Francisco, CA		mail@ihartwig.me
Skills:	EE System Design	BRINGUP & TEST	Software		FABRICATION & CABLING
Fluent	<ul><li>Orcad / Allegro</li><li>Functional Specs</li><li>CM, JDM Workflows</li></ul>	<ul><li>CAN, USB, PCIe</li><li>I2C, SPI, I2S</li><li>LV DC &amp; AC Power</li></ul>	<ul><li>Linux / Ubuntu</li><li>Python</li><li>Git, Ansible, Docker</li></ul>		<ul><li>AutoCAD, Inventor, Solidworks</li><li>3D Printing</li></ul>
Familiar	<ul><li>Altium Designer</li><li>Performance Modeling</li></ul>	<ul><li>Ethernet 1-25G</li><li>Rework 0201, QFN</li></ul>	<ul><li>C on X86</li><li>Pandas, S</li></ul>	, ARM SQL, SFDC	<ul><li>CNC Router, Laser</li><li>Mill, Lathe</li></ul>
Past Use	<ul><li>KiCAD, Eagle</li><li>Stackup Sim</li></ul>	<ul><li>Thermal Envelope</li><li>SCPI, GPIB Scripts</li></ul>	<ul><li>Bash, Jav</li><li>HTML, CS</li></ul>		<ul><li>Automotive Harness</li><li>Zuken E3</li></ul>
Experience: Industry Projects					
Present (1½ years) Aug 2018 (2½ years)	<ul> <li>Platform Hardware Engineer         Embark Trucks, San Francisco, CA     </li> <li>Led requirements-driven design for new truck series compute layout, HVAC integration, and contractor liai</li> <li>Planned and rolled-out new Intel Xeon + NVIDIA HPC</li> <li>Drove Gen. 2 HW design with review, revision controproduction for all cable harness, PCBA, and HPC</li> <li>Standardized AC &amp; LV DC power distribution and E-S</li> <li>Hardware Engineer</li> <li>Pure Storage, Mountain View, CA</li> <li>FlashArray fault-tolerant x86 server design team</li> </ul>		iaison PC platform trol, and CM	FIRST Robotics Mentor FRC 5026, Burlingame, CA Guide dynamic group of 30+ high school students in mechanical and electrical design of 150 lb. robot in 6 weeks  Design reviews, strategy, tutorials Debugging assistance under pressure at local and travel competitions 2019 World Championship win with alliance teams 1323, 973, 4201	
July 2016 Aug. 2015	<ul> <li>HW Design Lead on new NVMe product</li> <li>Modeling of Future Memories &amp; Interface options</li> <li>Multiphase VR Validation for designed-in Intel CPUs</li> <li>Design Review &amp; Sample Testing for PCle 3, NVMe, RoCE 2, 10G+ Ethernet, 12G SAS</li> <li>Field Failure statistic tracking of all FlashArray HW for exec. review - Python/Pandas, Salesforce, JIRA</li> <li>FlashArray Intern</li> <li>Developed tools to margin first-gen NVMe FlashModules</li> <li>SMD rework, measurement, threaded FW development in C, and test agripts in Puthon.</li> </ul>			<b>CMU Robo</b> Built a self-govehicle with a Embedded S	

and test scripts in Python

May. 2015

(1 year)

Aug. 2014 | Hardware Engineering Co-Op

Apple, Cupertino, CA iOS Device Accessories

 Drove schematic & PCB for internal developer kits in lockstep with engineers on Battery Case and AirPods

Jan. 2014 Sparked test automation sharing in Python across team

Ace Monster Toys RFID Entry Extended RFID locks at hackerspace in Oakland, CA. Custom microcontroller HW and SW with power relays and USB data.

github.com/ihartwig/amtdoor3 github.com/ihartwig/amtdoor2

### EDUCATION: DEGREE

## May 2016

Carnegie Mellon University

M.S. Electrical & Computer Engineering

- 18-623 Analog Integrated Circuits
- 18-625 Mobile and Server Product Design
- 18-649 Distributed Embedded Systems

#### May 2015 | Carnegie Mellon University

B.S. Electrical & Computer Engineering

- 18-578 Mechatronic Design
- 18-474 Embedded Control Systems
- 15-410 Operating Systems
- 18-349 Embedded Real-Time Systems

#### **PROJECTS**

# **Embedded Systems TA**

18-349 & 18-549

- Wrote labs, exams for C on ARM
- Designed Raspberry Pi lab HW
- Built Gitlab check-in workflow github.com/ihartwig/rpi-labio

#### **AB Tech Executive Board**

Live audio engineer and electrician for student-run productions co.

- 2-64 ch. Networked Audio
- 200+A 3-phase AC power