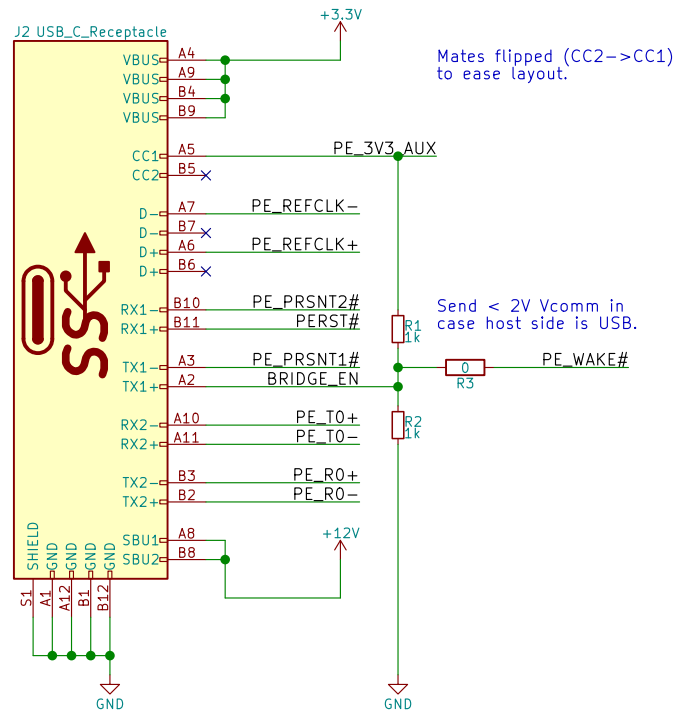
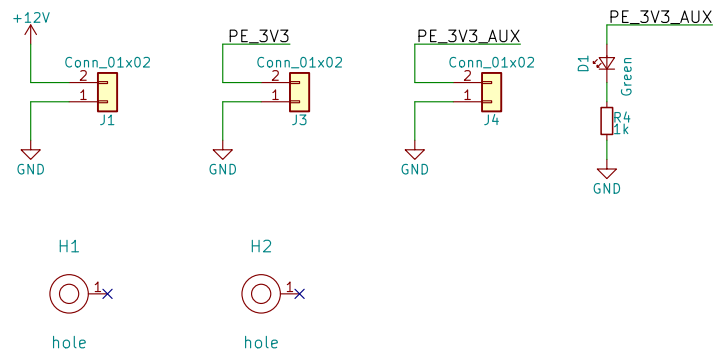


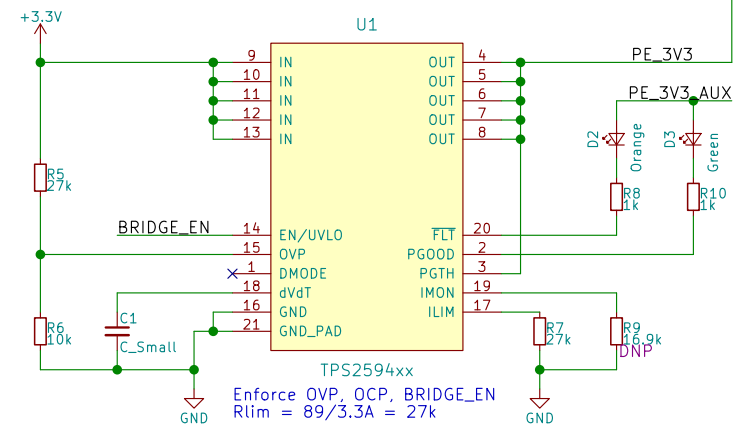
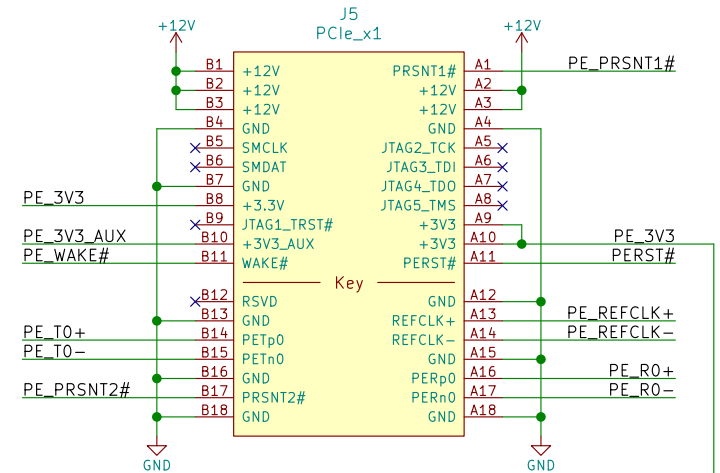
# Bridge to Host



# Debug Power



# PCIe Rcpt for Dev



$$3.3V * (10k / (10k + 27k)) = 0.89V$$

< Venf 0.9V min for OVP

Sheet: /  
File: pcie-mini-bridge-dev.sch

# Title:

Size: A4  
KiCad E.D.A. kicad 4.0.7

Date:

Rev:  
Id: 1/1