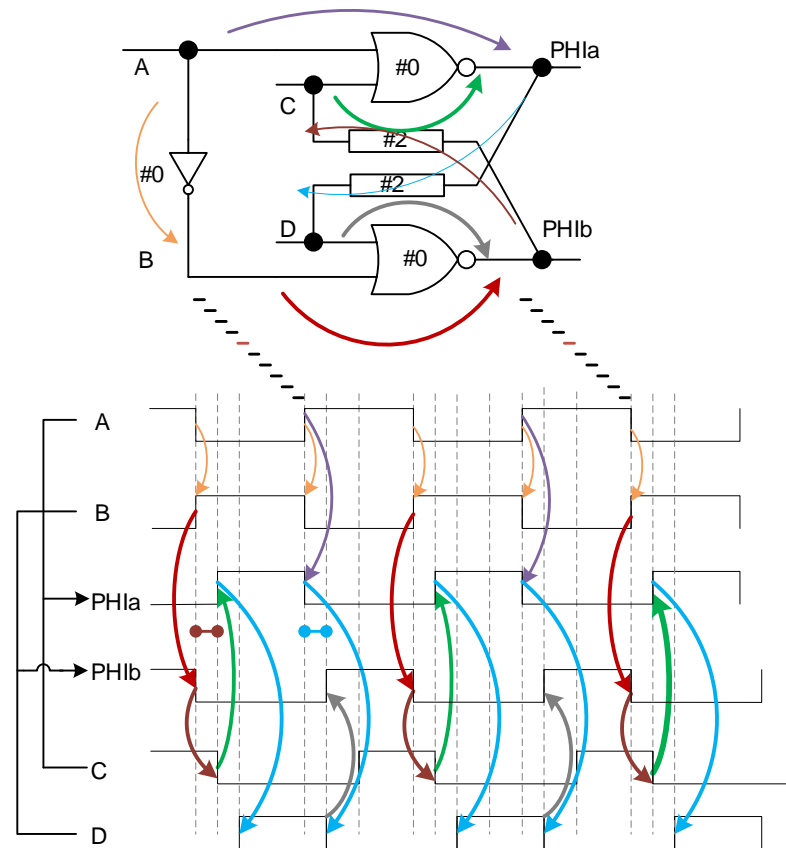


nonoverlapping clocks

1) Classical



2) DWC, **C2PH1a** is important to ensure nonoverlapping and DelayA2B is due to level shifter

