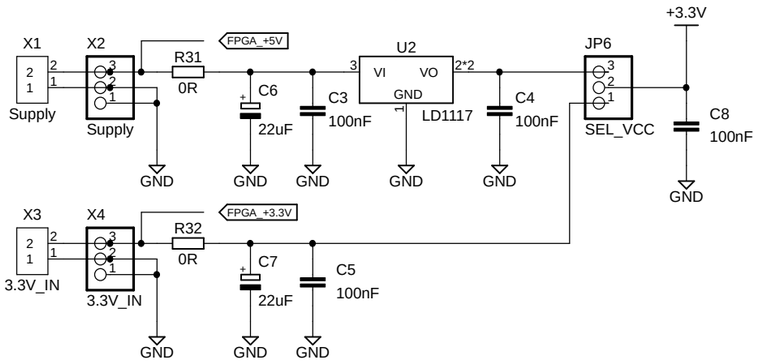
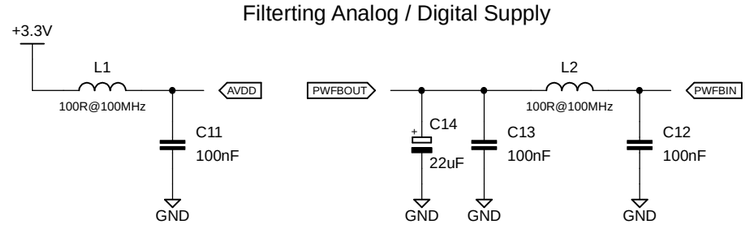
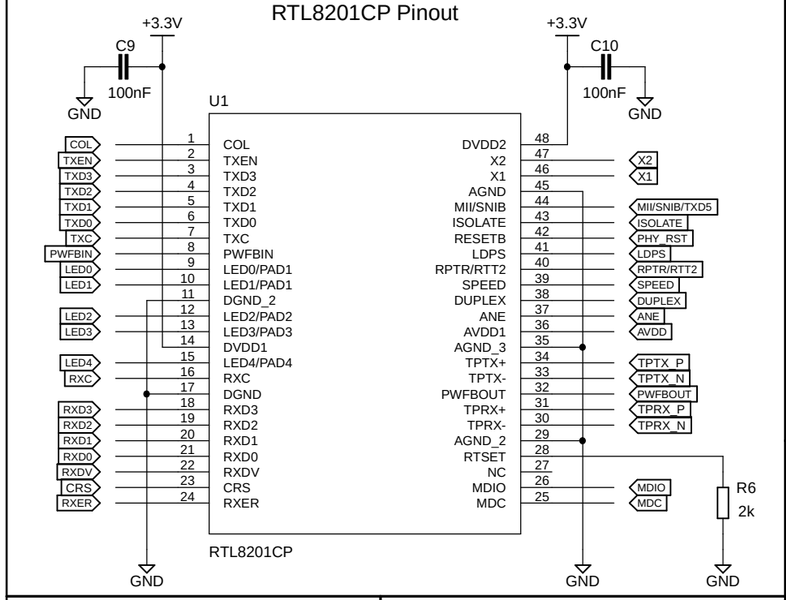
External Supply Voltage



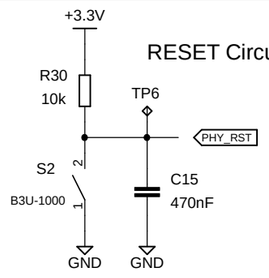
Filtering Analog / Digital Supply



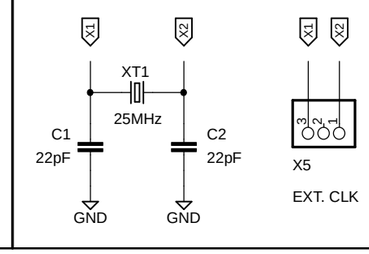
RTL8201CP Pinout



RESET Circuit

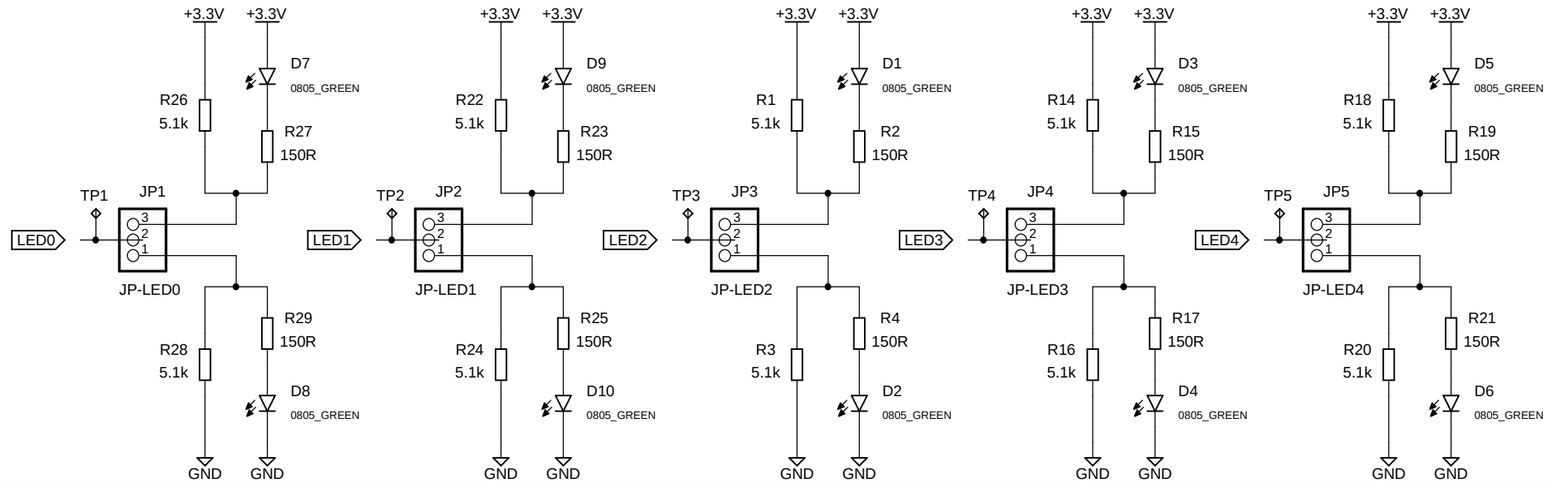


Quartz Circuit



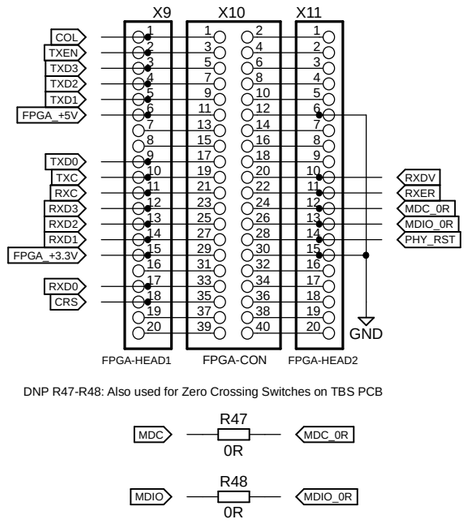
You can set PHY address from 00001b to 11111b with the corresponding Jumpers JP1-JP5.
Refer to datasheet's detailed description.

PHY Address / LED Configuration Interface

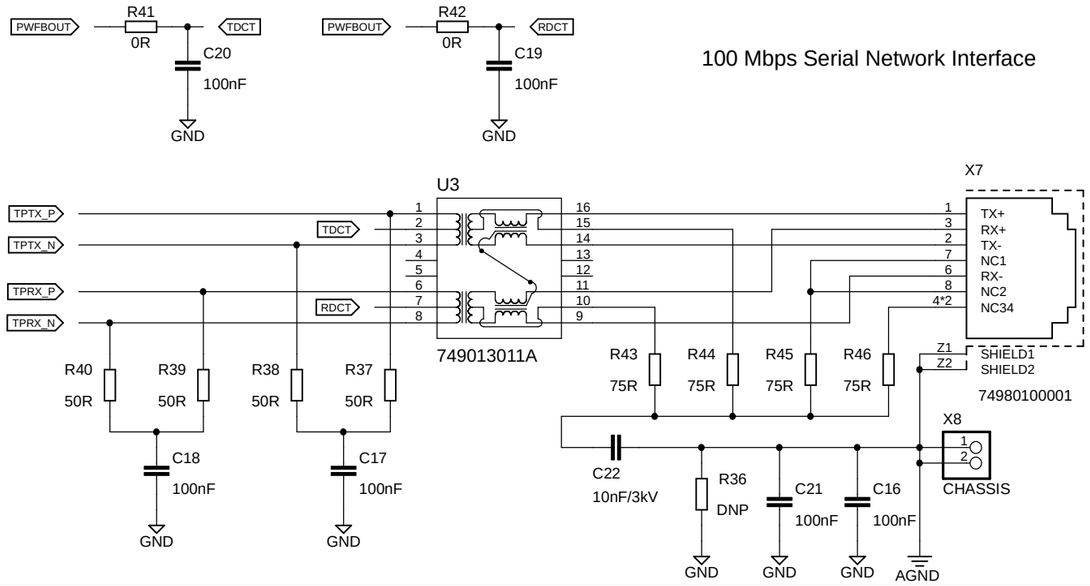


100 Mbps MII & Interface

Length-Matching of TX & RX Data with CLK!

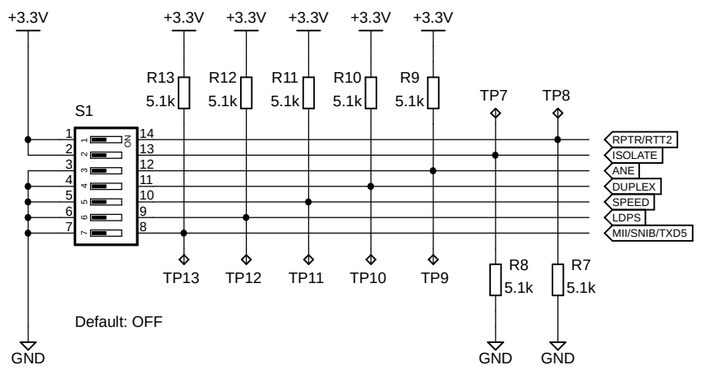


100 Mbps Serial Network Interface

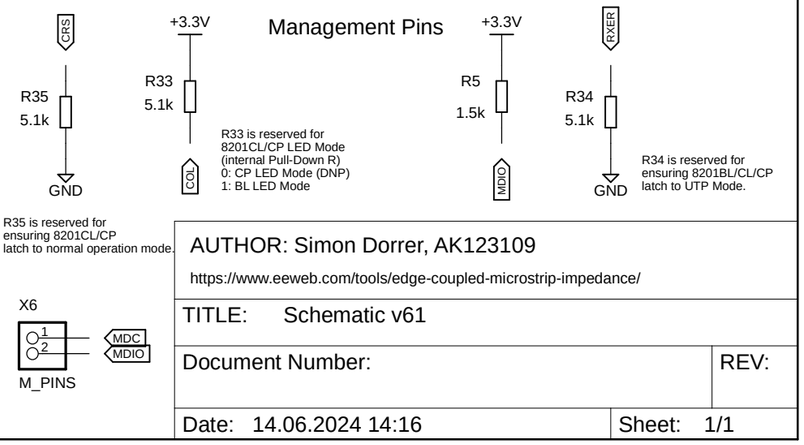


Device Configuration Interface

1. This configuration shows
-) Enable: Auto negotiation, FD 100Mbps, Link Down Power Saving, MII interface
-) Disable: Isolate, Repeater mode
2. These seven configuration pins could be connected to VDD or GND directly.



Management Pins



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https://www.eeweb.com/tools/edge-coupled-microstrip-impedance/

TITLE: Schematic v61

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