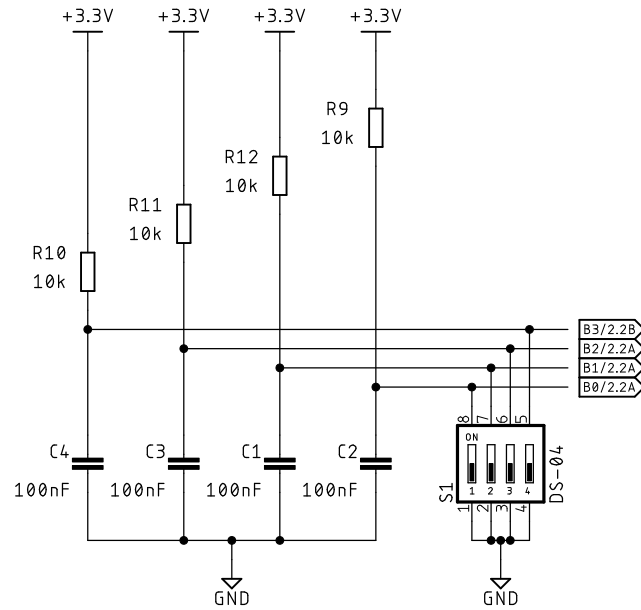
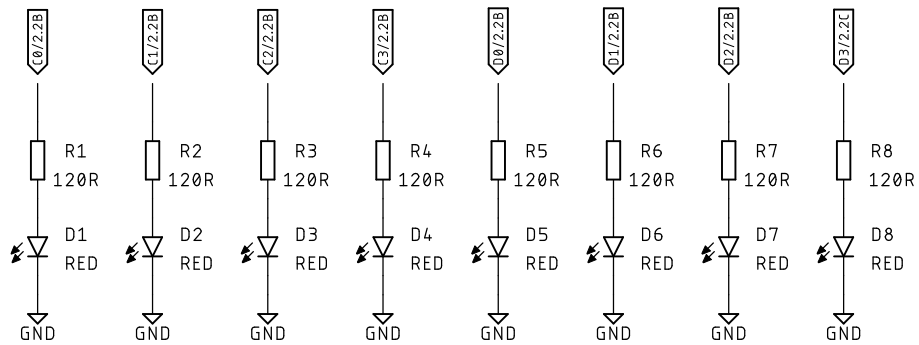


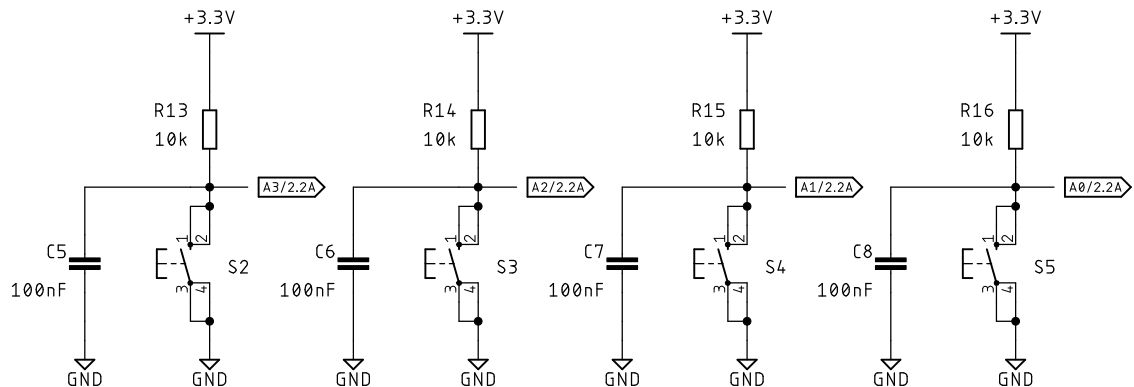
## Switches



## LEDs



## Buttons



by Simon Dorrer, JKU ISP

TITLE: s7-Mini-FPGA Development Board v5

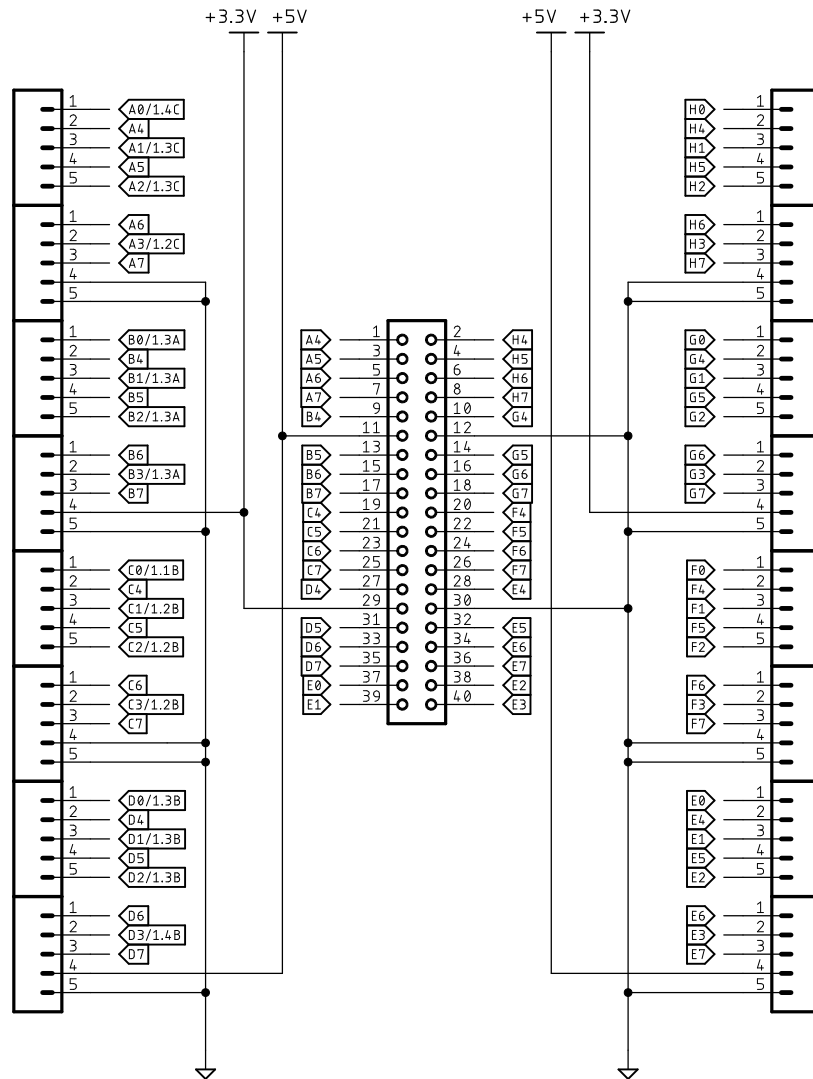
Document Number:

REV:

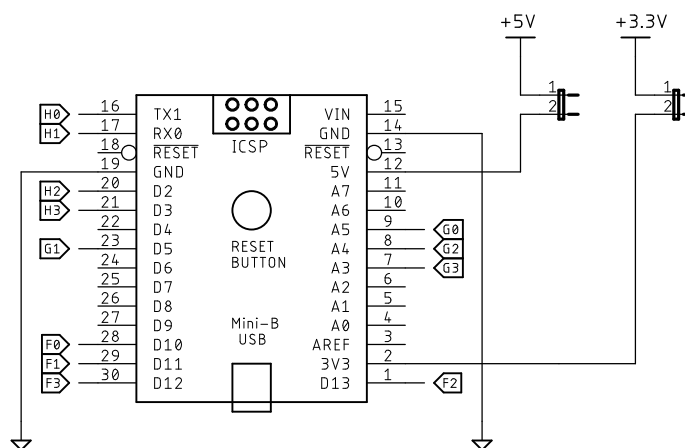
Date: 26.09.2024 11:05

Sheet: 1/2

## 80-Pin s7-Mini FPGA-Connector & 40-Pin Task-Board Connector



## Arduino Nano Connector



by Simon Dorrer, JKU ISP

TITLE: s7-Mini-FPGA Development Board v5

Document Number:

REV:

Date: 26.09.2024 11:05

Sheet: 2/2