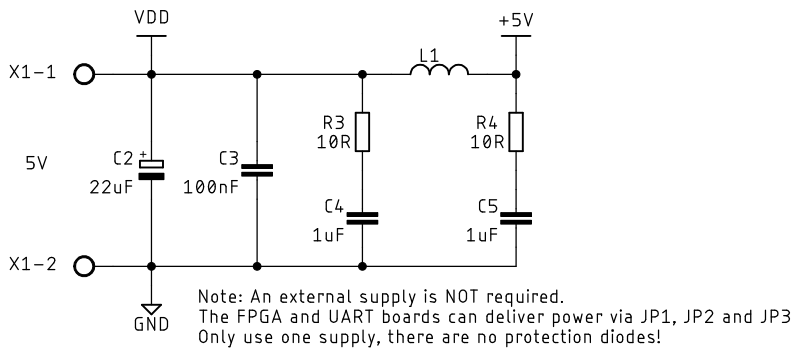


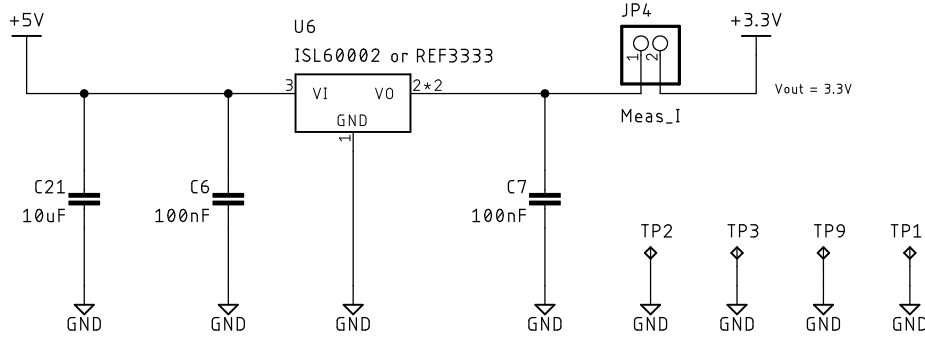
Threshold Based Sampling (Analog Front-End)

Asymmetric Power Supply & Filtering

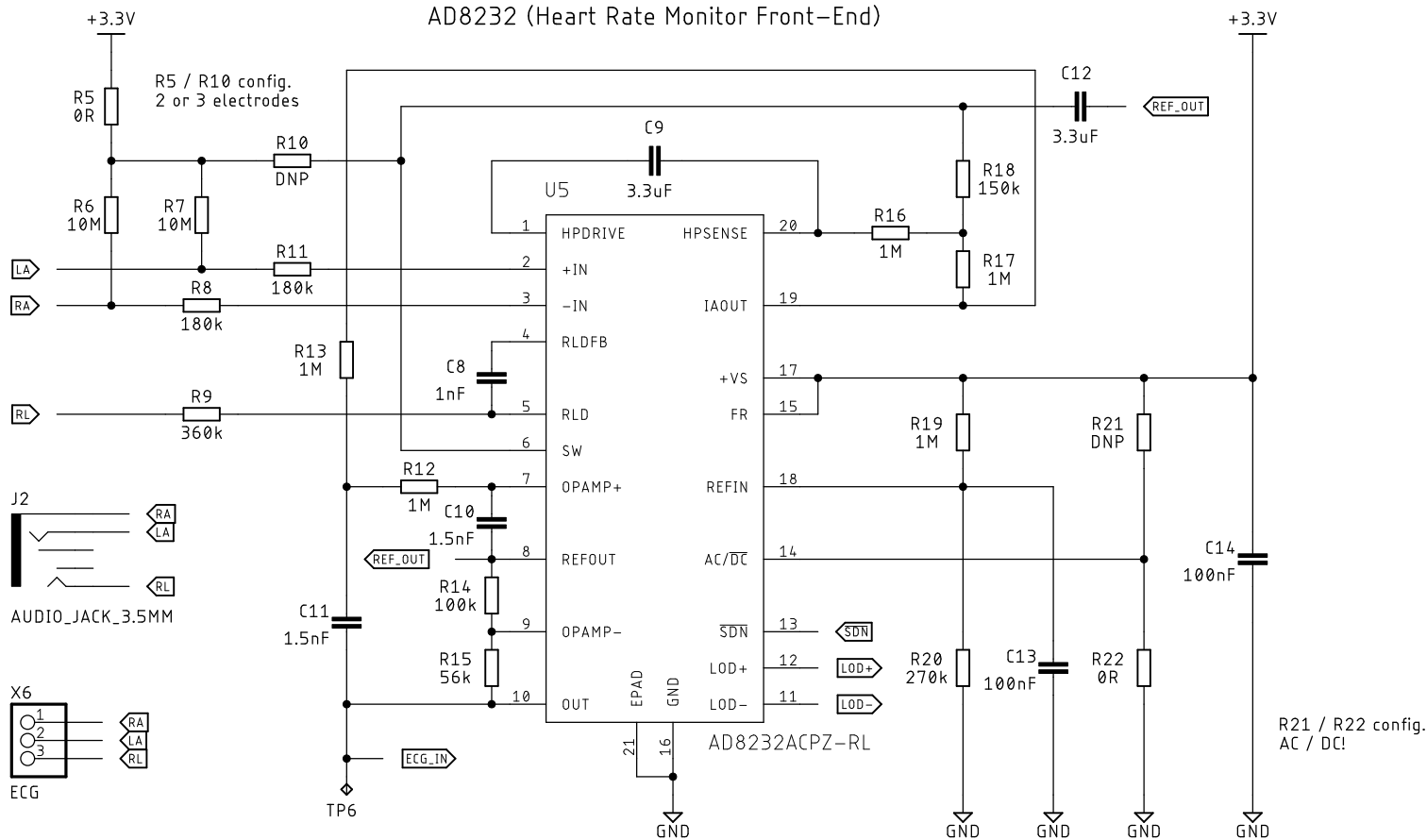


3.3V Precision Voltage Reference

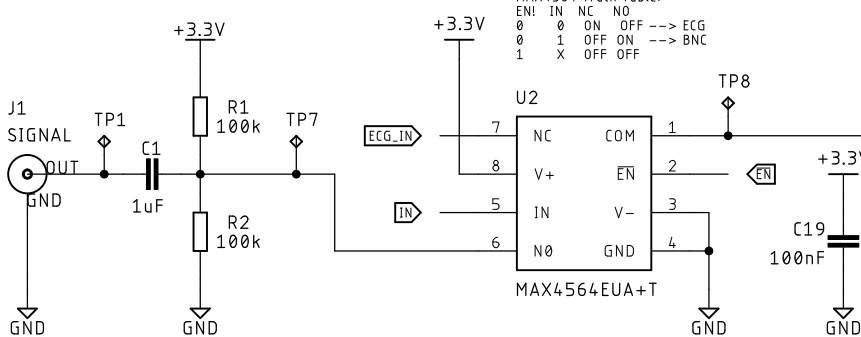
Note: Additional GND Pad on PCB for SOT-23 and SOT-223 compatibility.



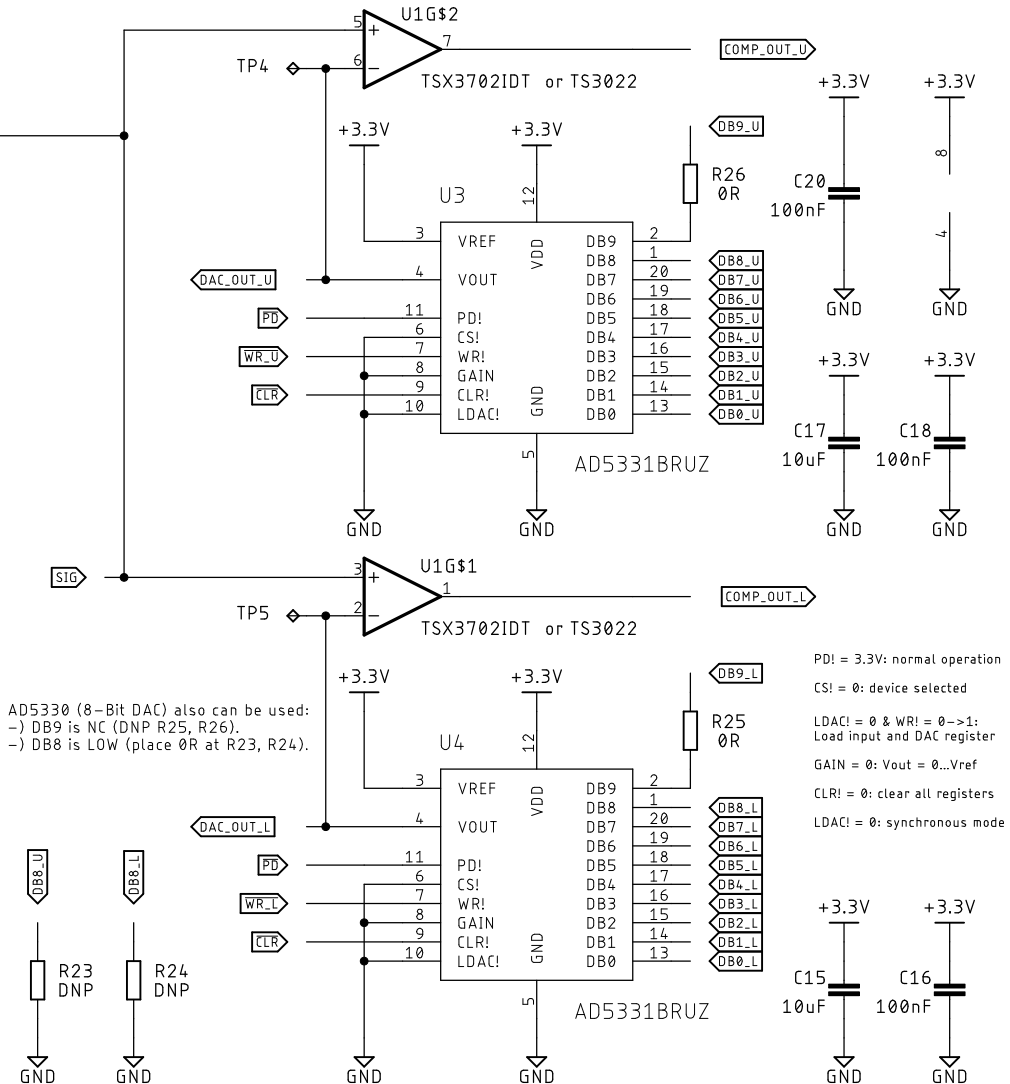
AD8232 (Heart Rate Monitor Front-End)



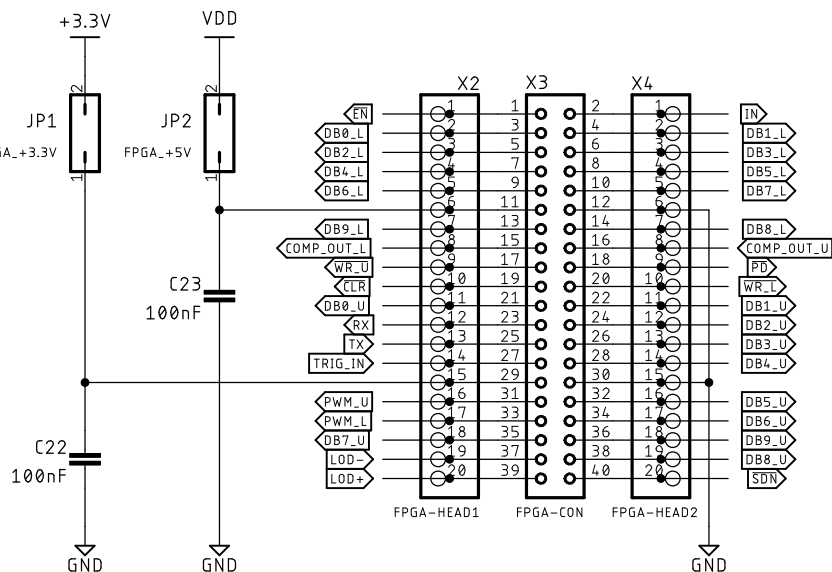
BNC Connector with Input Selection Switch



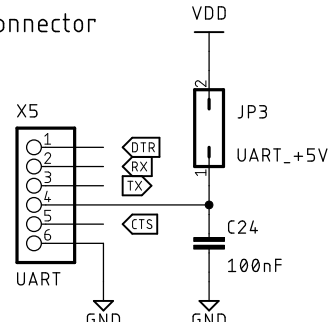
AD5331 (Parallel In Buffered Voltage-Out 10-Bit DAC) & TSX3702 (Comparator)



FPGA-Connector + Pinheader



UART Connector



TBS Analog Front-End
by Simon Dorrer
Master-Thesis, JKU
05.2024

NAME: Simon Dorrer, AK123109

TITLE: TBS-Analog_Front-End v55

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