```
NGSPICE CTRL
                                                                     NGSPICE SAVE
 .option sparse
                                                                       .save b d a n
                                                                       .save @n.xml.nsg13 lv nmos[cgsol]
 .temp 27
                                                                       .save @n.xml.nsg13 lv nmos[cgdol]
 .param wx=5u lx=0.13u vbx=0
                                                                       .save @n.xml.nsg13 lv nmos[cdd]
 .noise v(n) vg lin 1 1 1 1
                                                                       .save @n.xml.nsg13 lv nmos[cgb]
 .control
 option numdgt=3
                                                                       .save @n.xml.nsg13 lv nmos[cgd]
                                                                       .save @n.xml.nsg13 lv nmos[cgg]
 set wr singlescale
                                                                       .save @n.xml.nsg13 lv nmos[cgs]
 set wr vecnames
                                                                       .save @n.xml.nsg13 lv nmos[css]
                                                                       .save @n.xml.nsg13 lv nmos[gds]
 compose l vec values 0.13u 0.2u 0.3u 0.4u 0.5u 1u 5u 10u
 compose vg vec start= 0 stop=1.5 step=25m
                                                                       .save @n.xml.nsq13 lv nmos[qm]
                                                                       .save @n.xml.nsg13 lv nmos[gmb]
 compose vd vec start= 0 stop=1.5 step=25m
compose vb vec values 0 0.4 0.8 1.2
                                                                       .save @n.xml.nsg13 lv nmos[ids]
                                                                       .save @n.xml.nsg13 lv nmos[l]
                                                                       .save @n.xml.nsg13 lv nmos[vgs]
 foreach var1 $&l vec
                                                                       .save @n.xml.nsg13 lv nmos[vds]
   alterparam lx=\subsection var1
                                                                       .save @n.xml.nsg13 lv nmos[vsb]
   reset
                                                                       .save @n.xml.nsg13 lv nmos[vth]
   foreach var2 $&vg vec
                                                                       .save @n.xml.nsg13 lv nmos[vdss]
     alter vg $var2
     foreach var3 $&vd vec
                                                                       .save @n.xml.nsg13 lv nmos[fug]
       alter vd $var3
                                                                       .save @n.xml.nsg13 lv nmos[sid]
                                                                       .save @n.xml.nsg13 lv nmos[sfl]
       foreach var4 $&vb vec
         alter vsb $var4
                                                                       .save @n.xml.nsg13 lv nmos[cjd]
                                                                       .save @n.xml.nsg13 lv nmos[cjs]
         wrdata techsweep sg13 lv nmos.txt noise1.all
                                                                       .save @n.xml.nsg13 lv nmos[rg]
         destroy all
         set appendwrite
         unset set wr vecnames
       end
     end
   end
                                                simulate
 end
                                                annotate OP
 set appendwrite=0
 alterparam lx=0.13u
 alterparam vbx=0
                                                                                                        vdss(vds sat)=
                                                                         w = \{wx\}
 reset
                                                                         I=\{|x|\}
 *showmod
                                                                         sg13 lv nmos
 show
                                                                                                            fug(f t)=
 write techsweep sg13g2 lv nmos.raw
                                                                                  {vbx}
 .endc
                                                            0.65 AC 1
                                                                                  vsb
MODEL
                                                                                                               sid=
 .lib cornerMOSlv.lib mos tt
```

