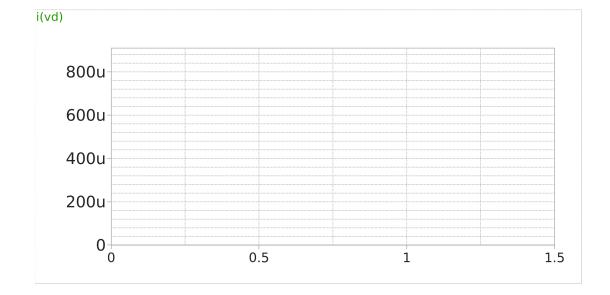
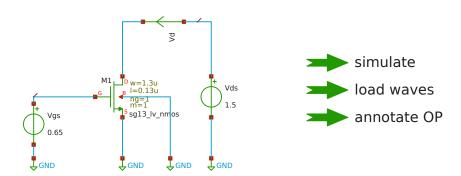
NGSPICE .temp 27 .include dc_lv_nmos.save .control save all op write dc_lv_nmos.raw set appendwrite dc Vds 0 1.5 0.01 Vgs 0 1.5 0.1 write dc lv nmos.raw



M1 Γ_?



MODEL

quit .endc

.lib cornerMOSlv.lib mos_tt

