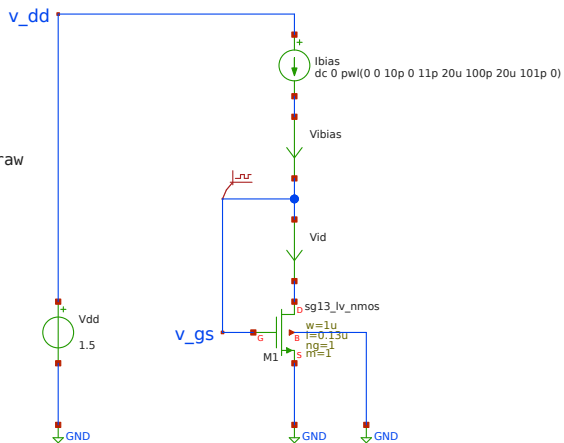


NGSPICE

```
.temp 27
.control
option sparse
save all
tran 0.1p 300p
write mosfet_diode_settling.raw
.endc
```



→ simulate

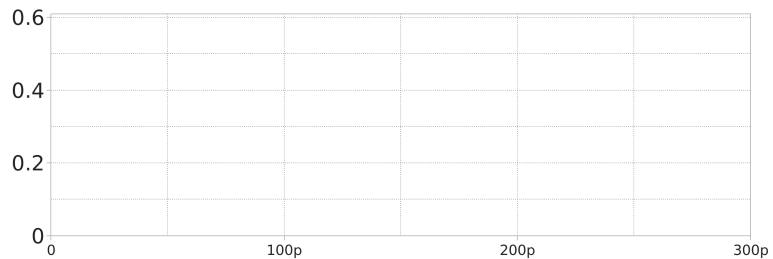
→ load waves

i(vibias)

i(vid)



v(v_gs)



MODEL

```
.lib cornerMOSlv.lib mos_tt
```

XSCHEM

Copyright 2024 Harald Pretl
mosfet_diode_settling.sch

2024-11-19 16:52:32