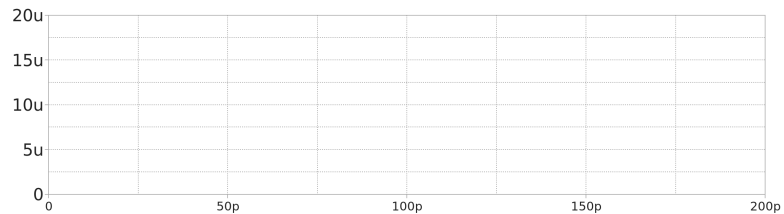


## NGSPICE

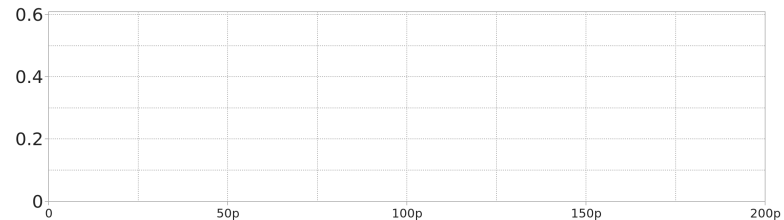
```
.temp 27
.control
option sparse
save all
save i(ibias)
tran 0.1p 200p
write mosfet_diode_settling.raw
.endc
```



i(@ibias[current])



v\_gs



→ simulate

→ load waves

## MODEL

```
.lib cornerMOSlv.lib mos_tt
```



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2024-08-27 17:40:05

/foss/designs/xschem/mosfet\_diode\_settling.sch