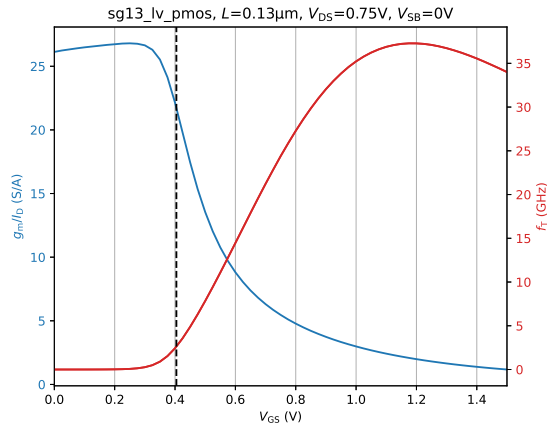
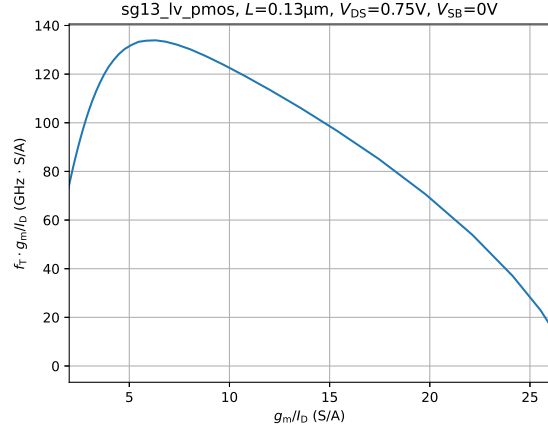


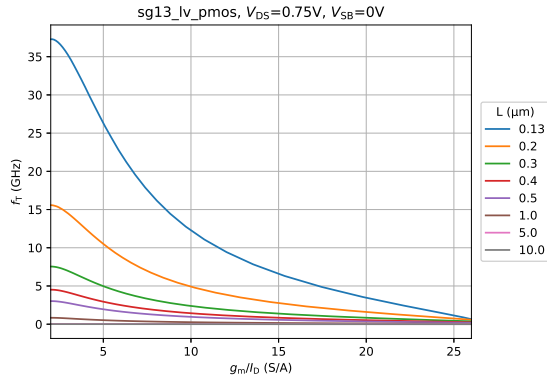
Figure 1: Characterization plots of SG13G2 NMOST for the g_m/I_D methodology.



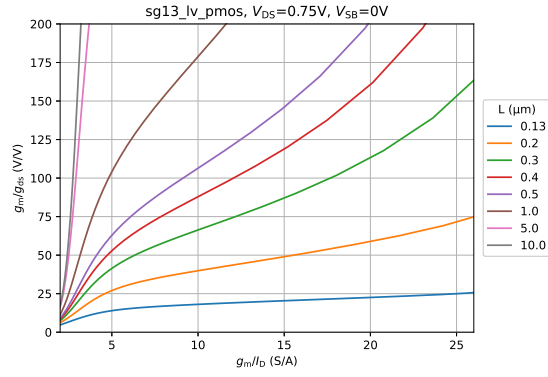
(a)



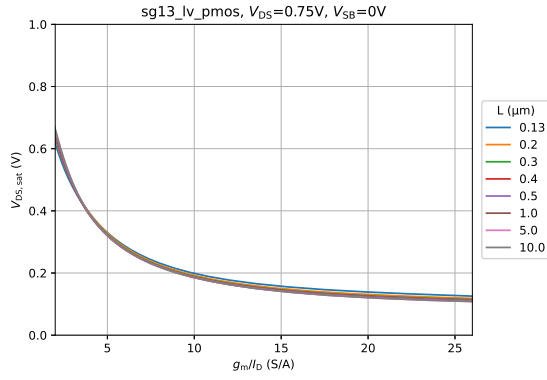
(b)



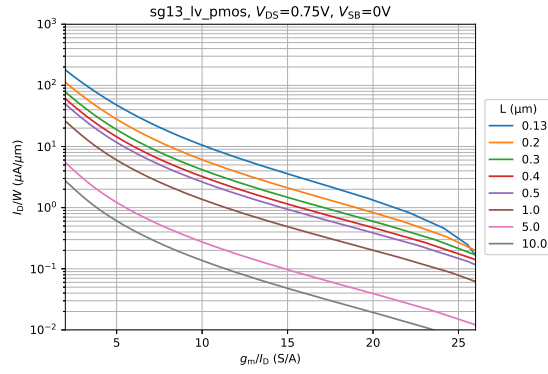
(c)



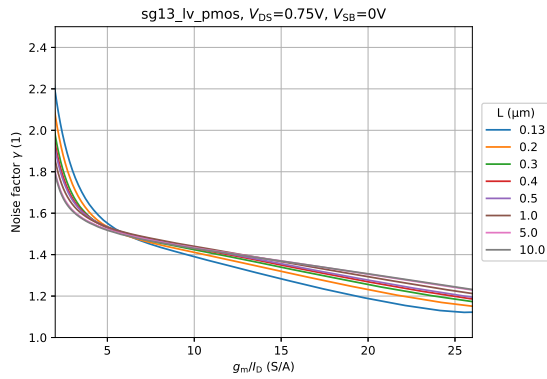
(d) Modelling issue, use NMOS data



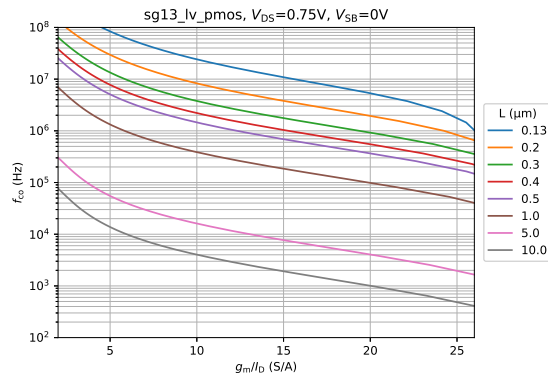
(e)



(f)



(g)



(h)

Figure 2: Characterization plots of SG13G2 PMOST for the g_m/I_D methodology.