

NGSPICE

```
.temp 27
.control
option sparse
save all
tran 0.1p 300p
write mosfet_diode_settling.raw
.endc
```



→ simulate

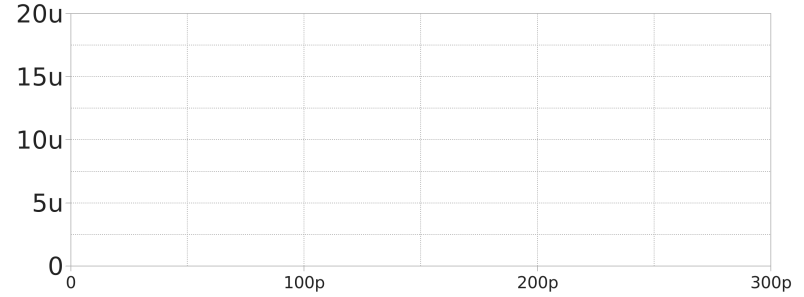
→ load waves

MODEL

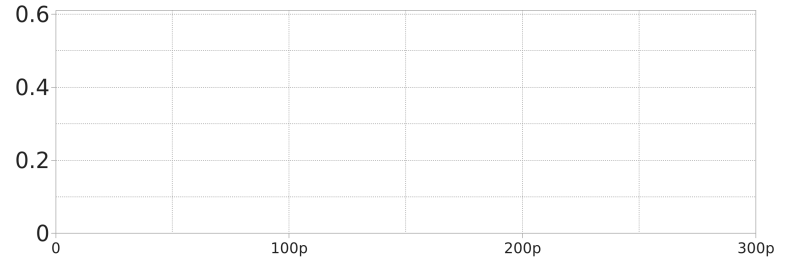
```
.lib cornerMOSlv.lib mos_tt
```

i(vibias)

i(vid)



v(v\_gs)



(c) 2024 H. Pretl, Apache-2.0 license  
mosfet\_diode\_settling.sch

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