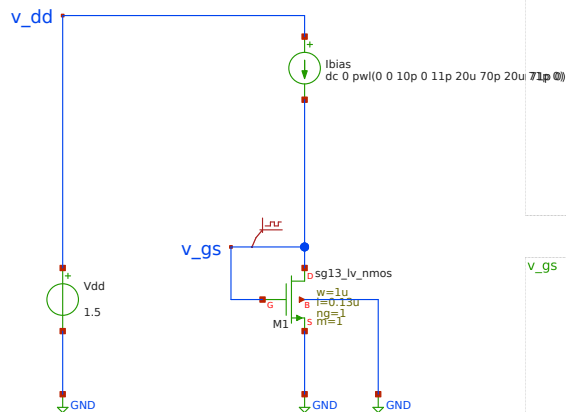
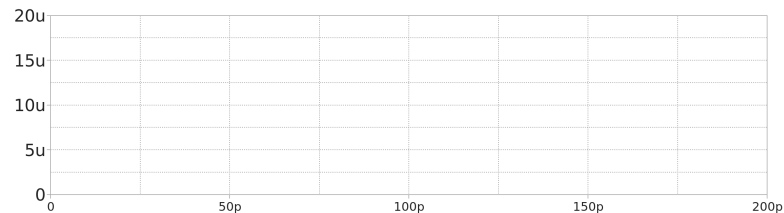


NGSPICE

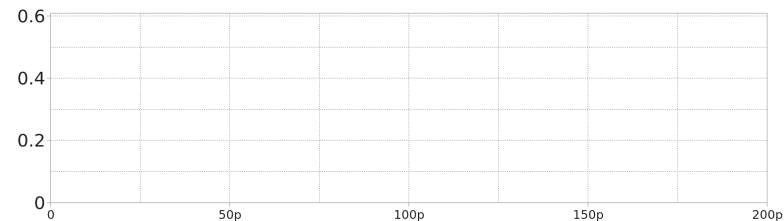
```
.param temp=27
.control
option sparse
save all
save i(ibias)
tran 0.1p 200p
write mosfet_diode_settling.raw
.endc
```



i(@ibias[current])



v_gs



→ simulate

→ load waves

MODEL

```
.lib cornerMOSlv.lib mos_tt
```



Copyright 2024 Harald Pretl

/foss/designs/xschem/mosfet_diode_settling.sch

2024-08-20 18:05:54