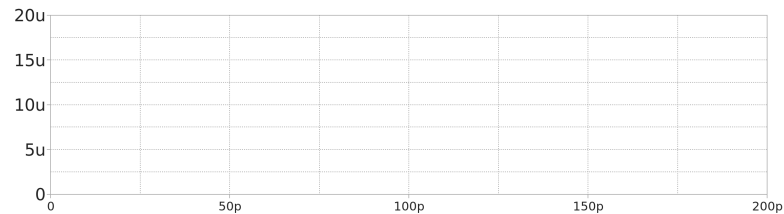NGSPICE
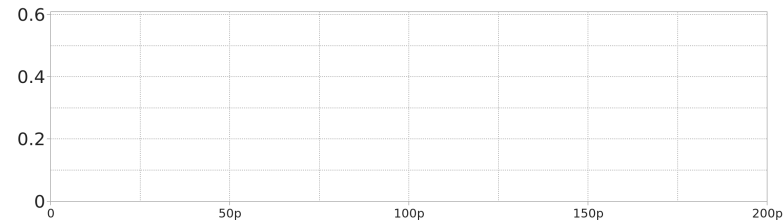
```
.temp 27
.control
option sparse
save all
save i(ibias)
tran 0.1p 200p
write mosfet_diode_settling.raw
.endc
```

v_dd

Ibias
dc 0 pwl(0 0 10p 0 11p 20u 70p 20u 71p 0))

v_gs

sg13_lv_nmos
w=1u
l=0.13u
ng=1
m=1
M1

Vdd
1.5

GND

GND

GND

i(@ibias[current])

20u

15u

10u

5u

0

0   50p   100p   150p   200p

v_gs

0.6

0.4

0.2

0

0   50p   100p   150p   200p

➡ simulate   ➡ load waves

MODEL
.lib cornerMOSlv.lib mos_tt

XSCHEM
mosfet_diode_settling.sch

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