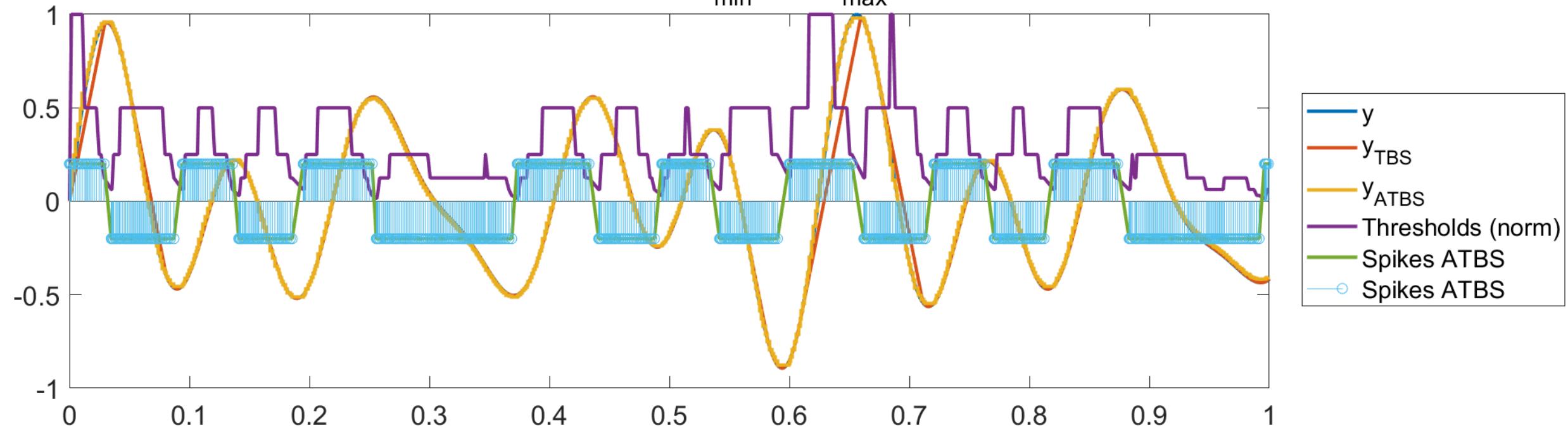


Matlab Co-Simulation ($D_{\min} = 2$, $D_{\max} = 2$)



VHDL Simulation ($D_{\min} = 2$, $D_{\max} = 2$)

