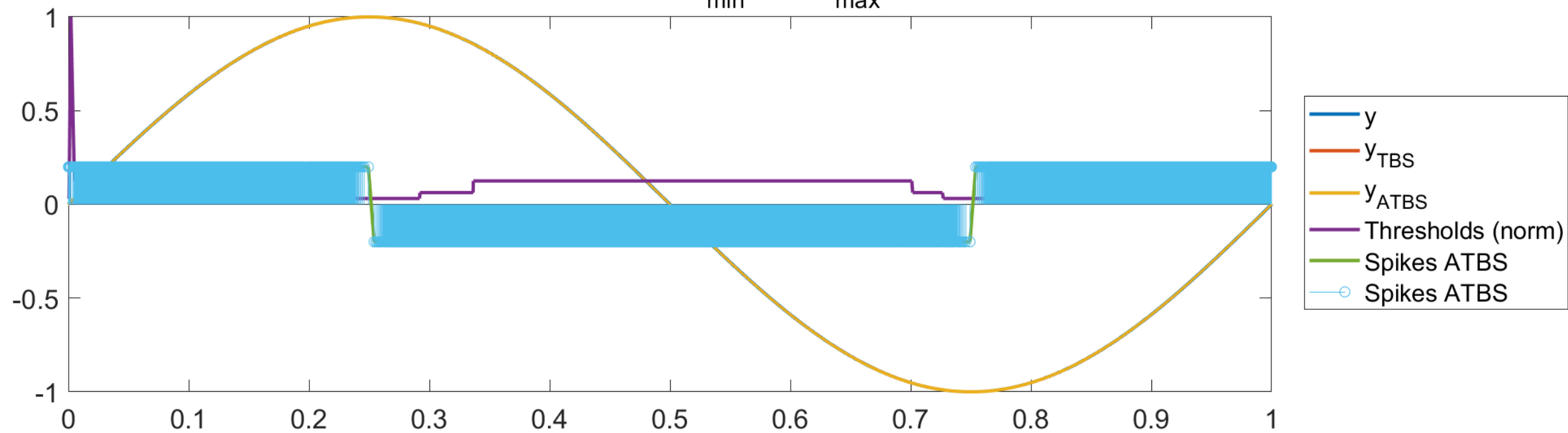


Matlab Co-Simulation ( $D_{\min} = 4$ ,  $D_{\max} = 10$ )



VHDL Simulation ( $D_{\min} = 4$ ,  $D_{\max} = 10$ )

