OTA:

SHORT
PMOS:
W = 54µm
L = 1µm
NF = 6
W_NF = 9µm

LONG
PMOS:
W = 63µm
L = 1µm
NF = 7
W_NF = 9µm

NMOS:
W = 18µm
L = 1µm
NF = 6
W_NF = 3µm
# 3

NMOS:
W = 21µm
L = 1µm
NF = 7
W_NF = 3µm
# 9

= 18 Finger + 63 Finger = 81 Finger

Filler: (132µm x 45µm = 5940µm²)

Floorplan

Resistors (high poly, W = 0,35µm):

R1: L1 = 36µm ≙ 2µm x 18
R2: L2 = 38µm ≙ 2µm x 19
R3: L3 = 126µm ≙ 2µm x 63
R4: L4 = 76µm ≙ 2µm x 38

} L = 2µm x 158

Capacitors (M3, MiM):

C1: W1 = L1 = 3µm, 7 x 10 = 70 (~1,42pF)
C2: W2 = L2 = 3µm, 7 x 6 = 42 (~851fF)
2x C3: W3 = L3 = 3µm, 1 x 5 = 5 (~2x101fF)
2x CDEC: WDEC = LDEC = 3µm, 1 x 11 = 11 (~2x223fF)

↳ Decoupling Capacitors

1x11	1x5
7x10	7x6
1x11	1x5

} 3µm x 3µm
9 x 16 array!