Department of Electronics and Communication Engineering

Tentative Time Table, B.Tech. First Year (Sem 2) (Even Semester 2017-2018), W.E.F- 2/01/2018

Class Room: 228

Day / Time	6:00-6:55	8:00-8:55	9:00-9:55	10:00-10:55	11:00-11:55	12:00-12:55	1:00-1:55	2:00-3:55	4:00-4:55
MON	CAD 101		FCI 103	CSI 102	MAL 102	CSL 103		ECL 102 (Lab)	CSL 102 (Lab)
MON	SAP 101		ECL 102	CSL 102	MAL 102	CSL 103		Batch 1	Batch 1
T 115	CAD 404		ECI 400	651.402	1441 402	CCL 102		ECL 102 (Lab)	CSL 102 (Lab)
TUE	SAP 101		ECL 102	CSL 102	MAL 102	CSL 103		Batch 2	Batch 2
14/50			ECI 400	CCL 402	A4A1 403	CCI 403	RECESS	BSL 101 (Lab)	CSL 103 (Lab)
WED			ECL 102	CSL 102	MAL 102	CSL 103		Batch 1	Batch 1
			DCI 404					BSL 101 (Lab)	CSL 103 (Lab)
THU	1		BSL 101					Batch 2	Batch 2
FRI					BSL	101			

Course Code	Course Name	Faculty / Incharge
MAL 102	Mathematics-II	Anuradha Singh
BSL 101	Applied Sciences	Aatish Daryapurkar
ECL 102	Digital Electronics	(AAPP-3)
CSL 102	Data Structures	Robin singh Bhadoria
CSL 103	Application Programming	Rashmi Janbandhu
SAL 101	Health, Sports, and Safety	Sunil Dhole
HUL 102	Environmental Studies	D. Lataye



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Department of Electronics and Communication Engineering Tentative Time Table, B.Tech. Eirst Year (Sem 4) (Even Semester 2017-2018), W.E.F- 2/01/2018

Class Room: 227

Day / Time	6:00-6:55	8:00-8:55	9:00-9:55	10:00-10:55	11:00-11:55	12:00-12:55	1:00-1:55	2:00-3:55	4:00-4:55
MON		ECL 303	ECL 301	ECL 304	ECL 302		ECL 305	ECL 301 (Lab)	ECL 303 (Lab)
TUE		ECL 303	ECL 301	ECL 304	ECL 302		ECL 305	ECL 302 (Lab)	ECL 304 (Lab)
WED		ECL 303	ECL 301	ECL 304	ECL 302	RECESS	ECL 305		
THU			EC	L 306			CSP 202		1"
FRI			ECL 306	CSP	202				

Course Code	Course Name	Faculty / Incharge
ECL 301	Digital Signal Processing	Ankit A. Bhurane
ECL 302	Analog Communication	Ankit A. Bhurane
ECL 303	Hardware Description Languages	(AAPP-4)
ECL 304	Control System	B. Rajpathak
ECL 305	Electromagnetics	(AAPP-4)
ECL 306	Computer Architecture and Organization	(AAPP-3)
CSP 202	IT Workshop-II	(AAPP-2)



<u>Department of Computer Science and Engineering</u>

Tentative Time Table, B.Tech. First Year (Sem 2) (Even Semester 2017-2018), W.E.F- 2/01/2018

Class Room: Seminar Hall

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Day / Time	6:00-6:55	8:00-8:55	9:00-9:55	10:00-10:55	11:00-11:55	12:00-12:55	1:00-1:55	2:00-3:55	4:00-4:55
MON		HUL	101	CSL 103	ECL 102	CSL 102		CSL 102 (Lab) Batch 1	ECL 102 (Lab) Batch 1
TUE		ECL 102	MAL 102	CSL 103	BEL	101		CSL 102 (Lab) Batch 2	ECL 102 (Lab) Batch 2
WED		HUL 101	MAL 102	CSL 103	ECL 102	CSL 102	RECESS	CSL 102 (Lab) Batch 3	ECL 102 (Lab) Batch 3
THU		MAL 102	HUL 101	BEL 101	BEL 101			CSL 102 (Lab) Batch 4	ECL 102 (Lab) Batch 4
FRI		CSL 102		.03 (Lab) atch 3	CSL 103 (Lab) Batch 4		2	CSL 103 (Lab) Batch 1	CSL 103 (Lab) Batch 2

Course Code	Course Name	Faculty
MAL 102	Mathematics-II	Anuradha Singh
ECL 102	Digital Electronics	(AAPP-3)
CSL 102	Data Structures	Robin singh Bhadoria
CSL 103	Application Programming	Rashmi Janbandhu
HUL 101	Communication Skills	Guest Faculty
BEL 101	Mechanics and Graphics	S.S. Gadve / A.Y. Vyavhare



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Department of Computer Science and Engineering 2nd Tentative Time Table, B.Tech. First Year (Sem 4) (Even Semester 2017-2018), W.E.F- 2/01/2018

Class Room: 223

Day / Time	6:00-6:55	8:00-8:55	9:00-9:55	10:00-10:55	11:00-11:55	12:00-12:55	1:00-1:55	2:00-3:55	4:00-4:55
MON			CSL 205	CSL 206	CSL 207		CSL 208	CSL 205 (Lab)	
TUE			CSL 205	CSL 206	CSL 207		CSL 208	CSL 207 (Lab)	
WED			CSL 205	CSL 206	CSL 207	RECESS	CSL 208	CSL 208 (Lab)	
THU			CSL 205	CSL 206	HUL 201		CSP 202	CSP 202	
FRI					HUL 201			CSP 202	

Course Code	Course Name	Faculty / Incharge
CSL 205	Design and Analysis of Algorithms	(AAPP-2)
CSL 206	Software Engineering	(AAPP-1)
CSL 207	Operating Systems	(AAPP-2)
CSL 208	Design Principles of Programming Languages	Robin Singh Bhadoria
HUL 201		Y. M. Deshpande
CSP 202	IT Workshop-II	(AAPP-2)



