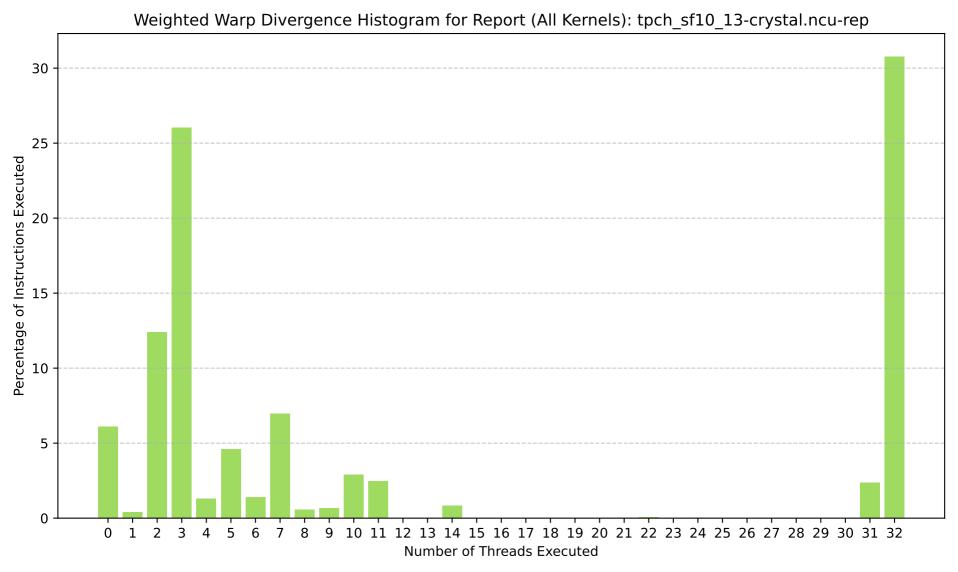


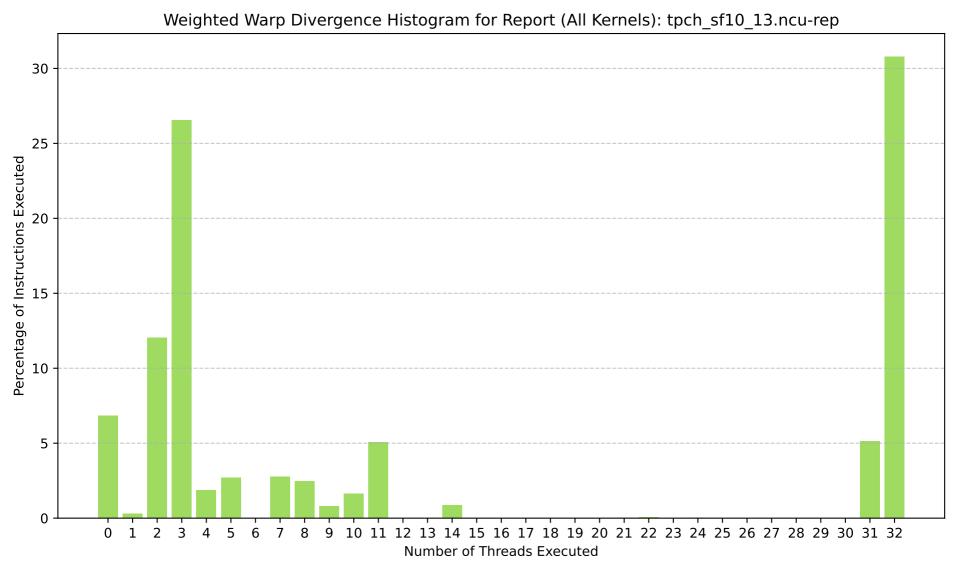
Report: tpch_sf10_13-crystal.ncu-rep Kernel: main 3 - Warp Divergence Histogram Expected Value: 13.06 30 Percentage of Instructions Executed 0 C C C C 5 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 5 6 8

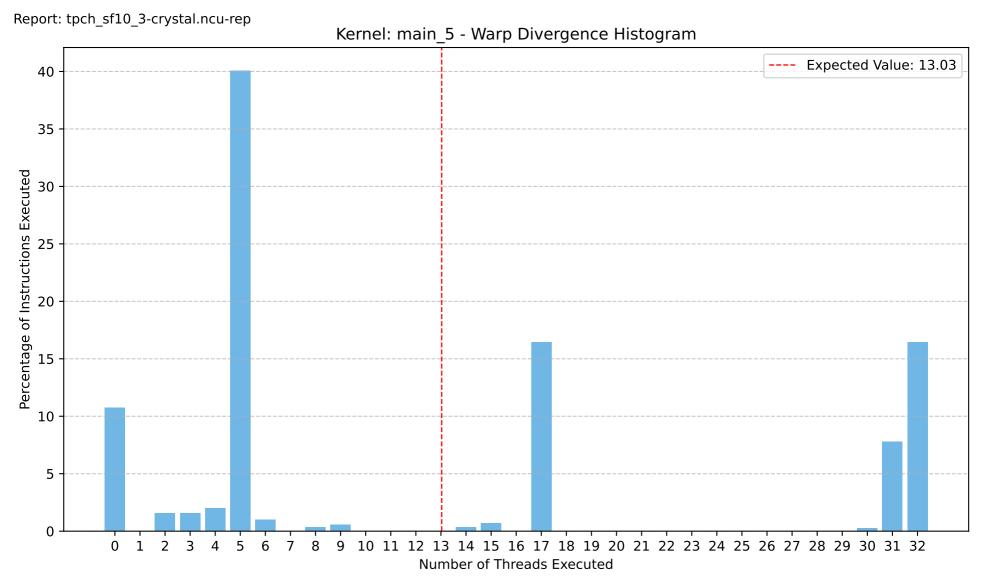
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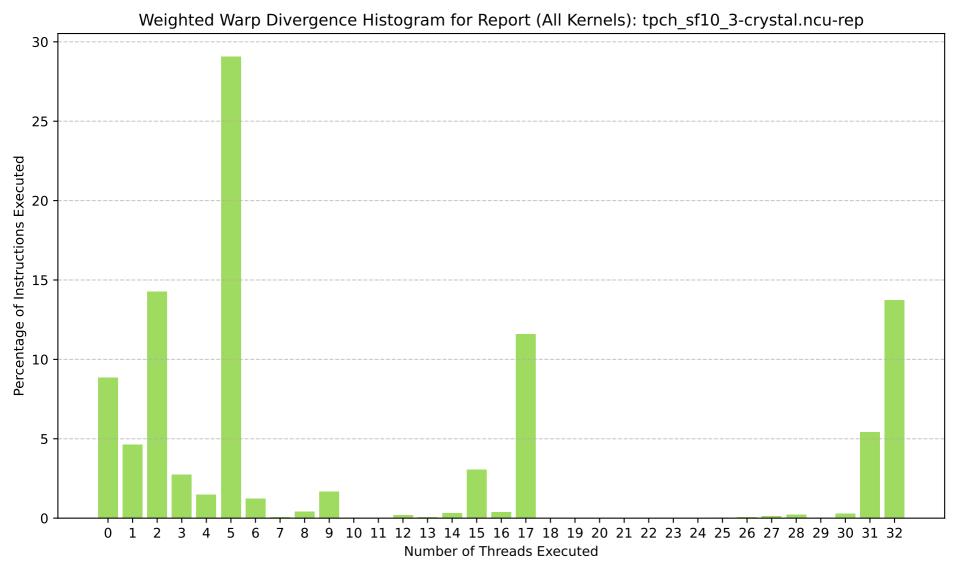


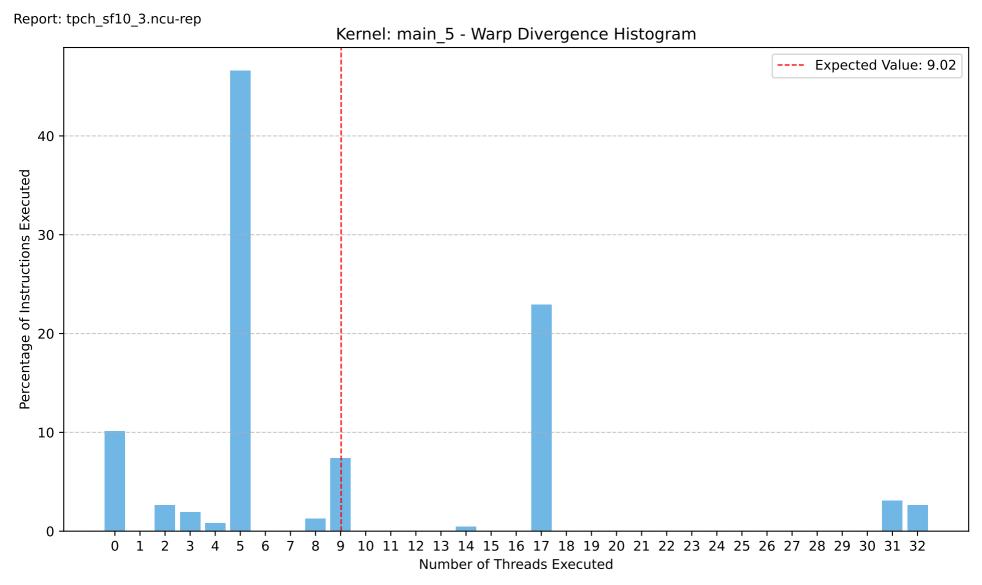
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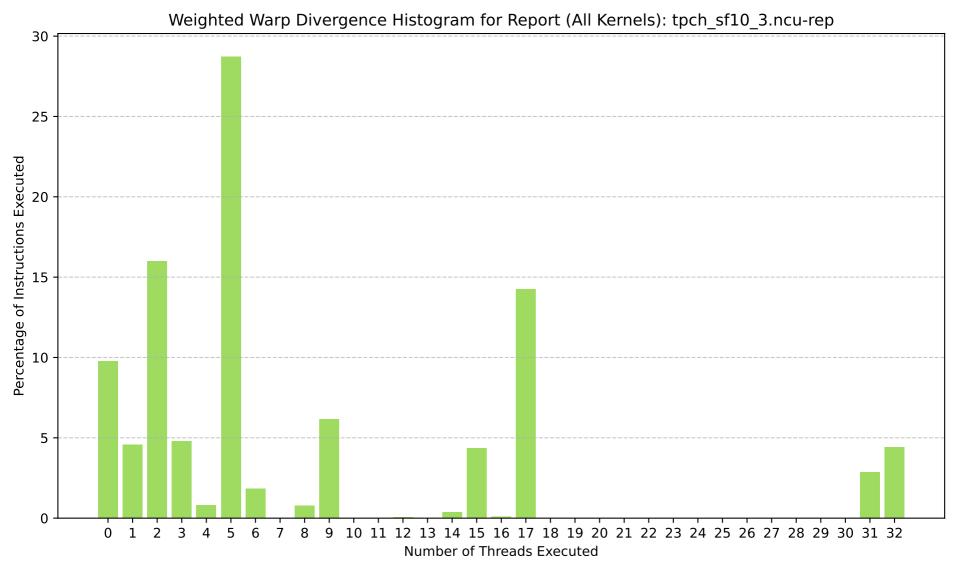
Number of Threads Executed

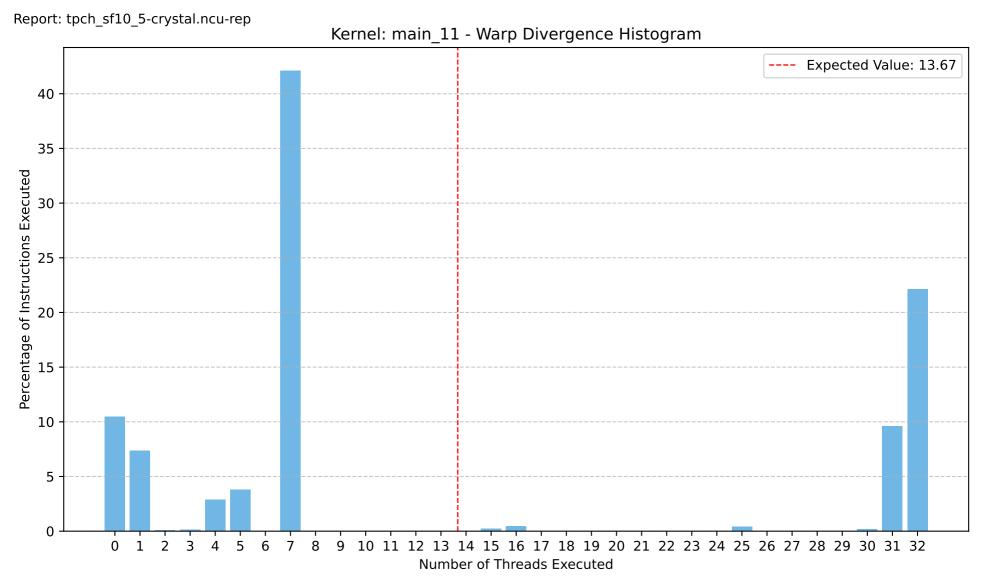


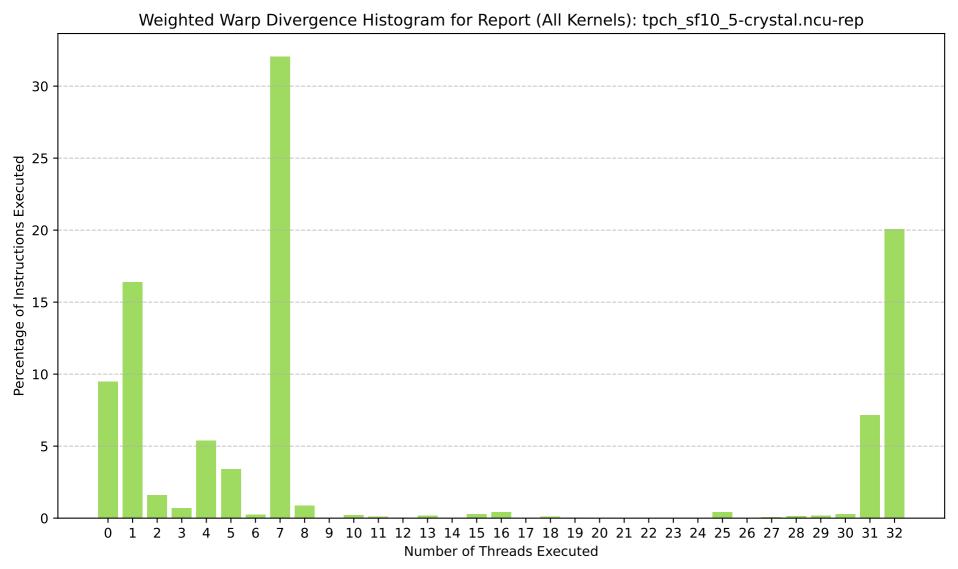


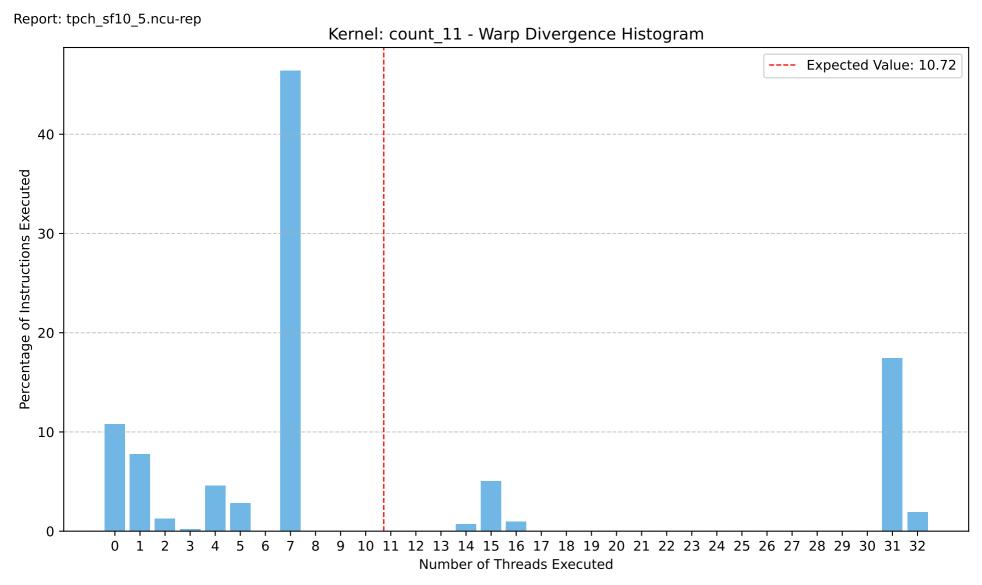


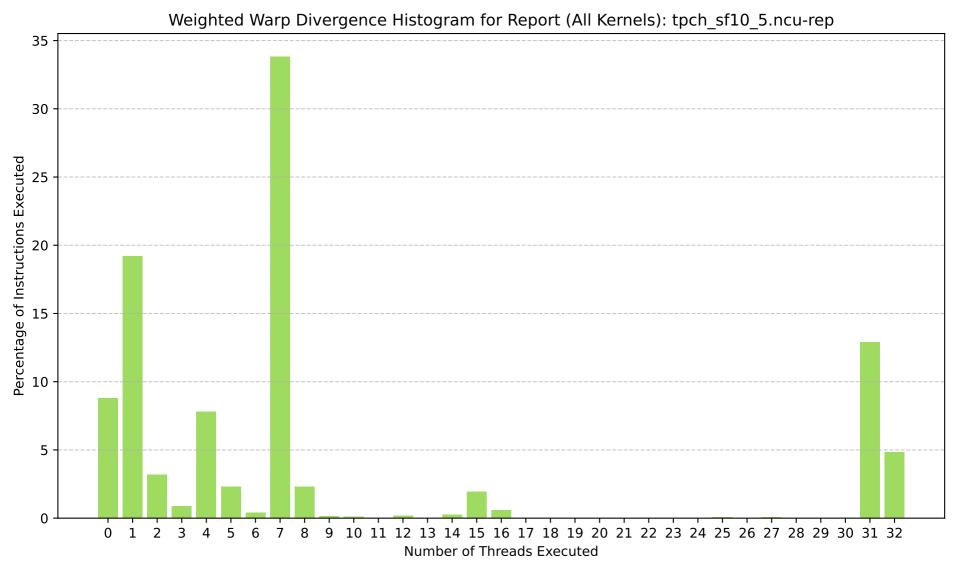


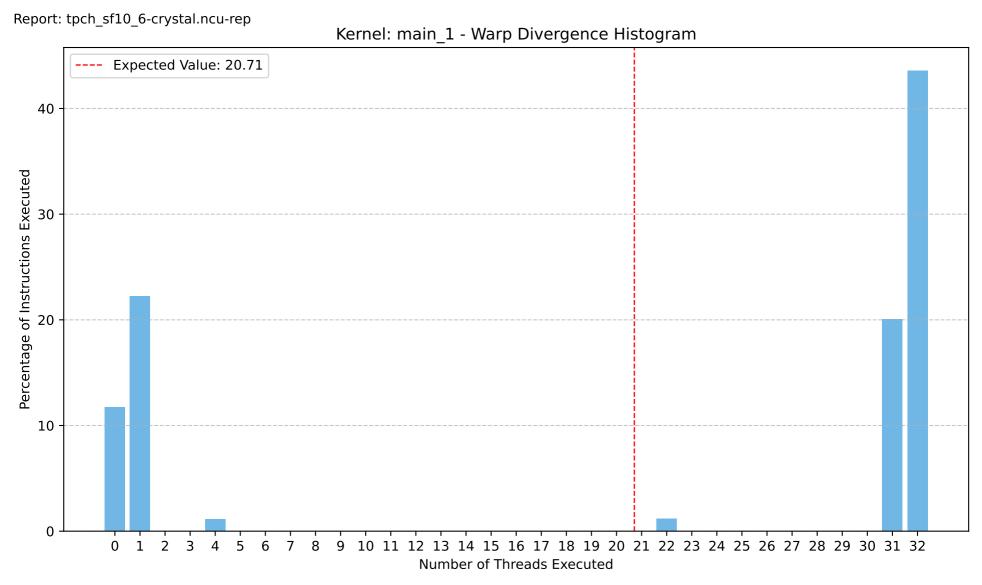


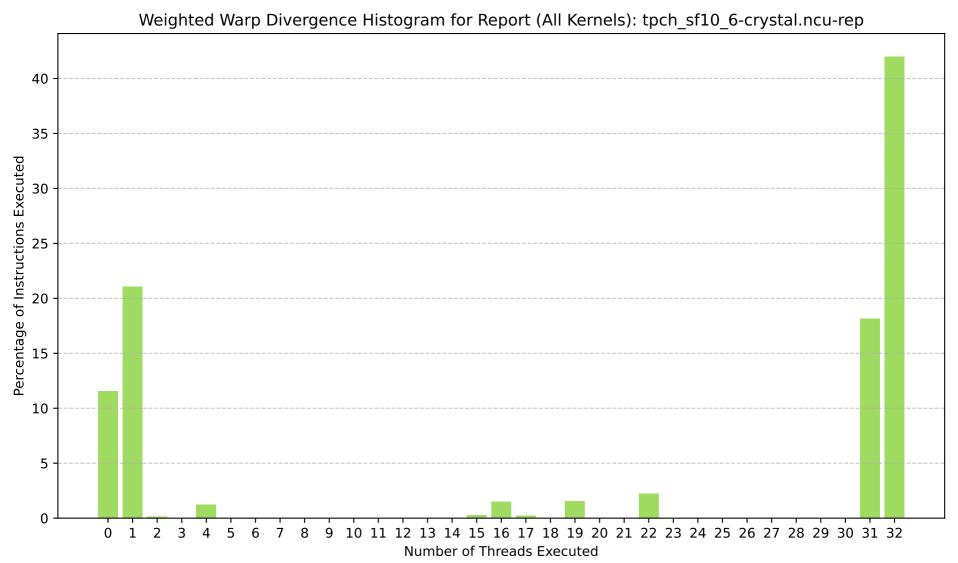


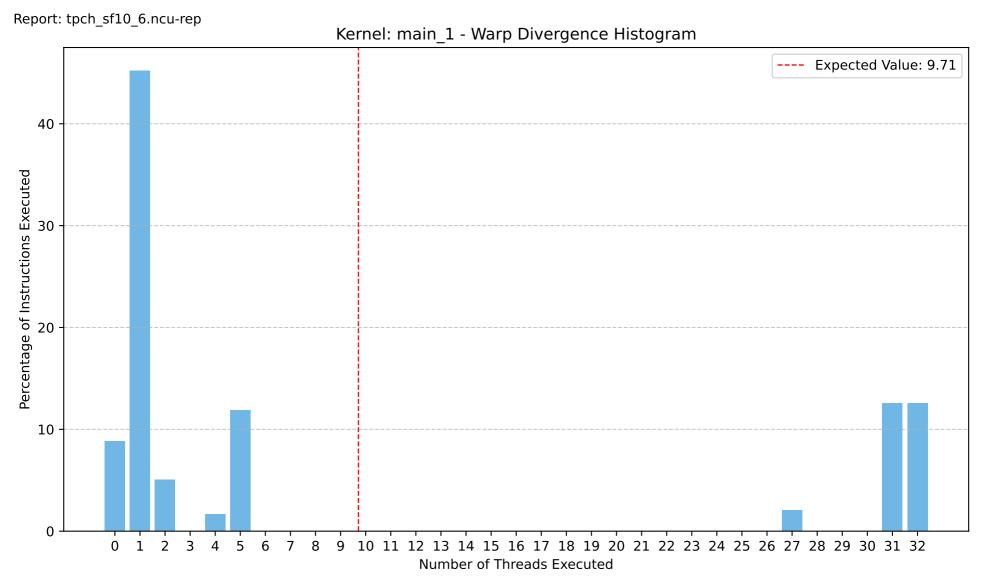


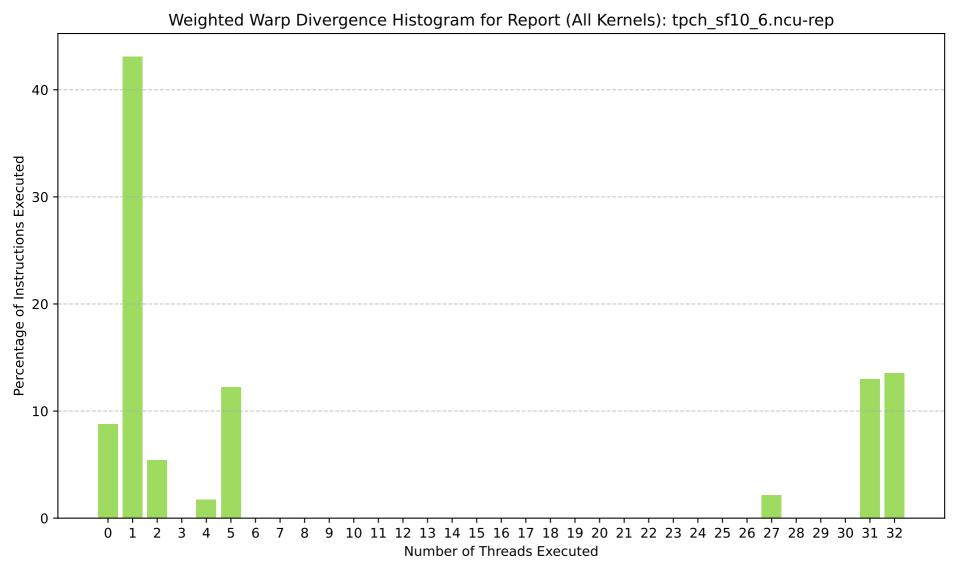


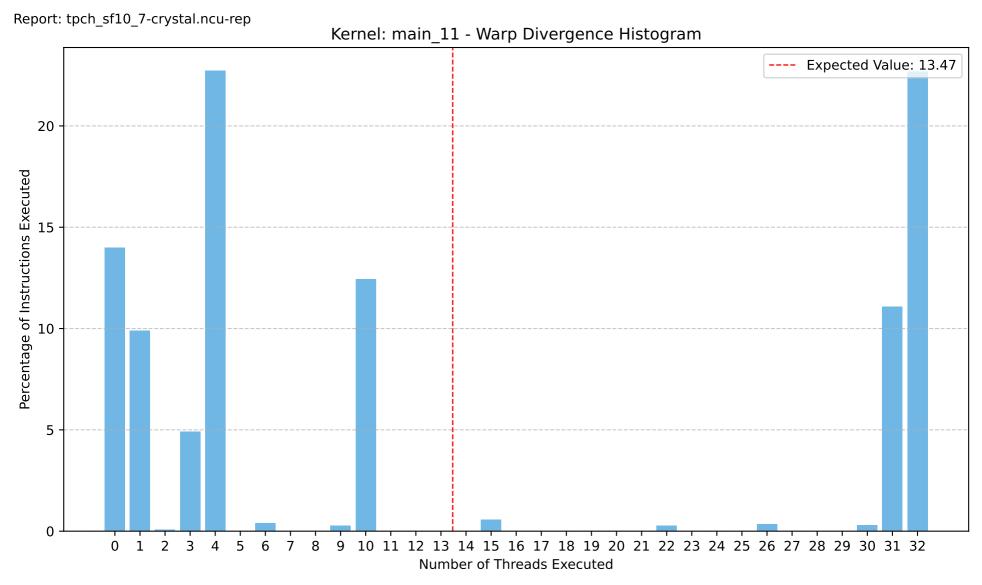


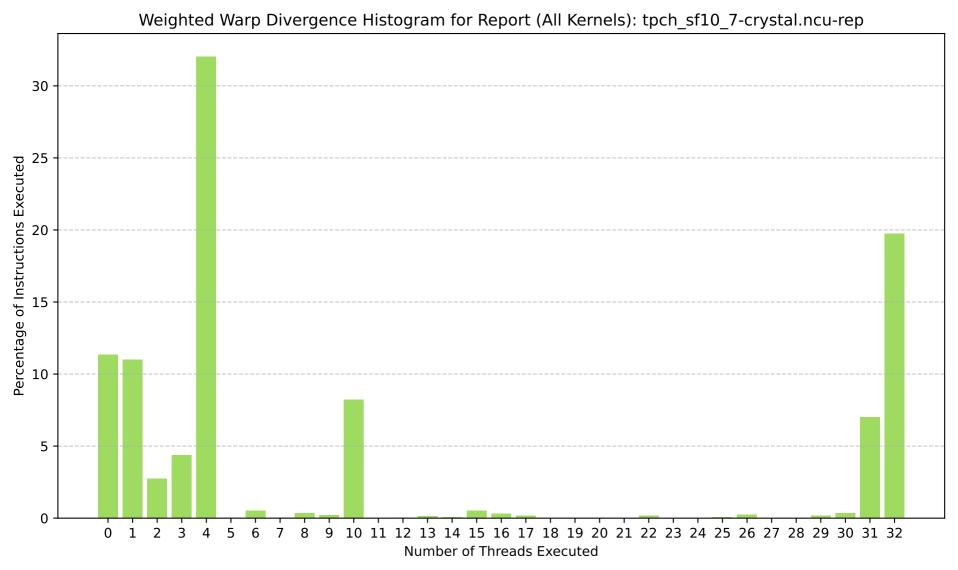


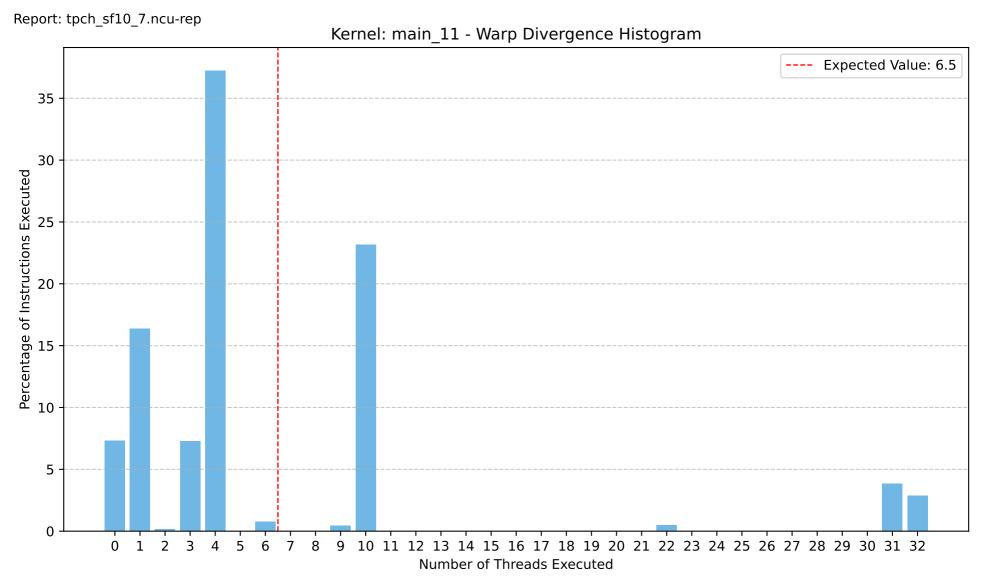


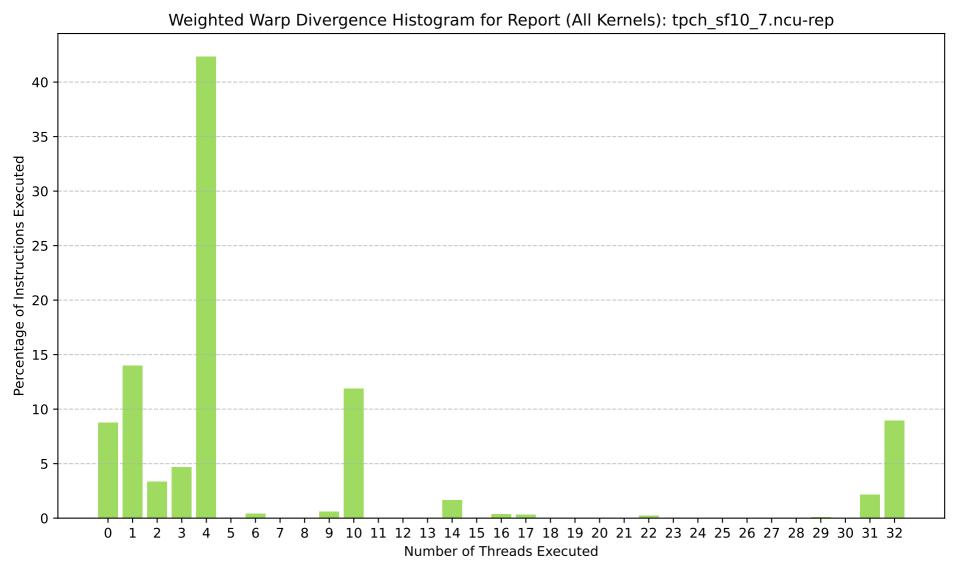


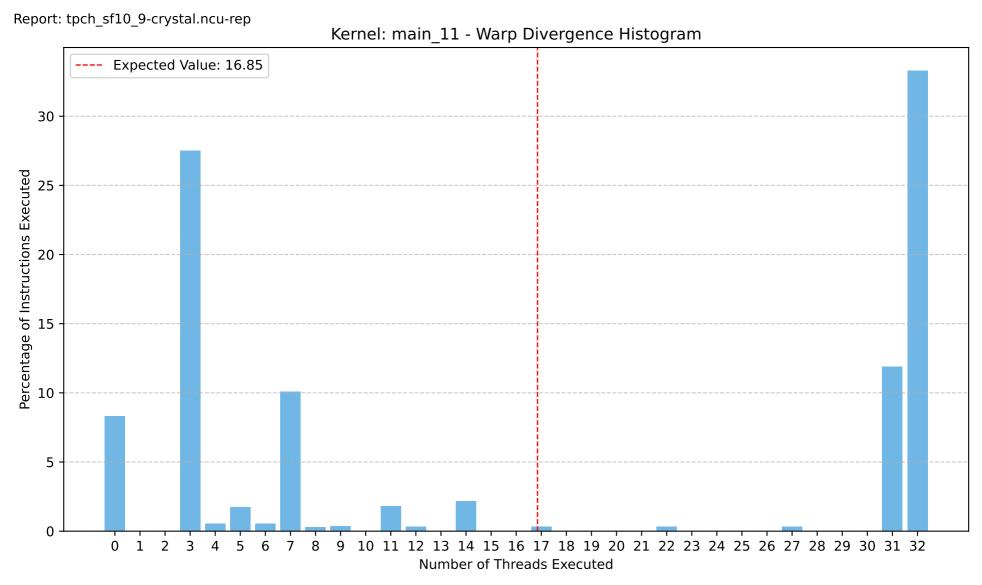


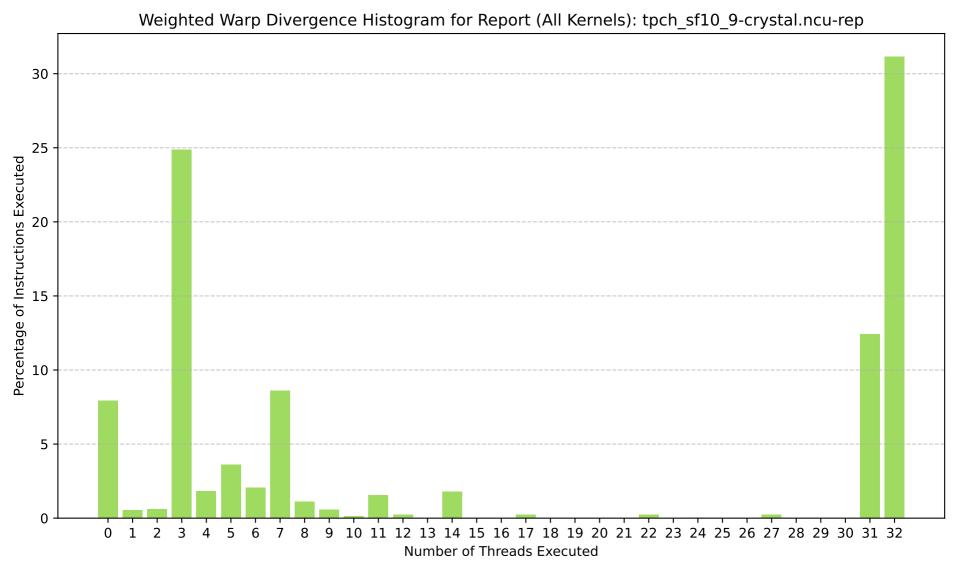












Report: tpch_sf10_9.ncu-rep Kernel: main_11 - Warp Divergence Histogram Expected Value: 16.29 40 Percentage of Instructions Executed 10 6 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 Number of Threads Executed

