

Shared main memory

```
graph TD; SMM[Shared main memory] --- MB[Memory bus]; MB --- LMem0[Local Mem]; LMem0 --- Core0[Core0]; MB --- LMem1[Local Mem]; LMem1 --- CoreU1[CoreU]; MB --- LMem2[Local Mem]; LMem2 --- CoreU2[CoreU]; MB --- LMem3[Local Mem]; LMem3 --- CoreU3[CoreU];
```

Memory bus

Local
Mem

Core0

Local
Mem

CoreU

Local
Mem

CoreU

Local
Mem

CoreU