COL 331 Homework 4

Spin locking:

- 8) What would happen if the following code snippet is executed?

 Struct spinlock lk;

 Init lock (& lk, "test lock");

 acquire (& lk);

 acquire (& lk);
- And Inside acquise() for it chears whether there lack is already held by this could not. So, there will be a Panic.
- a) For ide-lock, why did the keiner panic when intersupts are enabled?
- Ans) If we set intersupts (using still) after acquiring the lock, then intersupts an ame in the middle of Guitical section 4 can have the system in an inconsistent state. For example, when intersupt occass and the new process also the same acquired the same lock, then this will violate the condition that only one thread can hold a lock at a time.
- Ans) Keinel did not panic in the file-table lock case. The difference in the lengths of Giltical

Sections in the two cases.

The like-lock case, the cartical section thates much work time to complete because it like its for the disk prequest to complete. Hence, there is a high chance of an intessupt occaring in between (when intersupts are enabled).

The Gitical section in the file table case is much smaller. Here, there is a very less chance that an interrupt will occur in between. the Gitical section.

Hence, the kernel has parniked in ide lock but not for file table lock

Why does gelease() clear (k->pos(0) and 1k-> GPU before and not

Ans) This is because, if we first set lk s locked = 0, there is a chance that another process can acquire the lock. This laves the lock in an inconsistent state where in the lock is held by one copy but details stoned in the lock are that of the pierious copy. Thus the life we get when we call getcallerpos() for would be inconsect. Thus the first the copy into should be cleared before setting lock to 0.

Uniprocessor locking:

a) Does this implementation of locks work on a uniprocessor?

Lock (L) {
chi();
while (L == 0) continue;
L = 0;
8ti();

Ans) No this will not work because in case another has already held the lock, then this thread will keep on spinning to seven. And since interrupts are disabled, there is no way the other thread gets to run and release the lock.

a) Does this implementation of locks work on uniprocessor?

lnck(L) { lnc

This implementation will work because after each iteration, interrupts are enabled again. So, there is a chance of this thread preempting and the thread which has held the lock an get to run. Thus, there would be no deadlock.

Sleep and wakeup:

&) an both Producer (prawrite) and Consumer (prassed) skep on the 8ame channel?

Ans) yes, this is cossed and both producer and Consuma can sleep on the Same channel. But sleeping on a single channel is inefficient because waterp(9) would water upain the producer and consumer throads sleepings unnecessarily. There one of these threads would argume the lock and all the senaining threads would sleep again. This is an overhead. But Implementation wise, this is Cossect. Undelated Pout of Gode which has access to queue an wake up the Producers/ Consumas exemping on the queue (a):

XV6 tile system:

\$ echo > a

output: log-write 34 log-wate 34 log-write 59 From observation, we are intenthe following:

Block no 34 -> made of file a.

59 -> data black of povent (directory).

58 >> Bitump block of filea.

571-> data block of filea.

tor Greating tile, the following operations occur: - Greating mode (34) and setting its values Such as type, Size, Minkele!

Add entry for a in panent duectory of tika.

For writing to a file, following operations occur:

- allocate a block and write to bituap (58)

- Zero out block Contents and write "x" to block. (571)

- update a's INODE (34)

\$ echo x > a

output: log_write 58 log-write 571 109 - Write 571 109-Waite 34

109-W8/te 571

10g-wate 34

\$ 9m a

output: logurite 59

log-write 34

log-warte 58

log-write 34

109-wate 34

For randing the tile, operations occurring one:

- waiting zero to a's pasent disectory secord (59)
- update a's INDOE Values and set it to free (34)
- update free bit in the bitmap tona's datablocks (58)

ZCAV:

Laptop model: DELL PRECISION 7510.

File system of HID: NTFS

Published disk characteristics - model: HGST HTS721010A9E630

N TARUN SAI GANESH

2016E \$10438

Capacity = | TB

RAM = 07200

6 GB/8 SATA Interface

Any 9 pead speed obtained from zcavexperiment = 110 Mbps
Maximum Read Speed obtained = 143 Mbps
Minimum Read speed obtained = 70 Mbps

[Please tind the plot attacked]

USB dzile model: STRONTIUM POLLEX 329B

FILE 8YSLEM = FAT32

Read sate (published) = 25mbps

Avg Read 8 peed blained from ZCAV experiment = 22 mbps maximum 9 ead 8 peed obtained = 23.26 mbps minimum 9 ead 8 peed obtained = 20.3 mbps

[Please find the plot attacked]

For hardisk: Number of Zones found = no of steps 12, the plot ~ 25

From the graph, it is clear that hower block nos brake the highest rate, indicating the are mapped on the outermost tracks. and higher block nos are on the innortracks.

For USB: Rate is almost without because there so tracks present. There are no robiting parts.



