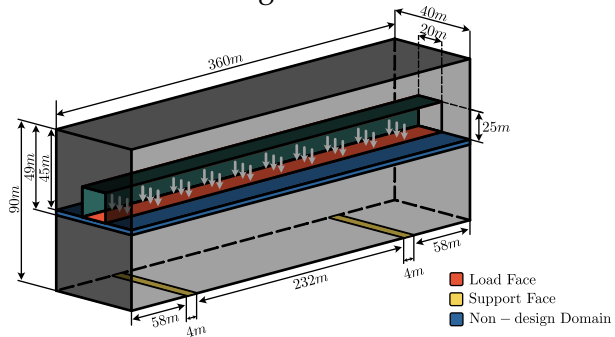


Design Domain



Topologically optimised design

