

A

B

C

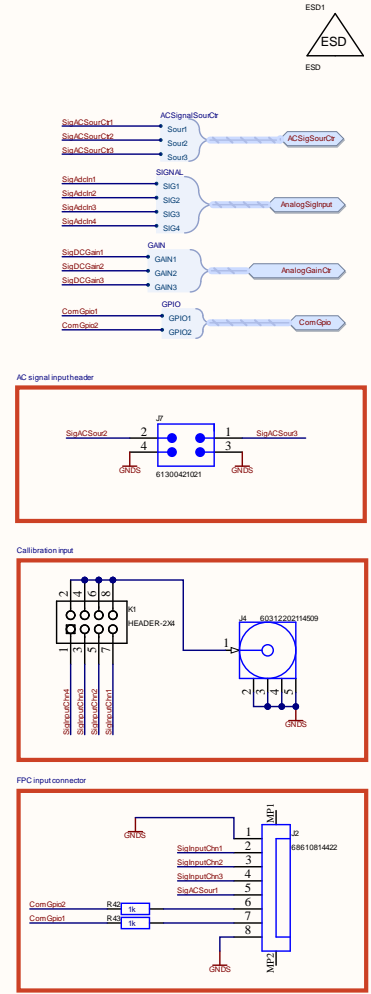
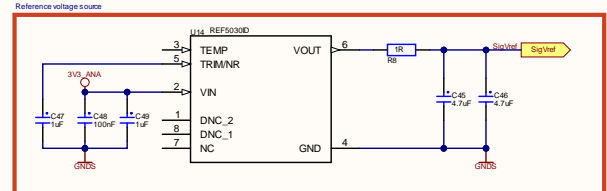
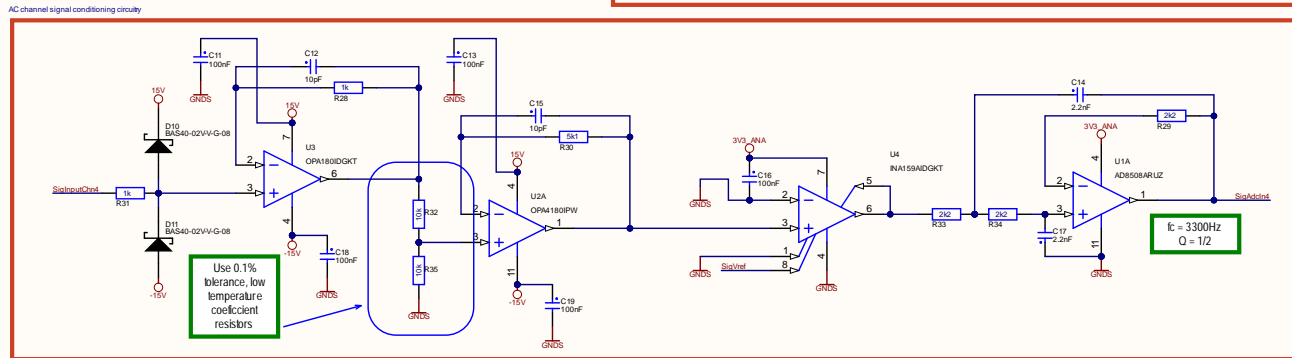
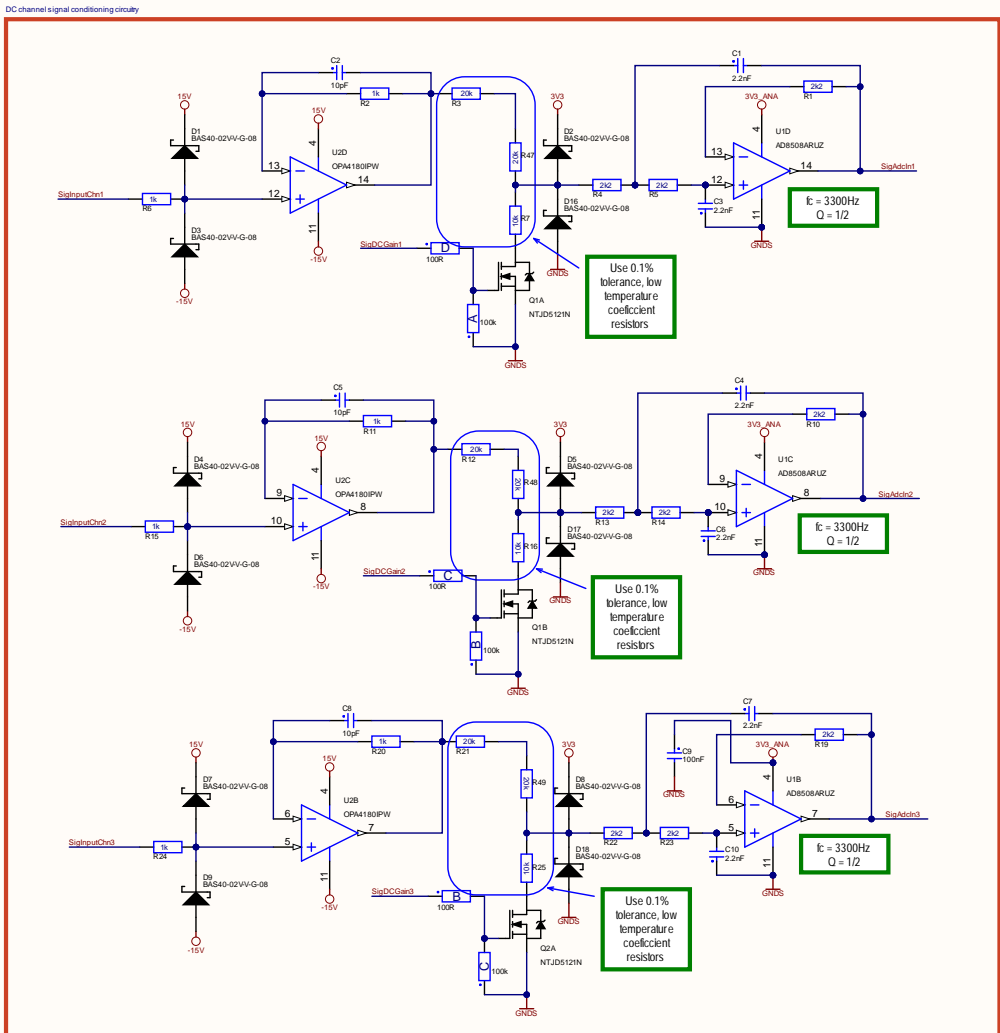
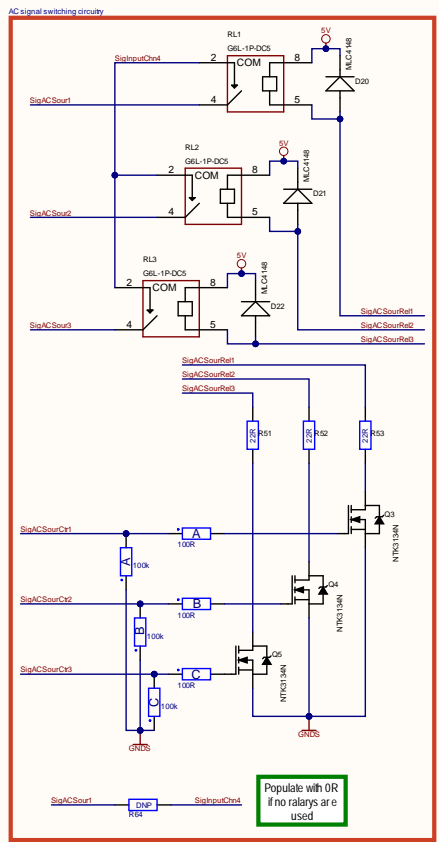
D

A

B

C

D

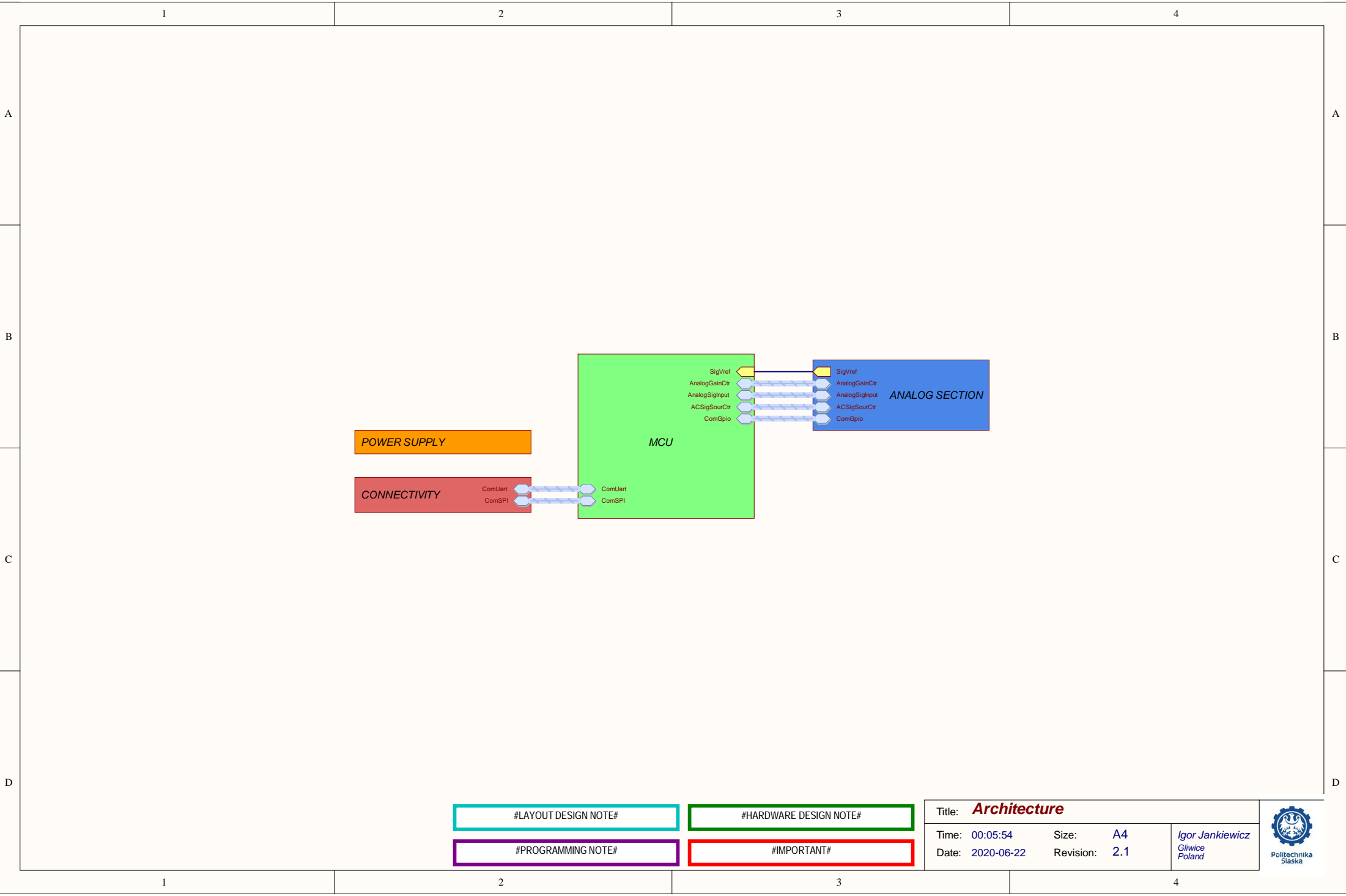


#LAYOUT DESIGN NOTE#	#HARDWARE DESIGN NOTE#
#PROGRAMMING NOTE#	#IMPORTANT#

Title:	Analog section	
Time:	00:05:53	Size: A3
Date:	2020-06-22	Revision: 2.1



Igor Jankiewicz  
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Poland



#LAYOUT DESIGN NOTE#

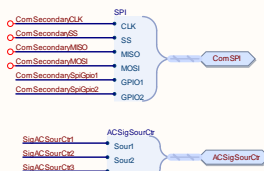
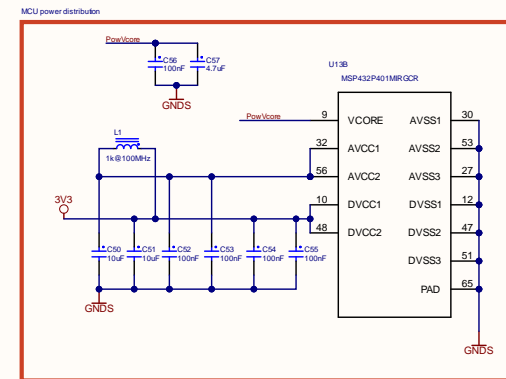
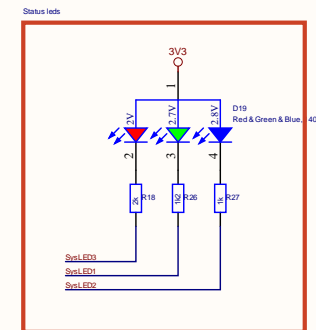
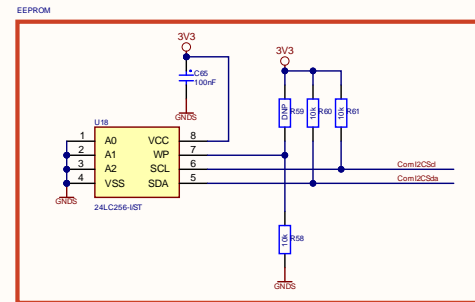
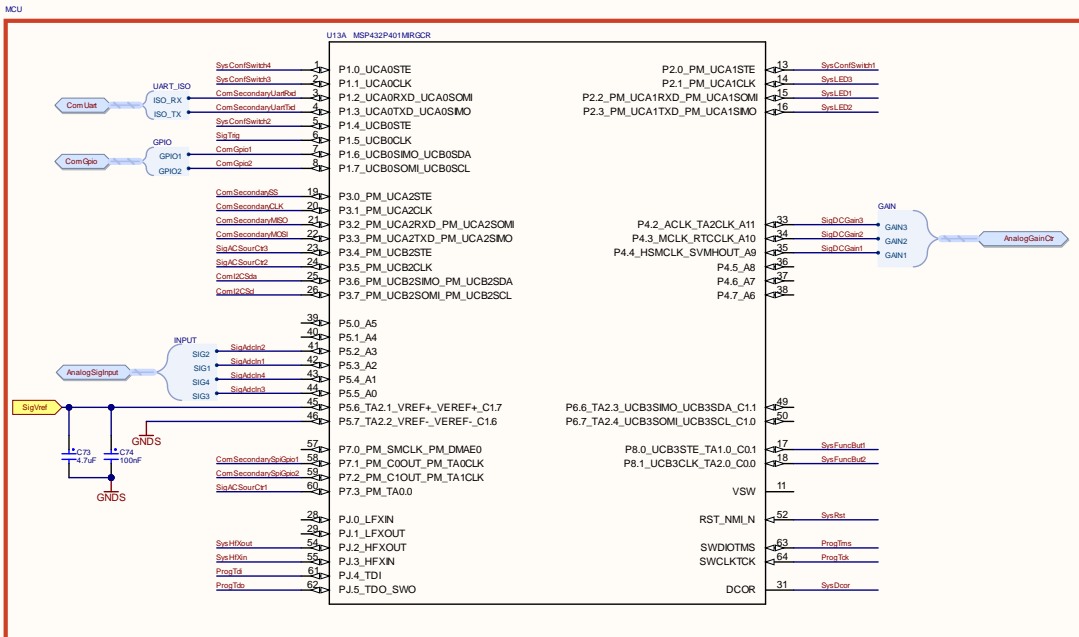
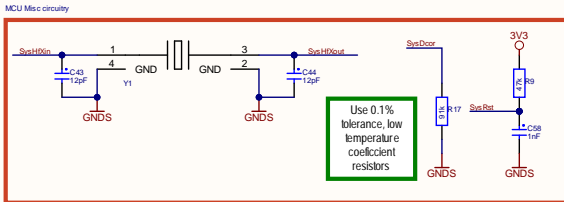
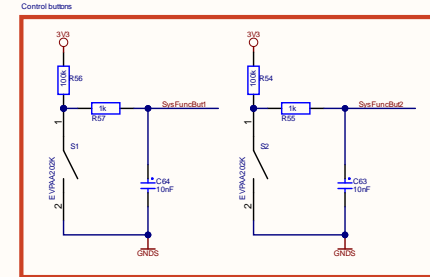
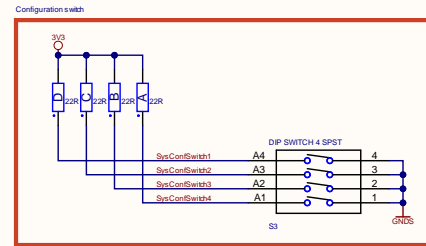
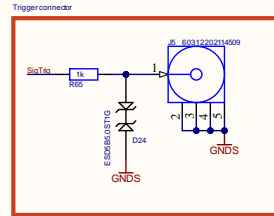
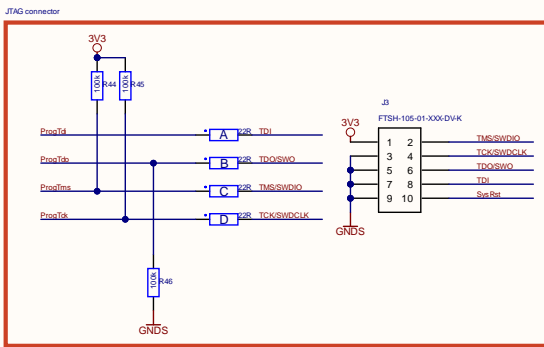
#HARDWARE DESIGN NOTE#

#PROGRAMMING NOTE#

#IMPORTANT#

Title: <b>Architecture</b>		
Time: 00:05:54	Size: A4	Igor Jankiewicz Gliwice Poland
Date: 2020-06-22	Revision: 2.1	

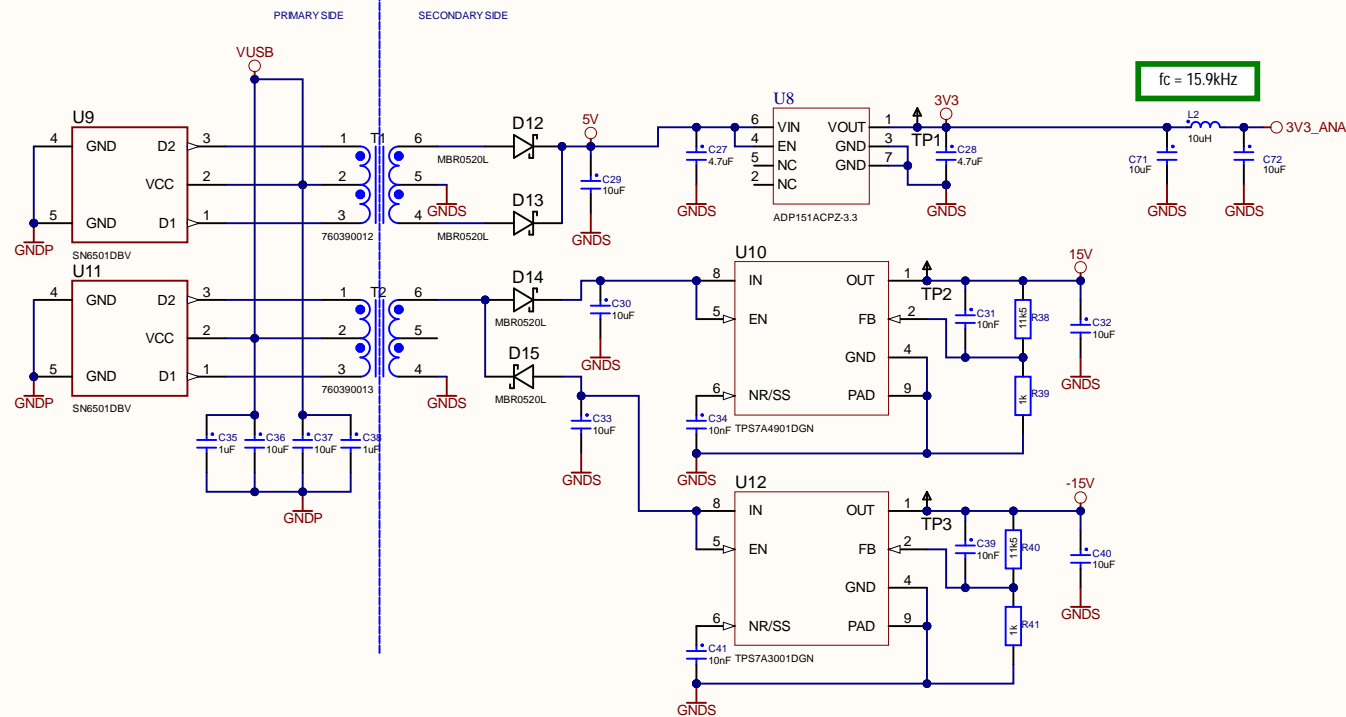




#LAYOUT DESIGN NOTE#	#HARDWARE DESIGN NOTE#
#PROGRAMMING NOTE#	#IMPORTANT#

Title: <b>MCU circuitry</b>	
Time: 00:05:54	Size: A3
Date: 2020-06-22	Revision: 2.1
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#LAYOUT DESIGN NOTE#

#HARDWARE DESIGN NOTE#

#PROGRAMMING NOTE#

#IMPORTANT#

Title: **Power supply**

Time: 00:05:54

Size: A4

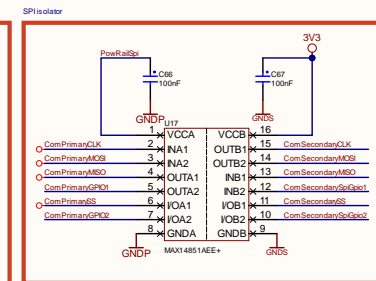
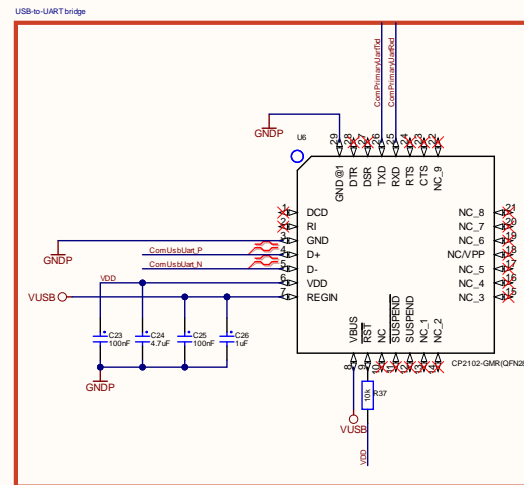
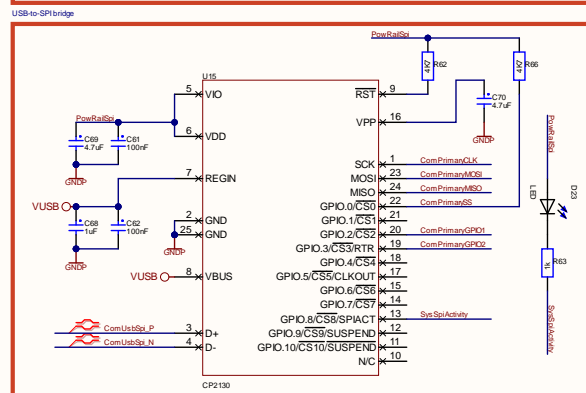
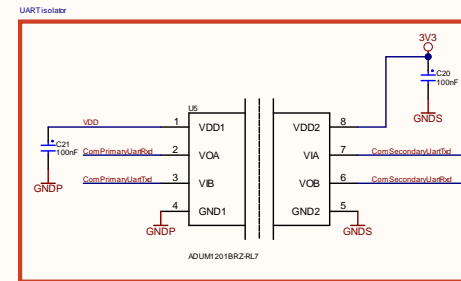
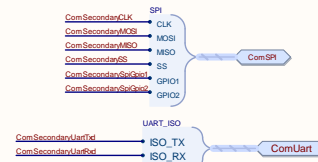
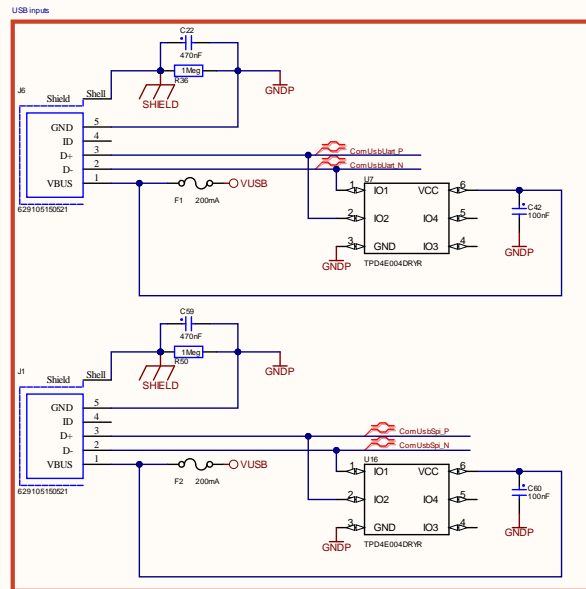
Igor Jankiewicz

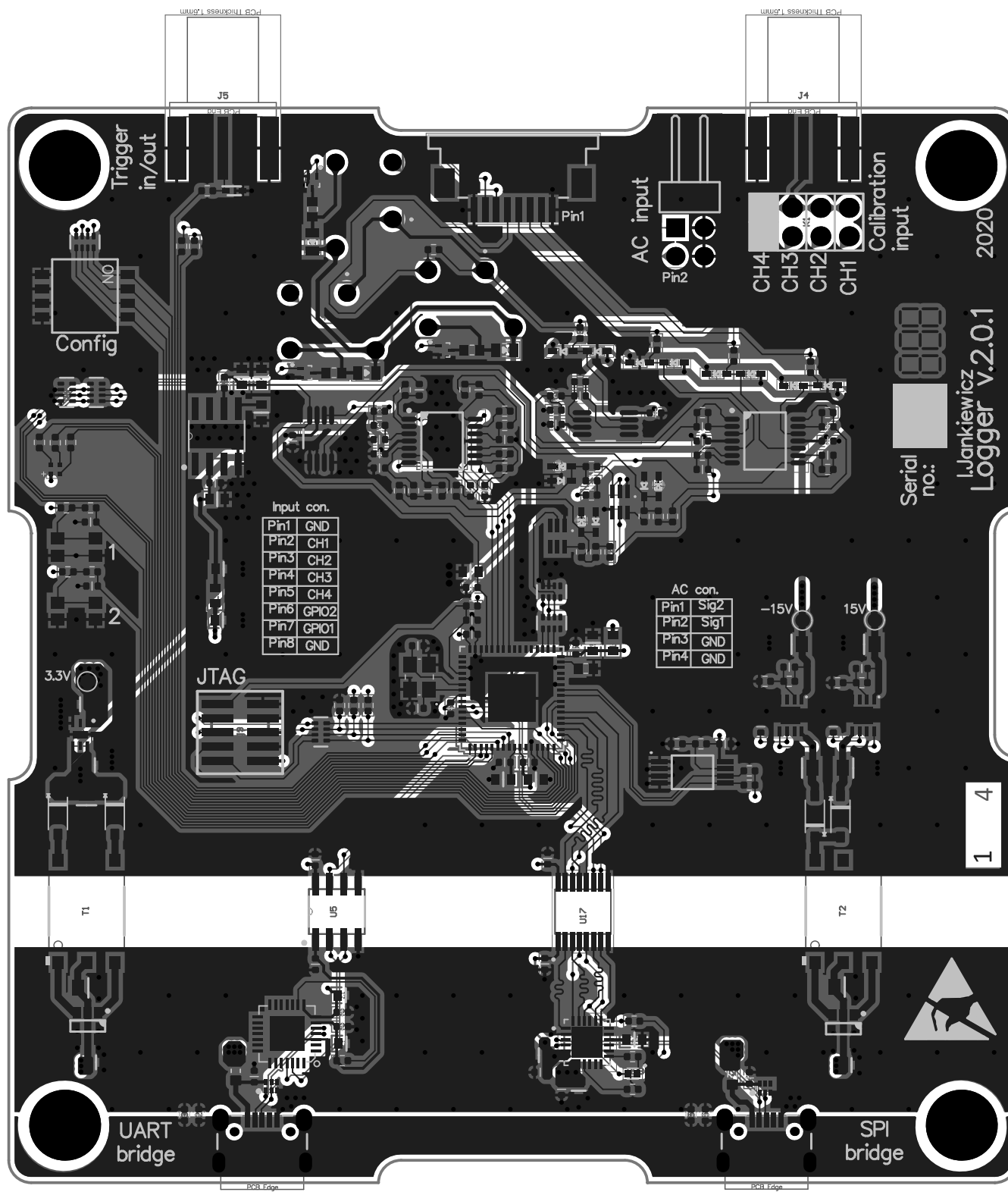
Date: 2020-06-22

Revision: 2.1

Gliwice  
Poland







Trigger  
in/out

Config

3.3V

JTAG

Input con.

Pin1	GND
Pin2	CH1
Pin3	CH2
Pin4	CH3
Pin5	CH4
Pin6	GPIO2
Pin7	GPIO1
Pin8	GND

AC input

Pin2

CH4  
CH3  
CH2  
CH1

Calibration  
input

Serial  
no.: [blank]

Jankiewicz  
Logger v2.0.1

1 4

2020

UART  
bridge

SPI  
bridge

