

#LAYOUT DESIGN NOTE#

#HARDWARE DESIGN NOTE#

#PROGRAMMING NOTE#

#IMPORTANT#

Title: **Active load**

Time: 19:47:53

Size: A4

Igor Jankiewicz

Date: 2020-06-21

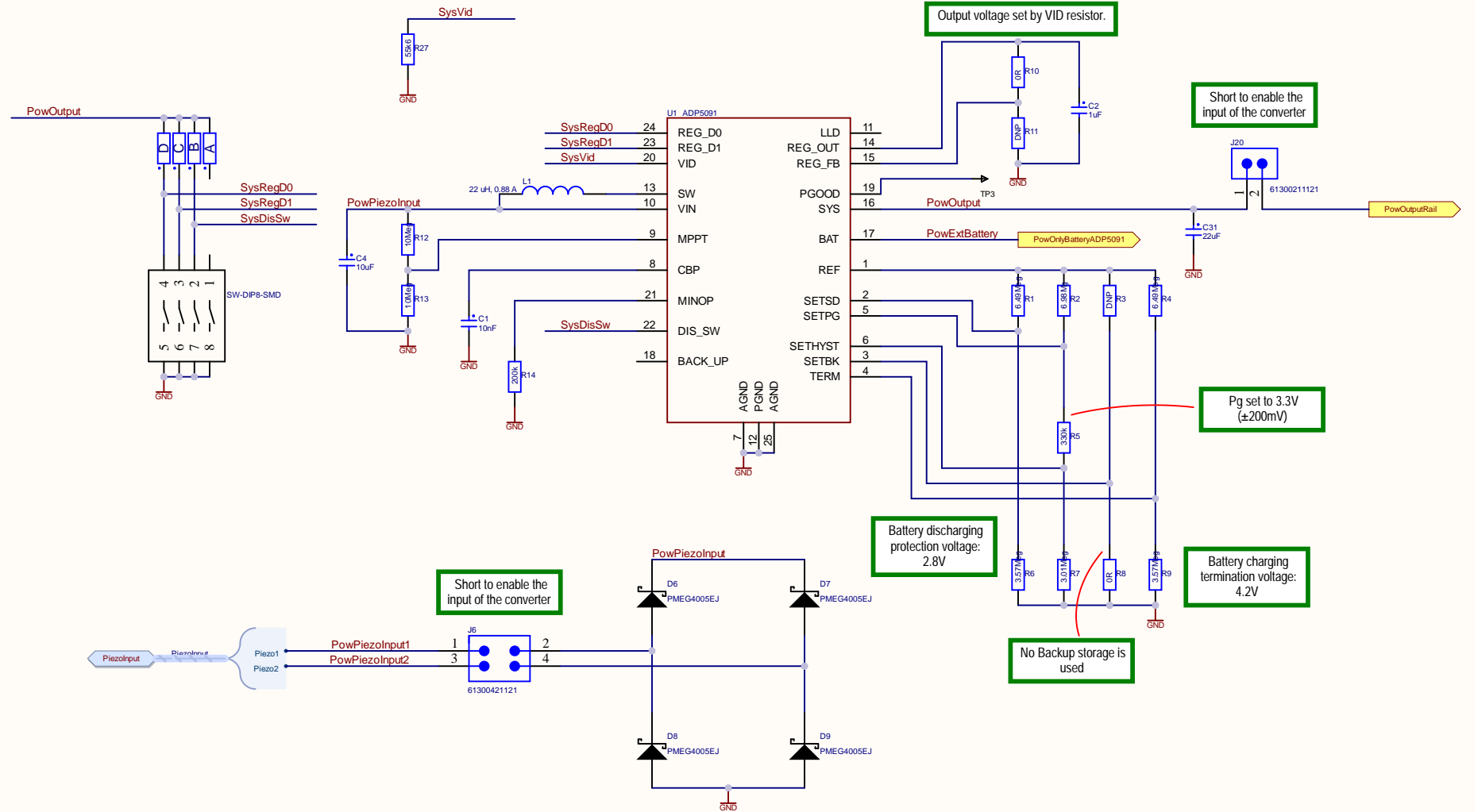
Revision: 1.0

Gliwice

Poland



Output voltage set to 3.3V. At this point, LDO is not used, but it is possible to take advantage of the regulator using a jumper wire. See the manufacturer's datasheet for more detailed info.



#LAYOUT DESIGN NOTE#

#HARDWARE DESIGN NOTE#

#PROGRAMMING NOTE#

#IMPORTANT#

Title: **ADP5091**

Time: 19:47:53

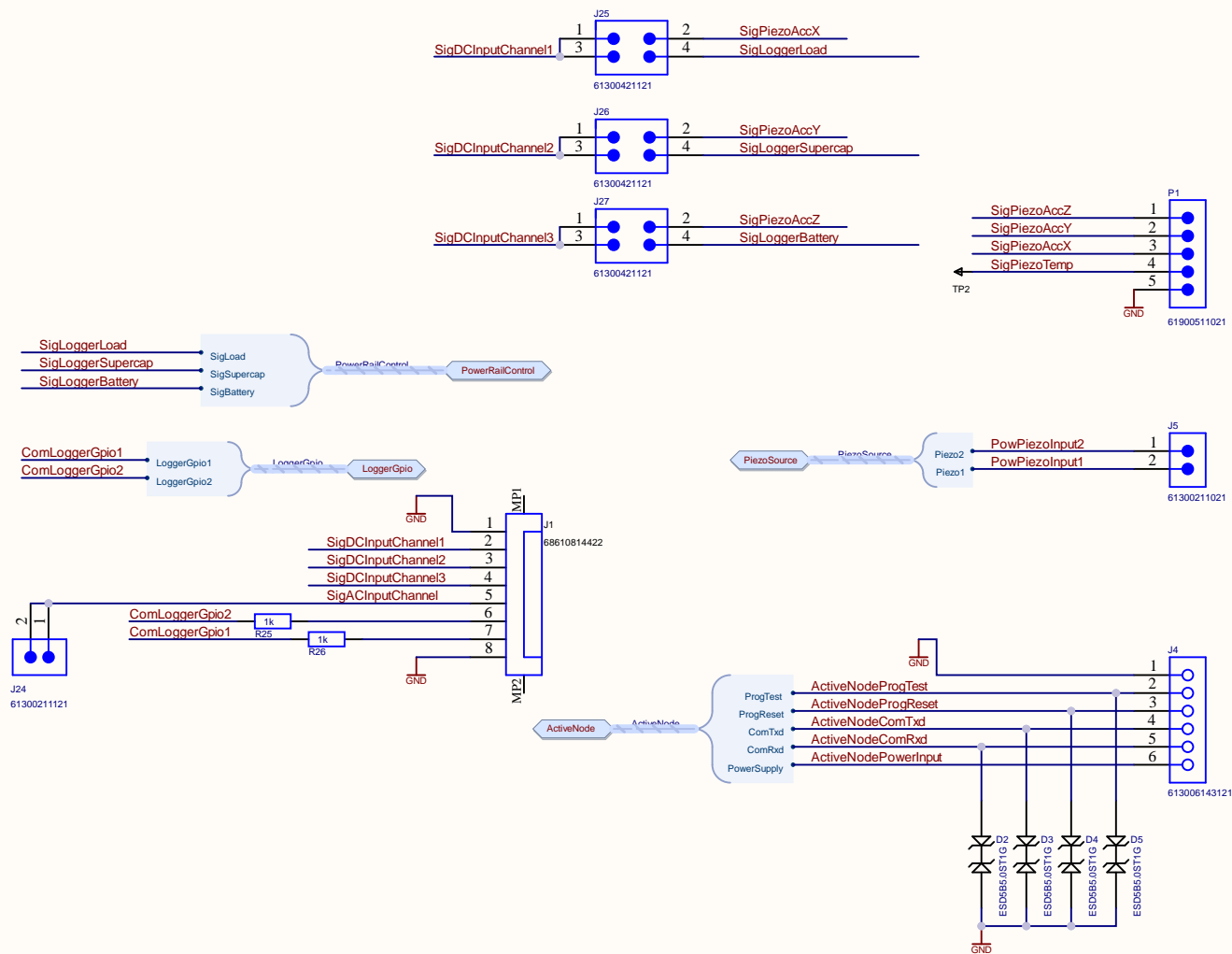
Size: A4

Igor Jankiewicz

Date: 2020-06-21

Revision: 1.0

Gliwice
Poland



#LAYOUT DESIGN NOTE#

#HARDWARE DESIGN NOTE#

Title: **Connectors**

Time: 19:47:53

Size: A4

Igor Jankiewicz

Date: 2020-06-21

Revision: 1.0

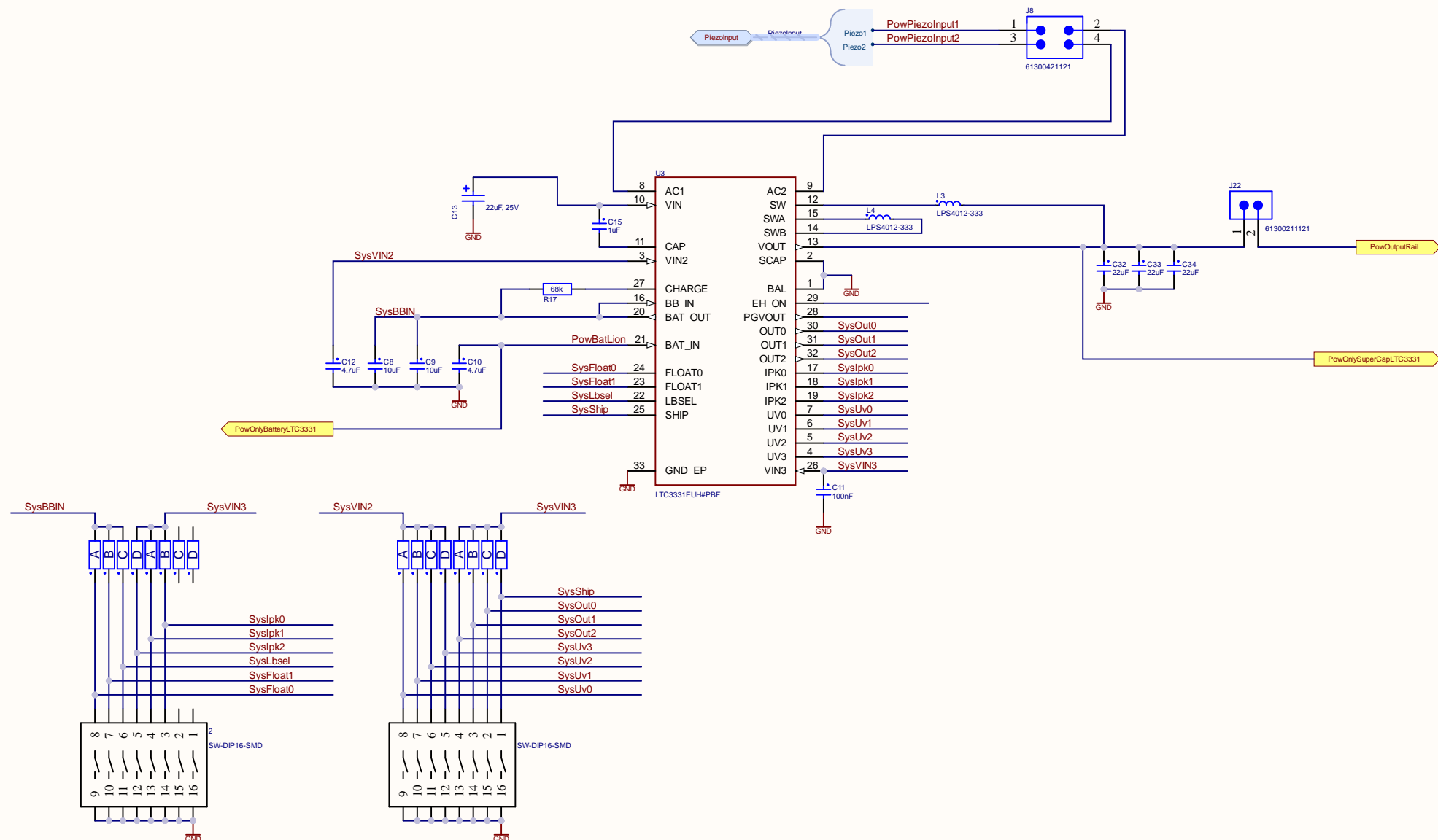
Gliwice

Poland



#PROGRAMMING NOTE#

#IMPORTANT#



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#IMPORTANT#

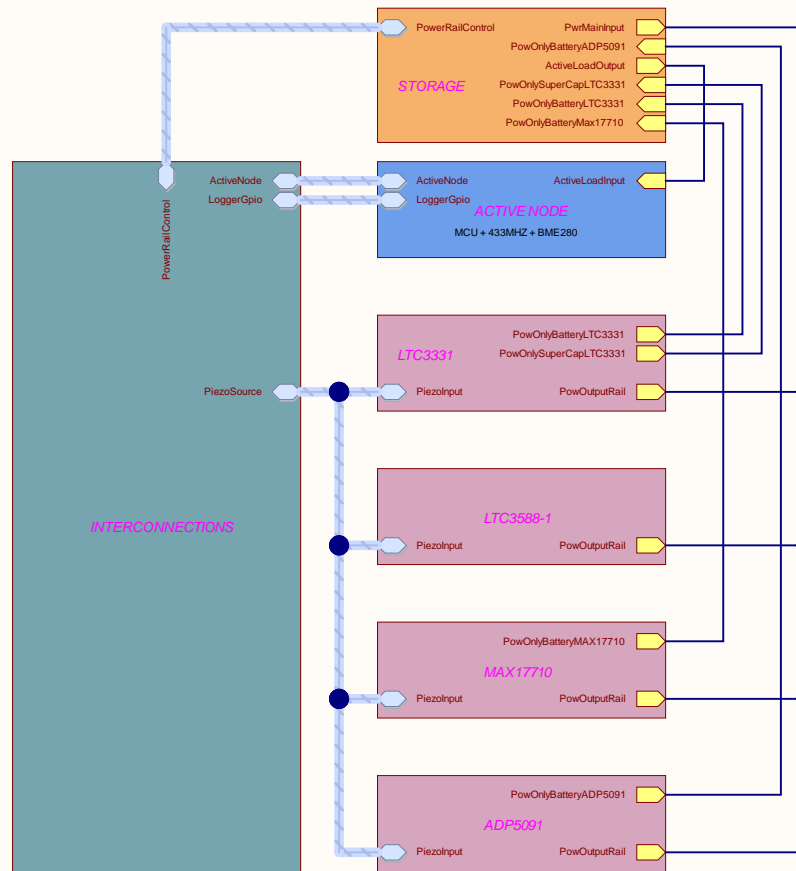
Title: **LTC3331**

Time: 19:47:53 Size: A4

Date: 2020-06-21 Revision: 1.0

Igor Jankiewicz
Gliwice
Poland





#LAYOUT DESIGN NOTE#

#HARDWARE DESIGN NOTE#

#PROGRAMMING NOTE#

#IMPORTANT#

Title: Main sheet		
Time: 19:47:54	Size: A4	Igor Jankiewicz Gliwice Poland
Date: 2020-06-21	Revision: 1.0	



1

2

3

4

A

A

B

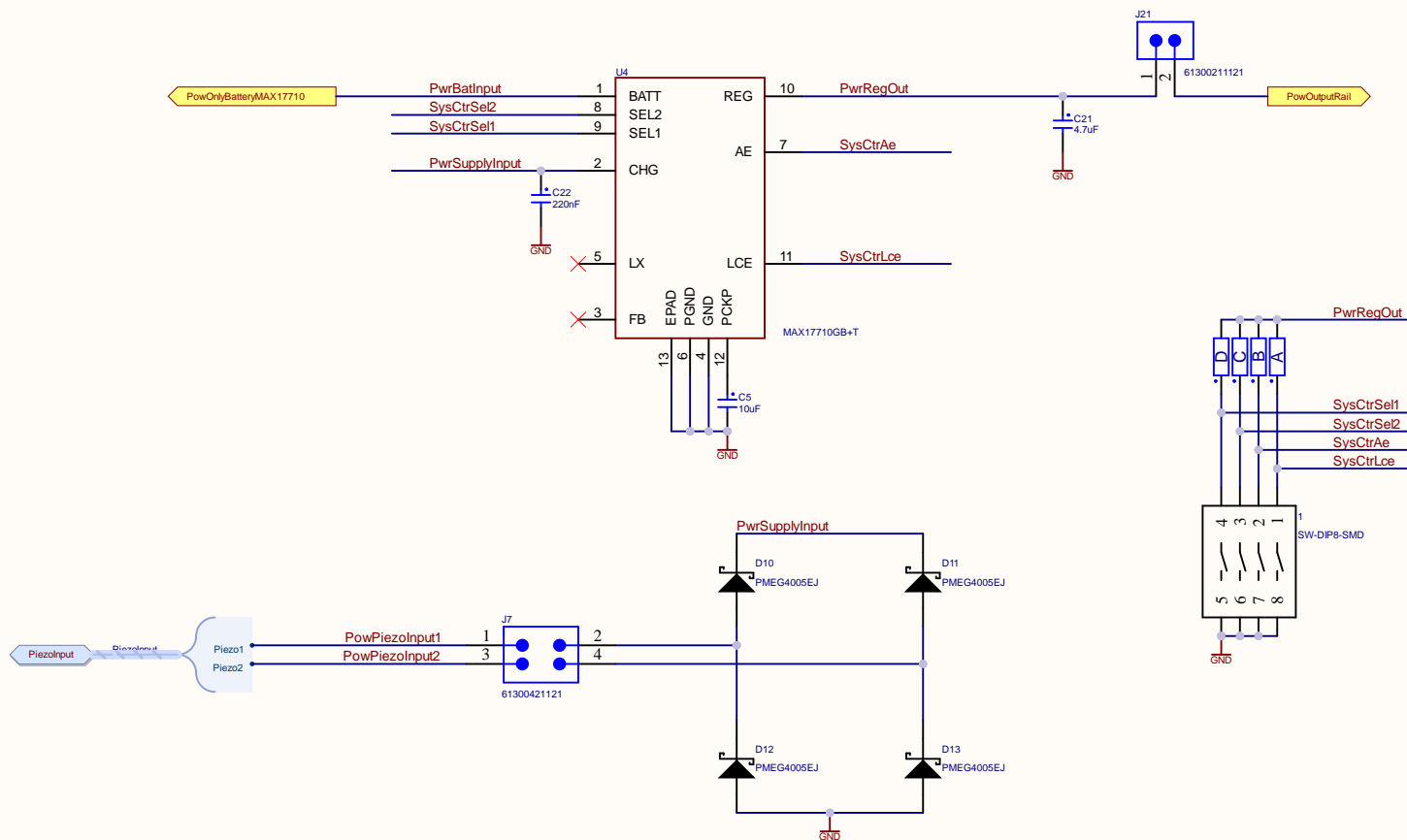
B

C

C

D

D



#LAYOUT DESIGN NOTE#

#HARDWARE DESIGN NOTE#

#PROGRAMMING NOTE#

#IMPORTANT#

Title: **MAX17710**

Time: 19:47:54

Size: A4

Igor Jankiewicz

Date: 2020-06-21

Revision: 1.0

Gliwice
Poland

1

2

3

4

LTC3331			
1	Shp	9	PK0
2	Out0	10	PK1
3	Out1	11	PK2
4	Out2	12	LBSEL
5	UV3	13	Float1
6	UV2	14	Float0
7	UV1		
8	UV0		

MAX17710			
1	SEL1		
2	SEL2		
3	AE		
4	LCE		

Serial no.: 000

Harvester v.1.0.1
I.Jankiewicz 2020

BATTERY			
1	ADP5091		
2	LTC3331		
3	MAX17710		
4	LOAD		

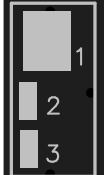
ADP5091			
1	RegD0		
2	RegD1		
3	DisSw		
4	VD		

LTC3588		
1	RegD0	
2	RegD1	

LOAD				
1	LTC3331			
2	MAX17710			
3	ADP5091			
4	LTC3588			
5	LTC3331			

Node connector					
1	GND	4	Txd		
2	Reset	5	Rxd		
3	Test	6	Vcc		

1-LOAD
2-LOAD EN
3-NODE EN



BAT

#E1