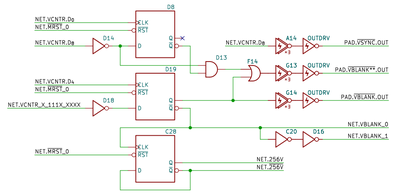
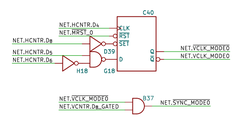
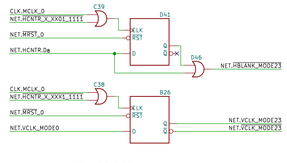
VERTICAL VIDEO TIMINGS



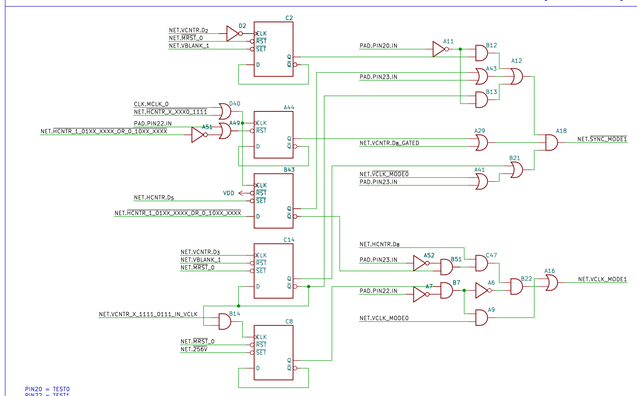
VCLK/SYNC GENERATOR



MODE0 = {H288, INTER}=00



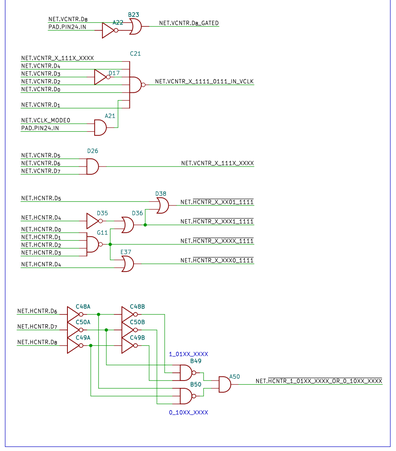
MODE23 = {H288, INTER}=1X



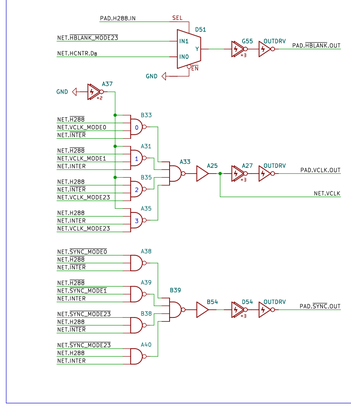
PIN20 = TEST0
PIN22 = TEST1
PIN23 = TEST2
PIN24 = TEST3

MODE1 = {H288, INTER}=01

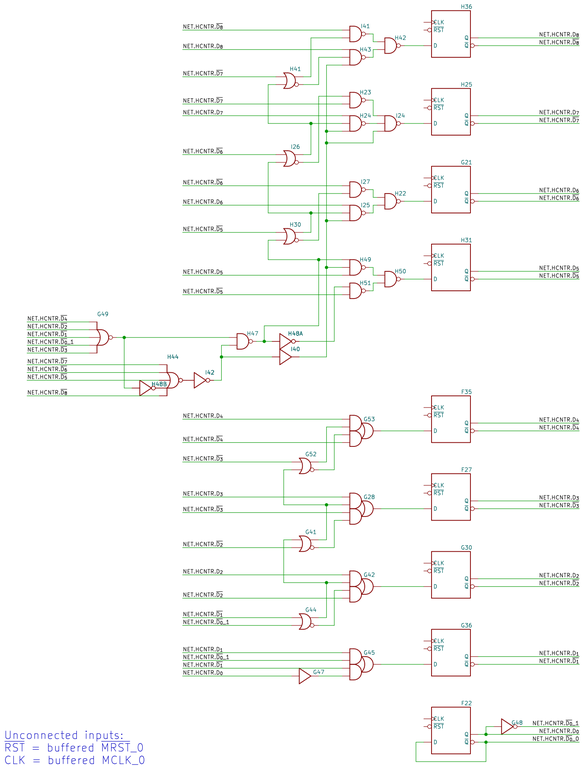
COUNTER FLAGS



TIMING OUTPUT SELECTOR

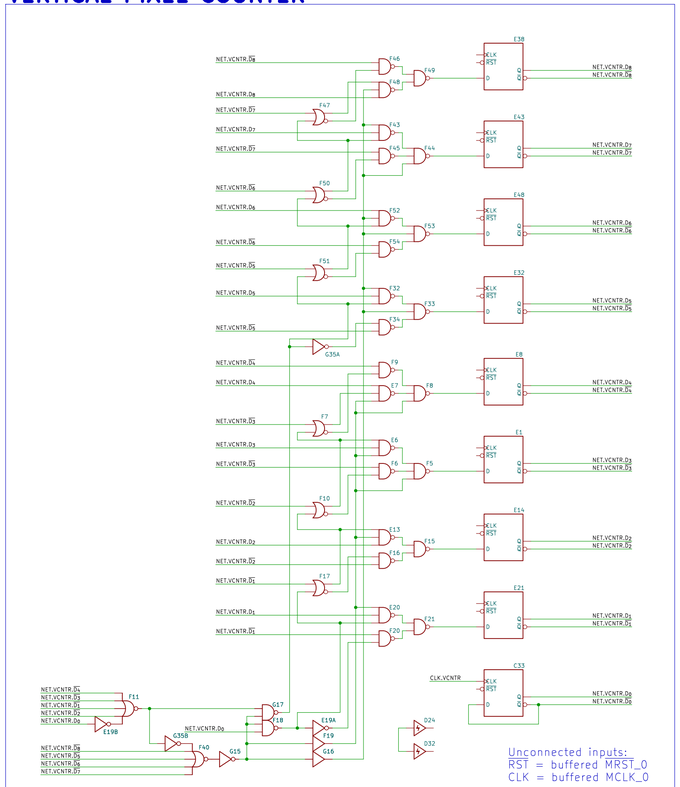


HORIZONTAL PIXEL COUNTER



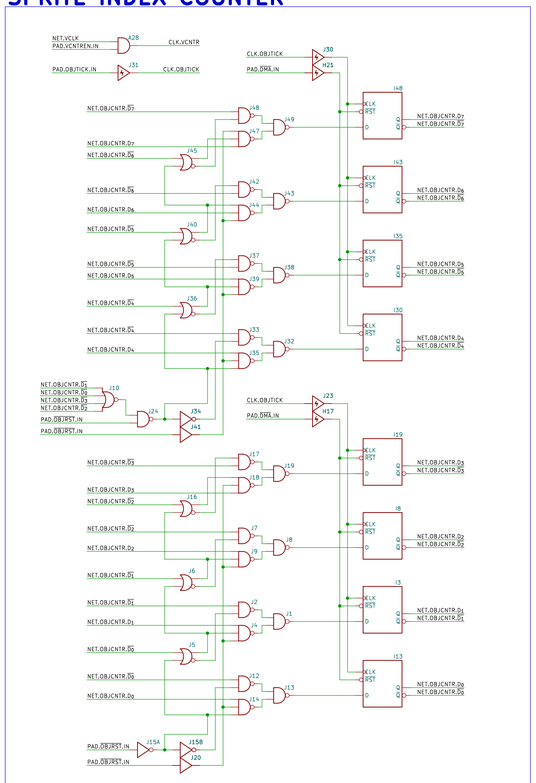
Unconnected inputs:
RST = buffered MRST_0
CLK = buffered MCLK_0

VERTICAL PIXEL COUNTER



Unconnected inputs:
RST = buffered MRST_0
CLK = buffered MCLK_0

SPRITE INDEX COUNTER



OUTPUTS

IKA Victor Co., Inc.
Sheet: /
File: K005292.kicad_sch
Title: Konami 005292
Size: A1 | Date: 2023-12-07 | Rev: v1.0
KiCad E.D.A. eeschema 7.0.6 | Id: 1/1