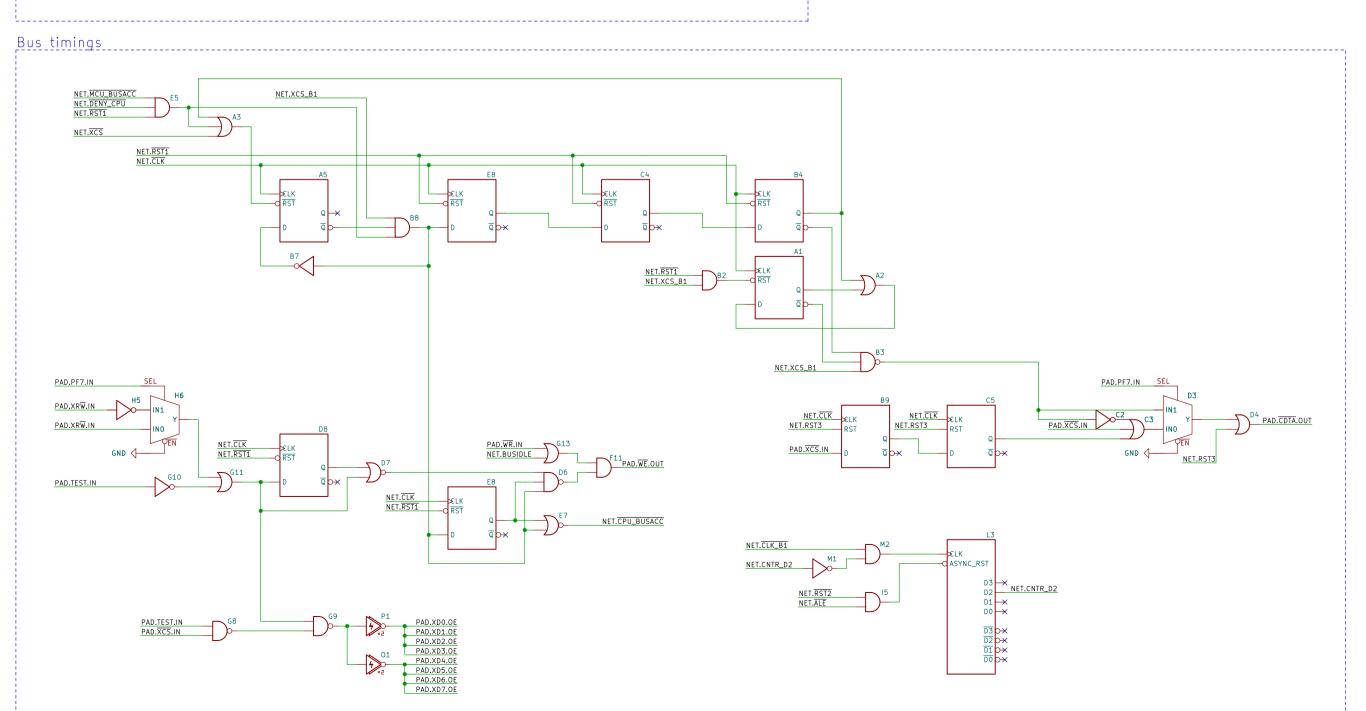
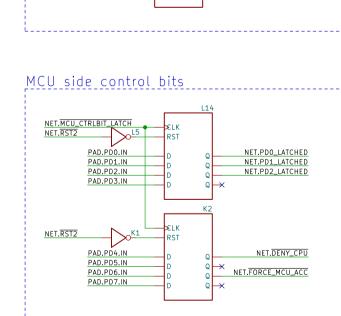


PAD.X1.OUT PAD.X2.OUT





PAD.PD4.IN PAD.PD5.IN PAD.PD6.IN PAD.PD7.IN

 Qa
 NET.MCU_ADDR_LATCHED_A0

 Qb
 NET.MCU_ADDR_LATCHED_A1

 Qc
 NET.MCU_ADDR_LATCHED_A2

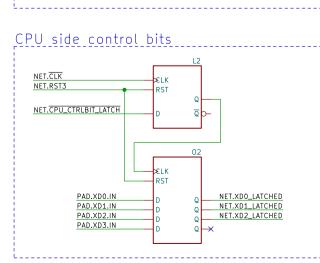
 Qd
 NET.MCU_ADDR_LATCHED_A3

 Qa
 NET.MCU_ADDR_LATCHED_A4

 Qb
 NET.MCU_ADDR_LATCHED_A5

 Qc
 NET.MCU_ADDR_LATCHED_A6

 Qd
 NET.MCU_ADDR_LATCHED_A7



Taito C-Chip ASIC Ika Victor Co., Inc. Sheet: / File: 65012-209.kicad_sch Title: 65012-205 Size: A1 Date: 2022-08-3 KiCad E.D.A. eeschema (6.0.7) Rev: v1.0