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The schematic diagram illustrates the internal structure of the NetX2500. It features two primary integrated circuits, X1 and X2, which are 2500-series components. A 15V power supply is connected to the top of both ICs. IC X1 is configured with five PAD.P0S.IN inputs and one NET.RSTZ input. Its outputs include four Q signals (NET.PD0_LATCHED through NET.PD4_LATCHED) and one NET.RSTZ output. IC X2 is configured with five PAD.P0S.IN inputs and one NET.RSTZ input. Its outputs include three Q signals (NET.SEND_P0P1, NET.FORCE_MCU_ZCC, and NET.FORCE_MCU_ZCC) and one NET.RSTZ output. The diagram uses standard logic symbols for AND gates and inverters to show the internal signal processing within each IC.