sipeed / **Tang-Nano-Doc**

Document for Tang Nano FPGA board

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🐱 **Zepan** Merge pull request #3 from kprasadvnsi/master ⋯		Latest commit 599a9dc 4 days ago
📁 assets	Pinout diagram added.	last month
📁 en	Pinout diagram added.	last month
📁 zh	Pinout diagram added.	last month
📄 .bookignore	Add base files.	4 months ago
📄 .gitignore	Add base files.	4 months ago
📄 .markdownlint.json	Add base files.	4 months ago
📄 .travis.yml	Add base files.	4 months ago
📄 CNAME	Create CNAME	5 months ago
📄 LANGS.md	Add base files.	4 months ago
📄 LICENSE	Initial commit	5 months ago
📄 README.md	Add files via upload	4 months ago
📄 SUMMARY.md	Add files via upload	4 months ago
📄 book.json	Add base files.	4 months ago
📄 deploy.sh	Add base files.	4 months ago
📄 gulpfile.js	Add base files.	4 months ago
📄 index.html	Add base files.	4 months ago
📄 index_zh.html	Add base files.	4 months ago
📄 package.json	Add base files.	4 months ago
📄 serve.sh	Pinout diagram added.	last month
📄 serve_daemon.sh	Pinout diagram added.	last month

📖 **README.md**

Tang Nano Document

What is Tang Nano?

The Lichee Tang Nano is a compact development board based on the GW1N-1 FPGA from the Gaoyun Little Bee series.

The onboard chip is GW1N-LV1QN48C6/I5, equipped with 1152 LUT4 logic resources, 1 PLL and 4 Block total 72Kbit SRAM, packaged as QFN48. The development board brings out all I/O interfaces for the design and experimentation of small digital logic.

Onboard 64Mbits PSRAM provides a rich storage resource for FPGAs.

At the same time, the development board has an RGB interface that can be connected to peripherals such as LCD screens and VGA converters, making it ideal for applications such as screen displays.

The board's onboard 24MHz crystal and USB to JTAG downloader make it easy to connect to a PC for development without the need for an external debugger and clock input. The onboard RGB LEDs can be used for indication and debugging, while the FPGA IOBANK group's adjustable IO levels allow the development board to easily interface with externally different level specifications.

Onboard resources:

- GW1N-1
- 64Mbit QSPI PSRAM
- RGB LED
- RGB LCD 40P interface and drive circuit
- 24MHz crystal
- USB debugger, based on Type-C communication interface

Resource download link

- [GoWin official download station](#)
- [dl Download station](#)
- [Tang nano Schematic](#)