# Custom Computing: Assessed Coursework

## Ioannis Kassinopoulos

March 3, 2013

## Question 1

**Recurring engineering costs** are the costs that will occur in a repeating fashion during the production, usually involving fabriction. These costs are usually descriped in a per unit form.

**Non-recurring engineering cost** is the one-time up-front cost for research, design, testing and development of a new product.

As we can see below, the minimum number of units that need to be sold for the ASIC implementation to be cost-effective is 1 million units.

$$C_{FPGA} > C_{ASIC} \Rightarrow \pounds 0 + \pounds 2 \times N_{units} > \pounds 10^6 + \pounds 1 \times N_{units} \Rightarrow N_{units} > 10^6$$

## Question 2

#### (a) Diagramatic and symbolic Simulation

#### Diagram of circuit Q1

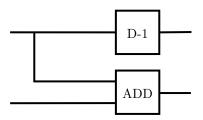


Figure 1: the circuit as derrived from Q1

#### Diagram of circuit P1

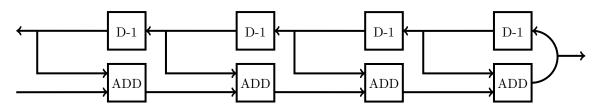


Figure 2: the circuit as derrived from P1

#### Simulation

The source code of the simulation (uninitialized delay) is the following:

```
INCLUDE "prelude.rby".
P1 n = Q1^n; fork\sim1.
Q1 = snd fork; rsh; [add,D^\sim1].
current = P1 4.
The result after executing re "a;b;c"
0 - \langle a,? \rangle \sim ((((a + ?) + ?) + ?) + ?)
1 - \langle b,? \rangle \sim ((((b + ?) + ?) + ?) + ((((a + ?) + ?) + ?) + ?))
2 - \langle c, ? \rangle \sim ((((c + ?) + ?) + ((((a + ?) + ?) + ?) + ?)) + ((((b + ?) + ?) + ?))
            +((((a + ?) + ?) + ?) + ?)))
The source code of the simulation (initialized delay with 0) is the following:
INCLUDE "prelude.rby".
P1 n = Q1^n; fork\sim1.
Q1 = snd fork; rsh; [add,DI 0^{\sim}1].
current = P1 4.
The result after executing re "a;b;c"
0 - \langle a, 0 \rangle  ((((a + 0) + 0) + 0) + 0)
1 - \langle b, 0 \rangle  ((((b + 0) + 0) + 0) + ((((a + 0) + 0) + 0) + 0))
2 - \langle c, 0 \rangle  ((((c + 0) + 0) + ((((a + 0) + 0) + 0) + 0)) + ((
              ((b + 0) + 0) + 0) + ((((a + 0) + 0) + 0) + 0)))
The result after executing re -s 4 1"
0 - <1,0> ~ 1
1 - <1,0> ~ 2
2 - \langle 1, 0 \rangle ^{\sim} 4
3 - <1.0> ~ 8
```

The simulation output (for 4 cycles) can be found in the included zip file.

## Question 3

### (a) Proof by induction

In order to show that  $[P,Q]^n$ ; R=R;  $Q^n$  for n>0, we first have to show that it is True for n=1.

Base case:  $[P,Q]^1$ ; R=R;  $Q^1$ 

This is intuitively shown to be true by the given assumption  $[P,Q]^n$ ; R=R; Q which is equivalent.

Assuming that it is also true for n = k > 0

Inductive Hypothesis:  $[P,Q]^k$ ; R=R;  $Q^k$ 

We need to show that the same is true for n = k + 1 and  $[P, Q]^{k+1}$ ; R = R;  $Q^{k+1}$ 

```
\begin{split} &[P,Q]^{k+1}; R \ \mathbf{LHS} \\ &= [P,Q]^k; [P,Q]; R \ \text{(by sequential expansion of } [P,Q]^{k+1}) \\ &= [P,Q]^k; R; Q \ \text{(since } [P,Q]; R = R; Q \ \text{given)} \\ &= R; Q^k; Q \ \text{(by the i.h. } [P,Q]^k; R = R; Q^k) \\ &= R; Q^{k+1} \ \text{(by sequential contraction of } Q^k; Q) \ \mathbf{RHS} \end{split}
```

So by induction we have **proved** that if [P,Q]; R=R; Q is given to be True, for n>0:

$$[P,Q]^n$$
;  $R=R$ ;  $Q^n$  is also  $True$ 

# (b) Inductive Definitions

## Right-reduction

$$\begin{split} rdr_1 &= fst \: [-]^{-1}; R. \\ rdr_{n+1} &= fst \: apl_n^{-1}; lsh; snd(rdr_n \: R); R. \end{split}$$

## Delta (triangle)

$$\Delta_0 = [].$$

$$\Delta_{n+1} = [\Delta_n, R^n] \backslash apr_n.$$

## (c) Horner's Rule

#### Left-hand side

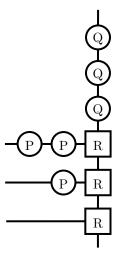


Figure 3: LHS of the rule for n=3

## Right-hand side

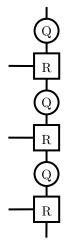


Figure 4: RHS of the rule for n=3

### (d) Polynomial Evaluation

R stands for the add operation (addition), P and Q both stand for multiplication by a constant (let this constant be x). For the given coefficients  $a_0, a_1, a_2, a_3$ , the circuit will be the following.

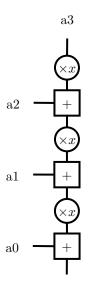


Figure 5: The optimised circuit as adjusted for polynomial evaluation

and the simulation written in Ruby: (x should be replace by the required number)

```
INCLUDE "prelude.rby".
multc n = pi1^~1; snd n; mult.
Q = multc 'x'.
R = add.
POL n = rdr n (snd Q; R).
current = POL 3.
run with re "a_0 a_1 a_2 a_3" produces the following output:
Simulation start :
    0 - <<a_0,a_1,a_2>,a_3> ~ (a_0 + ((a_1 + ((a_2 + (a_3 * x)) * x)) * x))
Simulation end :
```

## Question 4

#### (a) Non-recursive definition of btree<sub>3</sub> and its type

The non-recursive definition of  $btree_3$  R is:

```
btree 3 R = [[R,R];R,[R,R];R];R. and therefore its type is given by: <<< X,X>,< X,X>>> < X,X>>> < X.
```

# (b) Fully pipelined timeless implementation of btree for timeless R Definition

The system can be described by the following inductive equation:

```
pbtree_1 = R.
pbtree_{n+1} = [pbtree_n R, pbtree_n R]; [D^n, D^n]; R; AD^n.
Proof by induction
Here, we will try and prove that our equation is equivalent to the given.
Base Case: pbtree_1 = btree_1 (required to show)
pbtree_1 LHS
= R (by definition of pbtree)
= btree_1 (by definition of btree) RHS
Inductive Hypothesis: pbtree_k = btree_k
We need to show that: pbtree_{k+1} = btree_{k+1}.
pbtree_{k+1} LHS
= [pbtree_k R, pbtree_k R]; [D^k, D^k]; R; AD^k. (by the pbtree definition)
= [btree_k R, btree_k R]; [D^k, D^k]; R; AD^k. (replacing pbtree_k with btree_k by the hypothesis)
= [btree_k R, btree_k R]; R. (replacing [D^k, D^k]; R; AD^k with R since R is timeless and they cancel out.)
=btree_{k+1}. (definition of btree) RHS
Symbolic simulation of a binary adder
INCLUDE "prelude.rby".
btree n R =
         IF (n $eq 1) THEN
         ELSE
                   ([btree (n-1) R, btree (n-1) R]; [D^{(n-1)}, D^{(n-1)}]; add; (AD^{(n-1)}).
current = btree 3 add.
The results:
re -s 3a b c d p q r s
Simulation start :
    0 - <<< a_0, b_0>, < c_0, d_0>>, << p_0, q_0>, < r_0, s_0>>>
            (((a_0 + b_0) + (c_0 + d_0)) + ((p_0 + q_0) + (r_0 + s_0)))
```

#### (c) Change of type

Simulation end :

In order to understand the changes that need to occur in our definition let's consider btree<sub>3</sub>.

The transormation we wish to achieve is the following:

$$< X, X, X, X, X, X, X, X > \Rightarrow <<< X, X >, < X, X >>, << X, X >>>.$$

1 - <<<a\_1,b\_1>,<c\_1,d\_1>>,<<p\_1,q\_1>,<r\_1,s\_1>>> ~ ?
2 - <<<a\_2,b\_2>,<c\_2,d\_2>>,<<p\_2,q\_2>,<r\_2,s\_2>>> ~ ?

which is equivalent to applying to the initial flat list the prelude function  $half_4$  or  $half_{2(3-1)}$ , and then to each half  $half_2$ .

The obvious pattern is easily implementable in Ruby due to the functional nature of the language. The inductive definition of the equation is the following:

```
btree_1R=R
btree_nR = half_{2^{n-1}}; [btree_{n-1}R, btree_{n-1}R]; R.
The source code of the implementation:
INCLUDE "prelude.rby".
btree n R =
        IF (n $eq 1) THEN
                 (R)
        ELSE
                 (half (2 exp (n-1)); [btree (n-1) R, btree (n-1) R]; R).
current = btree 3 add.
The result (sum 1 - 8):
re 1 2 3 4 5 6 7 8
Simulation start :
    0 - <1,2,3,4,5,6,7,8> ~ 36
Simulation end :
The result (symbolic simulation):
reabcdpqrs
Simulation start :
    0 - \langle a,b,c,d,p,q,r,s \rangle (((a + b) + (c + d)) + ((p + q) + (r + s)))
Simulation end :
```