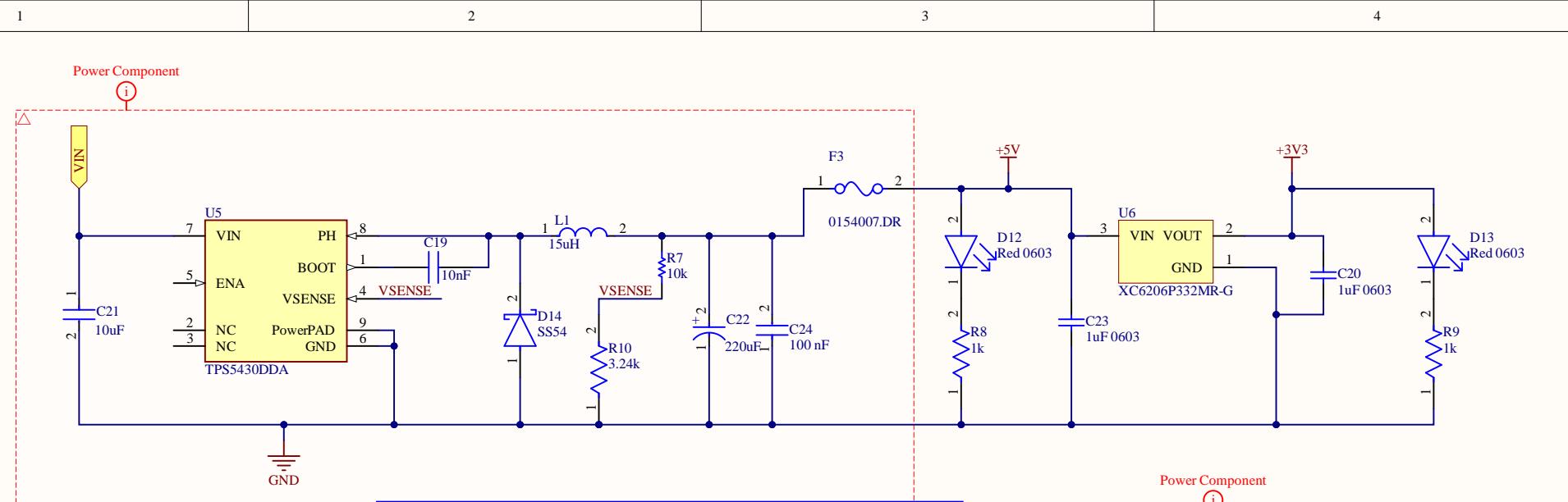
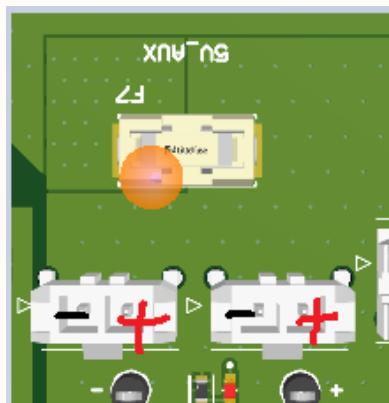


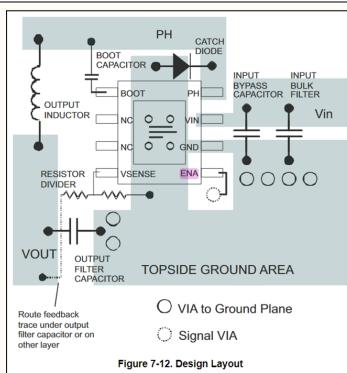
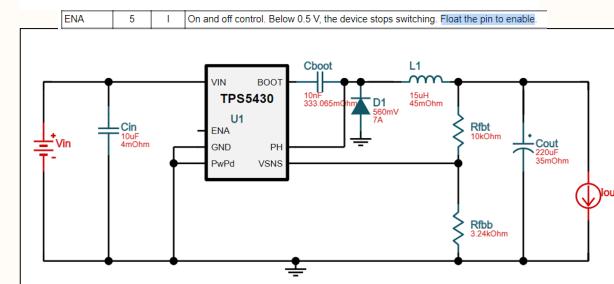
Title		
Size	Number	Revision
A3		
Date: 1/16/2026		Sheet of
File: C:\Users\..\Drivers.SchDoc		Drawn By:



Connector standard for power (clip side  $\rightarrow$  GND)

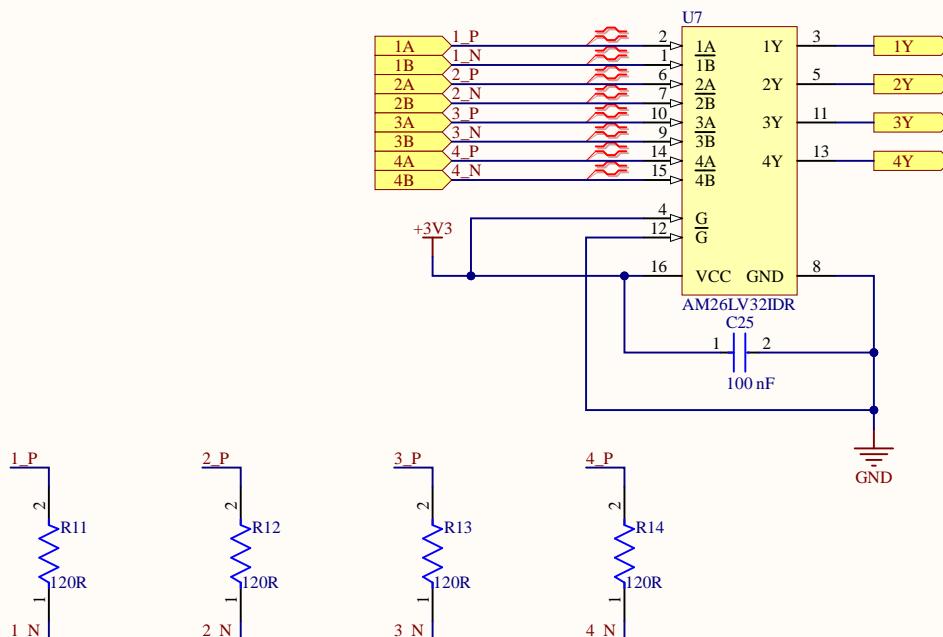


### Schematic + layout references



Title		
Size	Number	Revision
A4		
Date:	1/16/2026	Sheet of
File:	C:\Users...\Power.SchDoc	Drawn By:

A



Might have to change all the terminating resistors to 1k ohms - No data on the stepper motor's website.  
<https://www.quantumdev.com/how-to-use-termination-resistors-when-installing-rotary-incremental-encoders/>

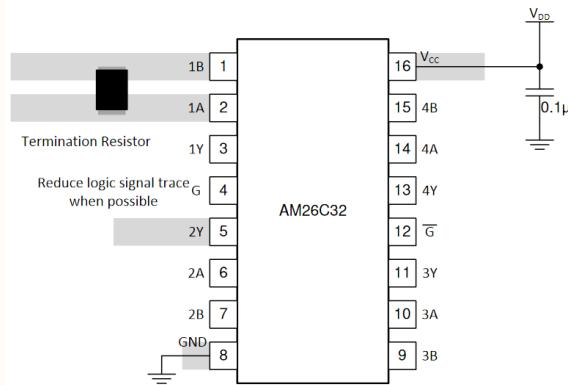


Figure 8-3. Trace Layout on PCB and Recommendations

(!!!OLD!!) Change to Low Voltage version now (it was AM26C32IDR)

### 7.3.3 Active-High and Active-Low

The device can be configured using the G and  $\overline{G}$  logic inputs to select receiver output. The high voltage or logic 1 on the G pin, allows the device to operate on an active-high and having a low voltage or logic 0 on the  $\overline{G}$  enables **active low operation**. These are simply a way to configure the logic to match that of the receiving or transmitting controller or microprocessor.

G is Active Low config  
Output is pulled up to 3v3 to interface with STM32

Title		
Size	Number	Revision
A4		
Date:	1/16/2026	Sheet of
File:	C:\Users\...\Differential Encoder Receiver	Designed By:

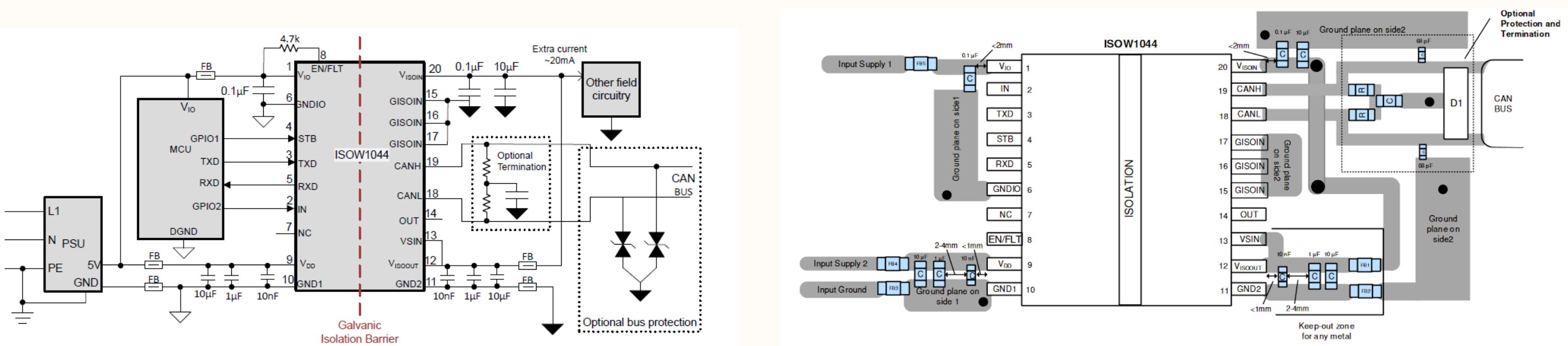
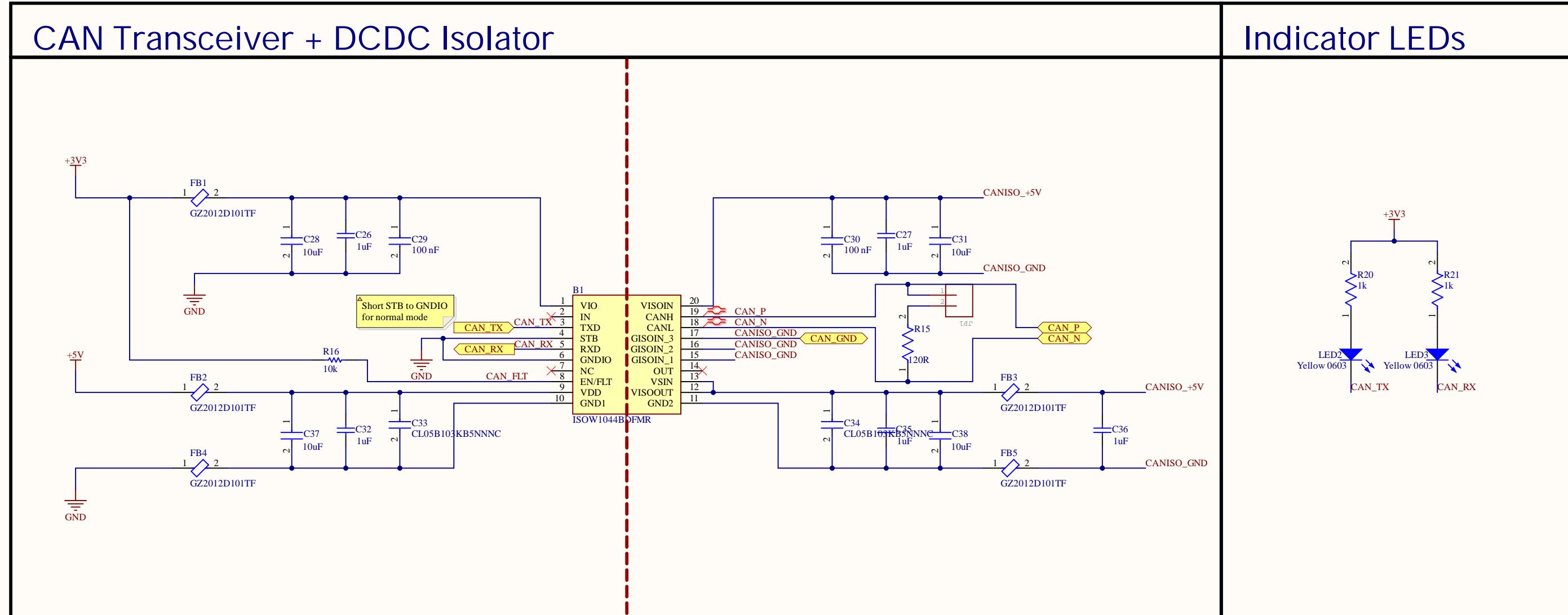


Figure 13-1. Layout example

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C		
Date:	1/16/2026	Sheet of
File:	C:/Users/ISO_CAN.SchDoc	Drawn By: