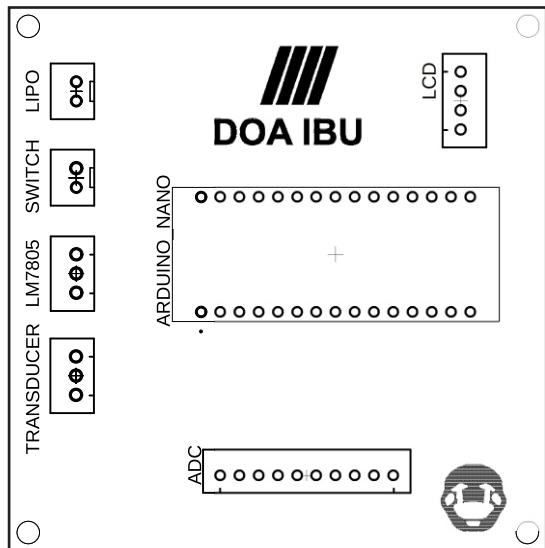
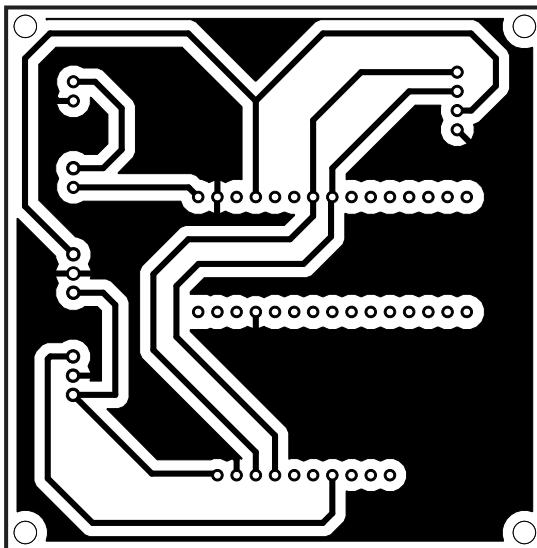


# SILK

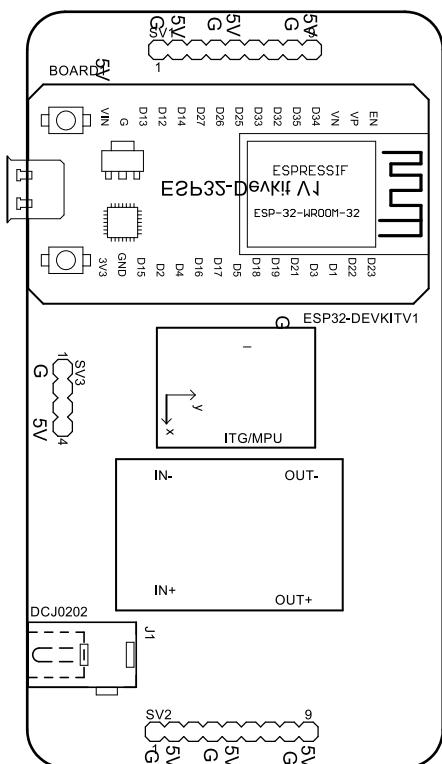


# BOT

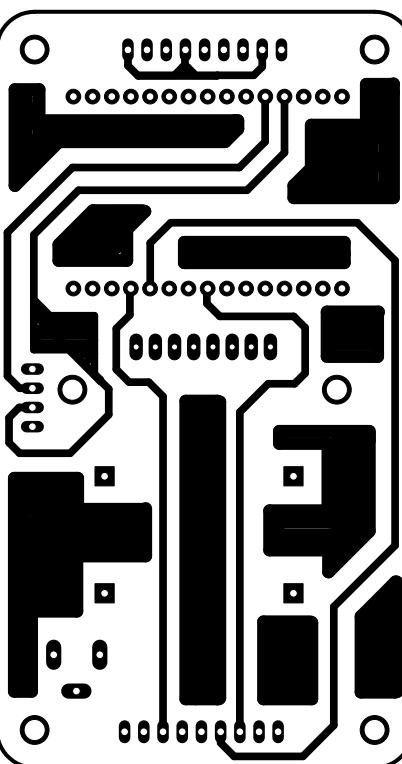


CONTOH  
SINGLE LAYER

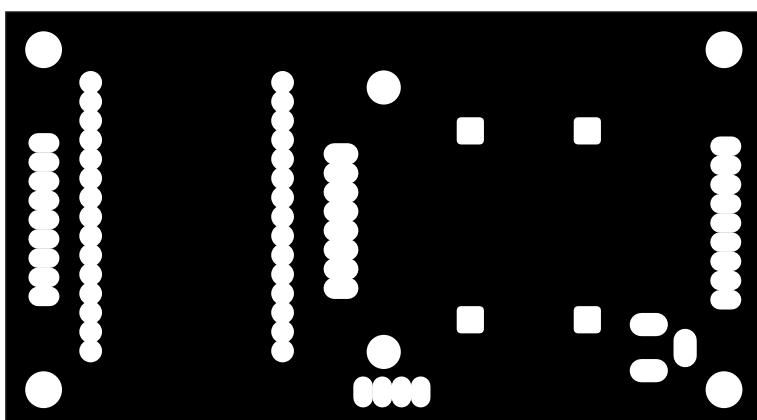
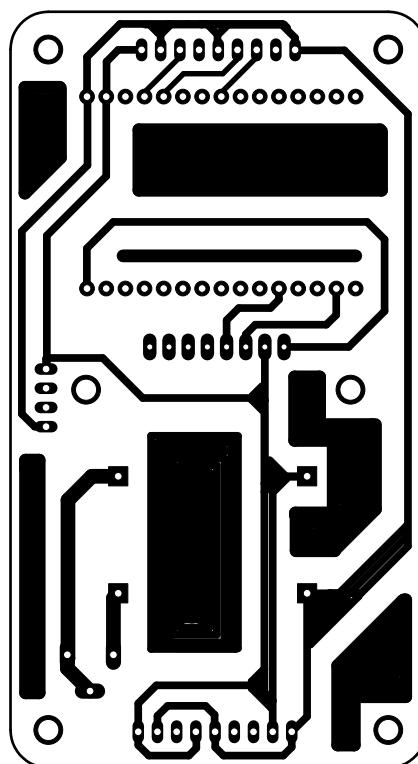
# SILK



# TOP



# BOT



Ketentuan Cetak PCB:

- File PDF hitam (jalur) - putih (background) asli: export tanpa mirror
- Pad-Vias > Hole min 0,6mm
- Jarak antar jalur: space trace min 0,35mm
- Jika ada polygon: Space Polygon min 1mm

Note:

- Belum bisa menggunakan through hole
- Jika tidak mengikuti ketentuan, resiko hasil cetak kurang maksimal (rawan short)

## Contoh MASK

- Background: Hitam, Pad Vias: Putih
- Ukuran Pad Vias diperbesar dari pad vias jalur PCB, untuk memudahkan soldering