

University of Patras - Polytechnic School  
Department of Electrical Engineering  
and Computer Technology



ΠΑΝΕΠΙΣΤΗΜΙΟ  
ΠΑΤΡΩΝ  
UNIVERSITY OF PATRAS

Department: Electronics and Computers Department  
Laboratory: Network Architectures & Management group, Applied  
Electronics Laboratory

## Thesis

of the student of Department of Electrical Engineering  
and Computer Technology of the Polytechnic School of the University of Patras

Ilias Xenogiannis of Efstathios

record number: 1053608

## Subject

**ML THESIS**

## Supervisor

Associate Professor Dr. Sophia Daskalaki, University of Patras

Thesis Number:

Patras, Dec 2023



# ΠΙΣΤΟΠΟΙΗΣΗ

Πιστοποιείται ότι η διπλωματική εργασία με θέμα

## ML THESIS

του φοιτητή του Τμήματος Ηλεκτρολόγων Μηχανικών και Τεχνολογίας  
Υπολογιστών

Ilias Xenogiannis of Efstathios

(A.M.: 1053608)

παρουσιάστηκε δημόσια και εξετάστηκε στο τμήμα Ηλεκτρολόγων Μηχανικών και  
Τεχνολογίας Υπολογιστών στις

\_\_\_/\_\_\_/\_\_\_

Ο Επιβλέπων

Ο Διευθυντής του Τομέα

Dr. Sophia Daskalaki  
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# CERTIFICATION

It is certified that the Thesis with Subject

**ML THESIS**

of the student of the Department of Electrical Engineering & Computer Technology

Ilias Xenogiannis of Efstathios

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Was presented publicly and defended at the Department of Electrical Engineering &  
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## Thesis details

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This thesis was written in L<sup>A</sup>T<sub>E</sub>X.





# Abstract

To be filled



# Acknowledgements

To be filled



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# Glossary

Imdb TK

xvii



# Acronyms

**QA** Quality Assurance

xvii



# 1. Introduction

To be filled





## **2. Problem statement- State of the art**

To be filled



### **3. System Architecture**



# 4. Implementation

TK

## 4.1 Scenario

TKTK

### 4.1.1 K

TK



## **5. Discussion**

TK





## 6. Results

TK



## **7. Future Work**

TK



# **Bibliography**



