Iliass Lasri

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Research Interests : Audio/Speech Models, Edge/Efficient AI

WORK EXPERIENCE

Pulse Audition part of EssilorLuxottica — **Nice, France**
AI Engineer Intern — *Jan–June 2025*

- Worked on Deep Learning based Acoustic Feedback Suppression in hearing-aid embedded in smart glasses.
- Modeled the phenomenon and implemented a highly configurable simulation used for training and evaluation.
- Designed and implemented training pipelines for online/offline speech models, integrating multiple innovative training techniques howling detection, and Designed an evaluation pipeline.
- Improved Added Stable Gain.

Huawei Paris Research Center — **Paris, France**
Research Intern (NDA) — *July–December 2024*

- Conducted computer architecture research in Hardware Accelerators (Many-core processors, Distributed Shared Memory).
- Improved SoC design and the kernel to maximize the Hardware Accelerators' utilization.
- Learned about HW/SW co-design, and worked on modern efficient C/C++, compilers & kernels.

Talan — **Tunis, Tunisia**
Summer Intern — *July–August 2023*

- Resource efficient ML algorithms, and prototyping on Raspberry-pi.

EDUCATION

École Normale Supérieure (ENS Paris-Saclay) — **Paris, France**
MSc MVA (Mathematiques, Vision, Apprentissage) — *Sept 2025–Present*

Télécom Paris - Polytechnic Institute of Paris — **Paris, France**
MSc in Engineering — *Sept 2022–Present*

- **Double Major:** Applied Math (Signal Processing & IA) & Embedded Systems
- **Relevant Courses:** Advanced Stats, Time Series, Signal Processing, ML/DL, Deep Learning Applied to Audio, etc.
- **GPA**: 3.98/4

Lycée Mohammed VI d'Excellence (Lydex) — **Benguerir, Morocco**
Sept 2020–Apr 2022

- **Preparatory Classes:** Advanced Mathematics, Physics, Mechanical and Electrical Eng.
- **GPA**: 4/4, **9th** in the Centrale Supelec competition, **7th** in Mines-Ponts competition.

PROJECTS

Text-To-Speech

- Developed a neural TTS system in pytorch using pretrained WhisperTokenizer, and EnCodec (RVQ) for high-fidelity speech synthesis [github]. WIP.

AIoT based Neural Decoding and Neurofeed-back for a Cognitive Training Acceleration — **Paris, France**
Supervisor: Van-Tam Nguyen @Telecom Paris — *May–July 2023*

- The main objective of this project is to design adequate tiny machine learning algorithms in order to extract the relevant features and characteristics from EEG and ECG signals, I implemented them on MCU and finally designed a neuro-feedback system to accelerate brain training.

Development on bare-metal STM32 IoT Node, STM32 microcontroller — **Paris, France**
Sep 2022 – Feb 2023

- Initiating the development with the boot-loader and the crt0 file, I implemented a custom user interface for data reception, displaying, and configured IRQ and UART, among other functionalities.
- I did the project in C [github] and in Rust [github].

EXTRA CURRICULAR ACTIVITIES

- Deep Learning Specialization, by Andrew Ng et al on [coursera]
- TinyML and Efficient Deep Learning Computing, by Prof. *Song Han* at MIT [my notes][course]
- GPU Programming Specialization by Johns Hopkins University on [coursera]
- Competitive Programming

HACKATHONS PARTICIPATED IN MANY HACKATHONS IN THE PARIS REGION.

2024 - Mistral AI Paris Hackathon, QRT Data Challenge & LeRobot Hackathon by Hugging Face .
2023 - Attended Exolegend (Robotics), Master Dev France.
2022 - 74th/250 in the largest development competition in France at Master Dev France.

TECH SKILLS

Programming languages **: Python,C/C++, Rust**
PyTorch (proficient) **& TensorFlow**
CUDA, Git, Docker, GNU/Linux
Debugging tools like ***gdb***