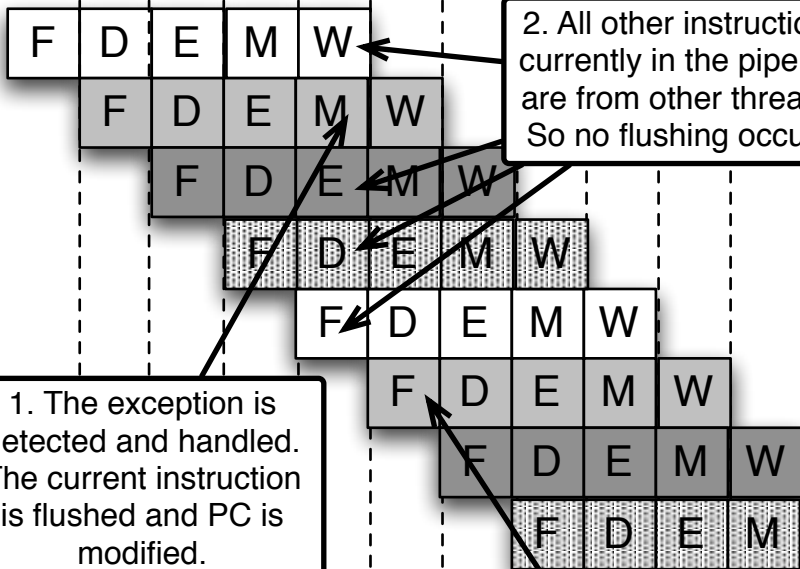


t0 t1 t2 t3 t4 t5 t6 t7 t8 t9 t10



2. All other instructions currently in the pipeline are from other threads. So no flushing occurs.

1. The exception is detected and handled. The current instruction is flushed and PC is modified.

3. This instruction fetches the exception handler code and begins handling the exception.