Cycle	Instruction	$t0
t	...	0
t+6	deadi $t0, (5) ———————→	5
t+12	sethi %hi(0x3f800000), %g1	4
t+18	or %g1, 0x200, %g1	3
t+24	add %g2, %g2, %g4	2
t+30	deadi $t0, (4)	1
t+36	<replay>	0
t+42	sethi %hi(0x3f800000), %g1	4
t+48	or %g1, 0x200, %g1	3

30 processor cycles