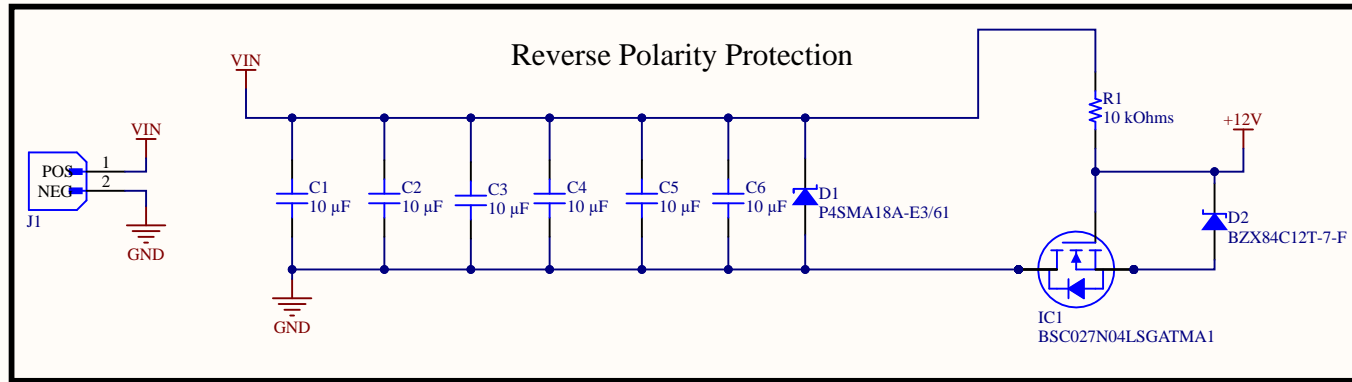
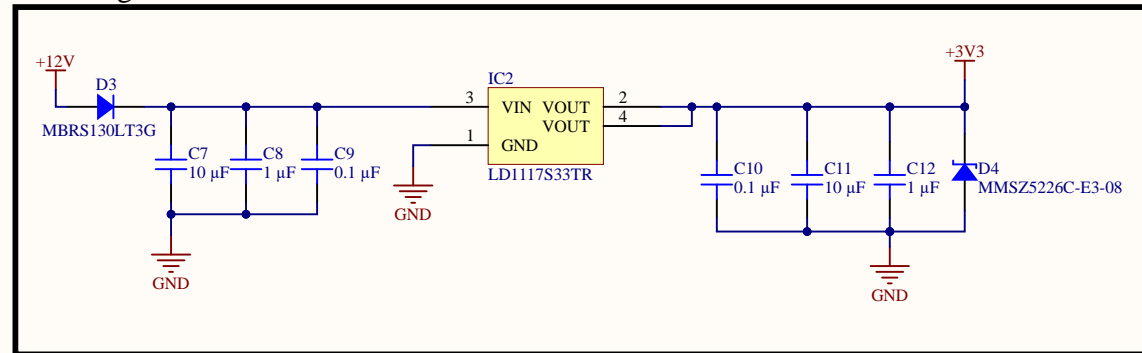


12V/10A Voltage Source



+3V3 Regulator



TITLE: Tracking Radar PCB.PrjPcb	
DOCUMENT NAME: Power Management.SchDoc	
PROJECT CODE: 02050838	Sheet: 3 of 9
TEMPLATE: A4	REVISION: V1.0
AUTHOR: İlker KESER	

K

A

B

C

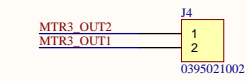
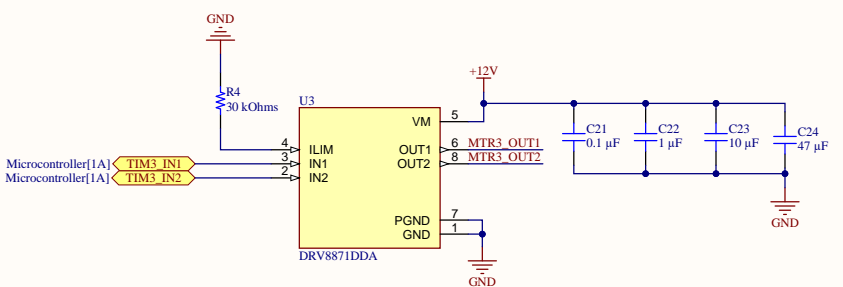
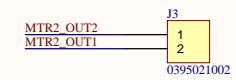
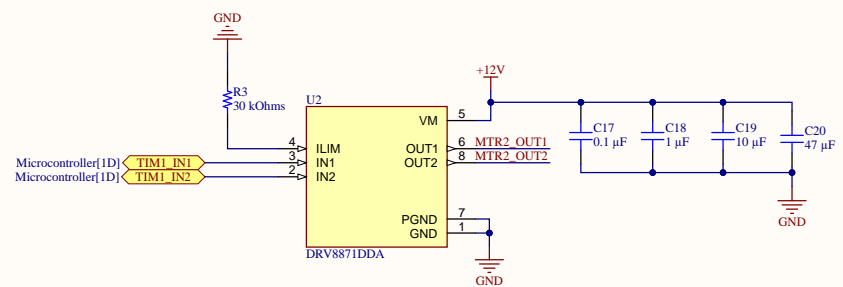
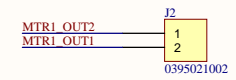
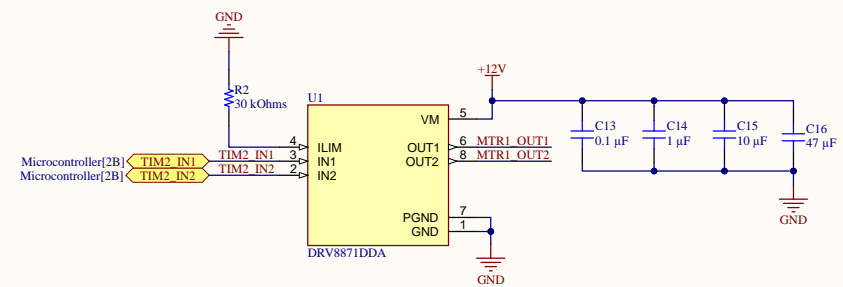
D

A

B

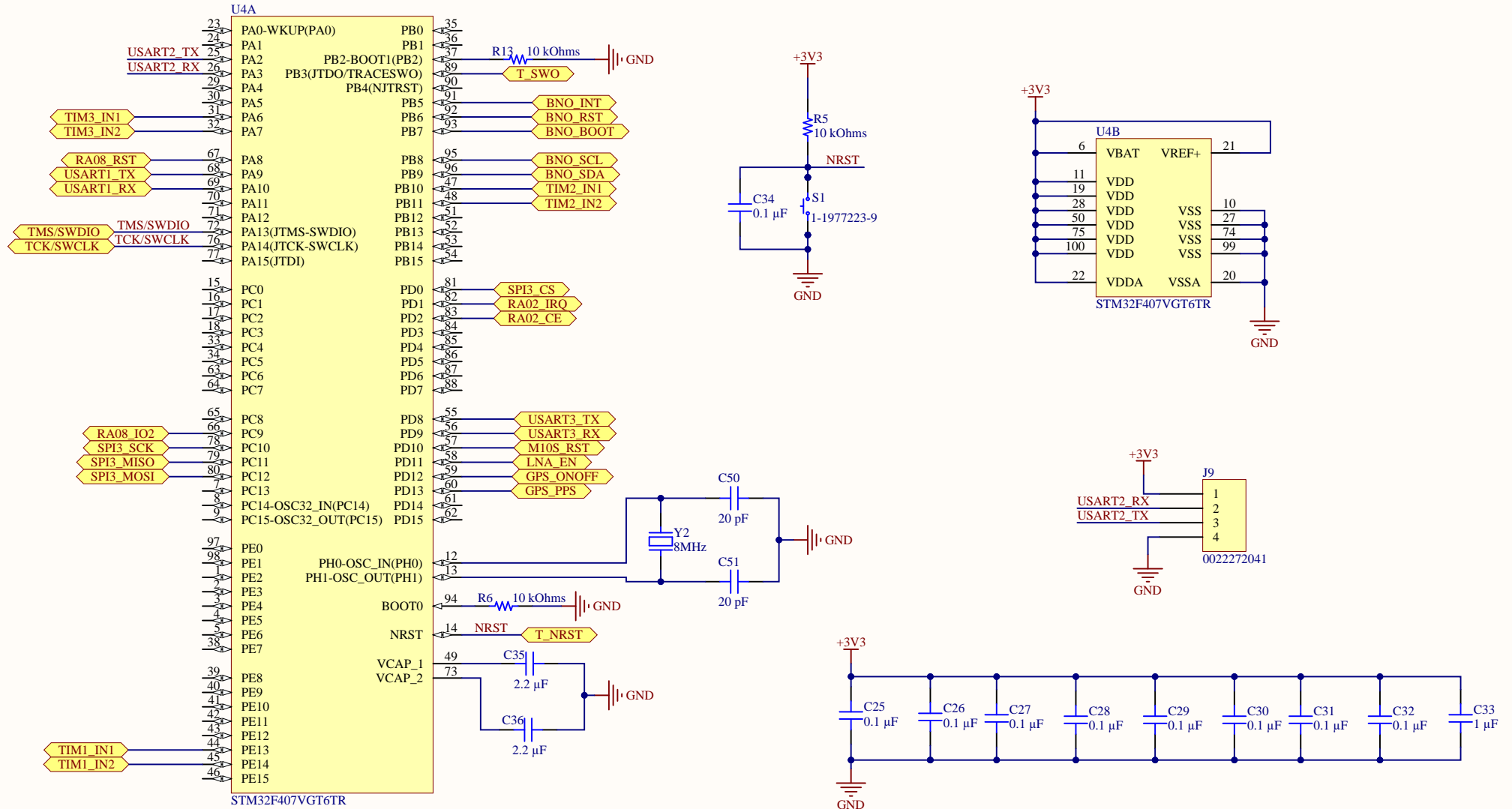
C

D



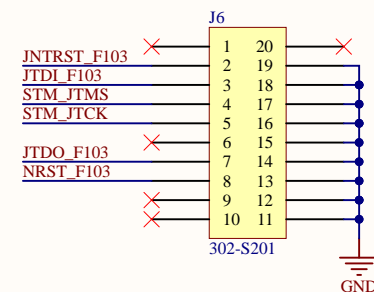
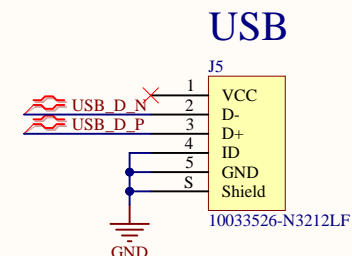
TITLE:		Tracking Radar PCB.PrjPcb	
DOCUMENT NAME:		Motor_Drivers.SchDoc	
PROJECT CODE:		02050838	Sheet: 4 of 9
TEMPLATE:		A3	REVISION: V1.0
AUTHOR:		İlker KESER	





TITLE: Tracking Radar PCB.PrjPcb	
DOCUMENT NAME: Microcontroller.SchDoc	
PROJECT CODE: 02050838	Sheet: 5 of 9
TEMPLATE: A4	REVISION: V1.0
AUTHOR: İlker Keser	

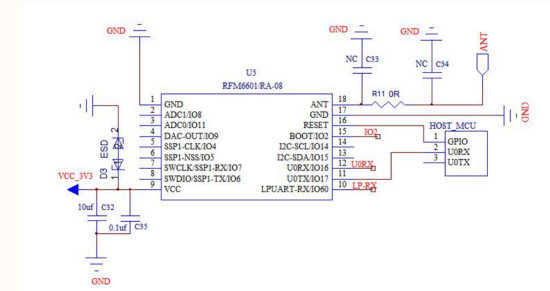
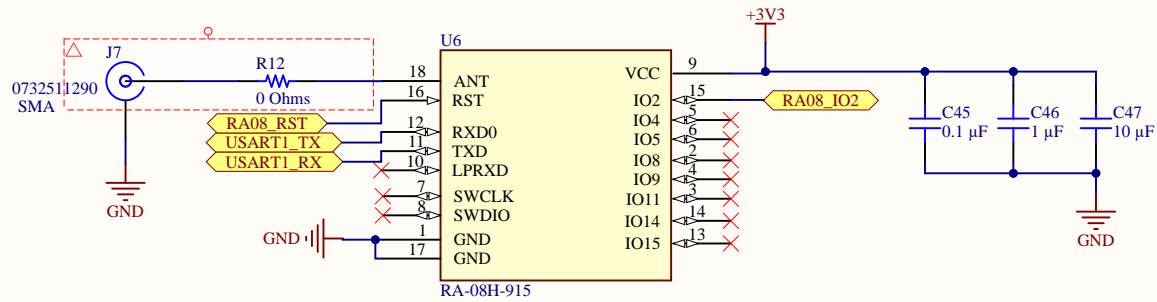
K



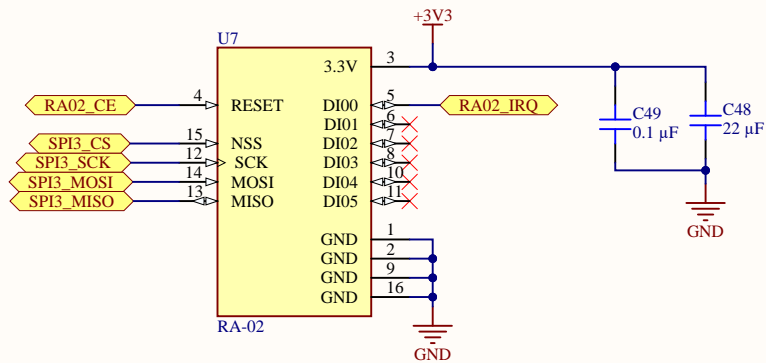
TITLE:		Tracking Radar PCB.PrjPcb	
DOCUMENT NAME:		Debugger.SchDoc	
PROJECT CODE:		02050838	Sheet: 6 of 9
TEMPLATE: A4		REVISION: V1.0	
AUTHOR:		İlker KESER	

K

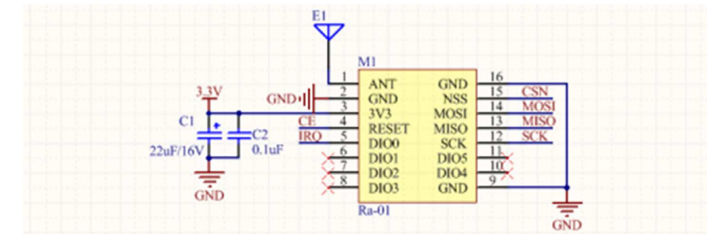
915MHz LoRa



433MHz LoRa



The connection diagram of Ra-01/Ra-02 module and MCU is shown in the figure below:



TITLE: Tracking Radar PCB.PrjPcb			
DOCUMENT NAME: LoRa_Com.SchDoc			
PROJECT CODE: 02050838	Sheet: 7 of 9		
TEMPLATE: A4	REVISION: V1.0		
AUTHOR: İlker KESER			

K

A

A

B

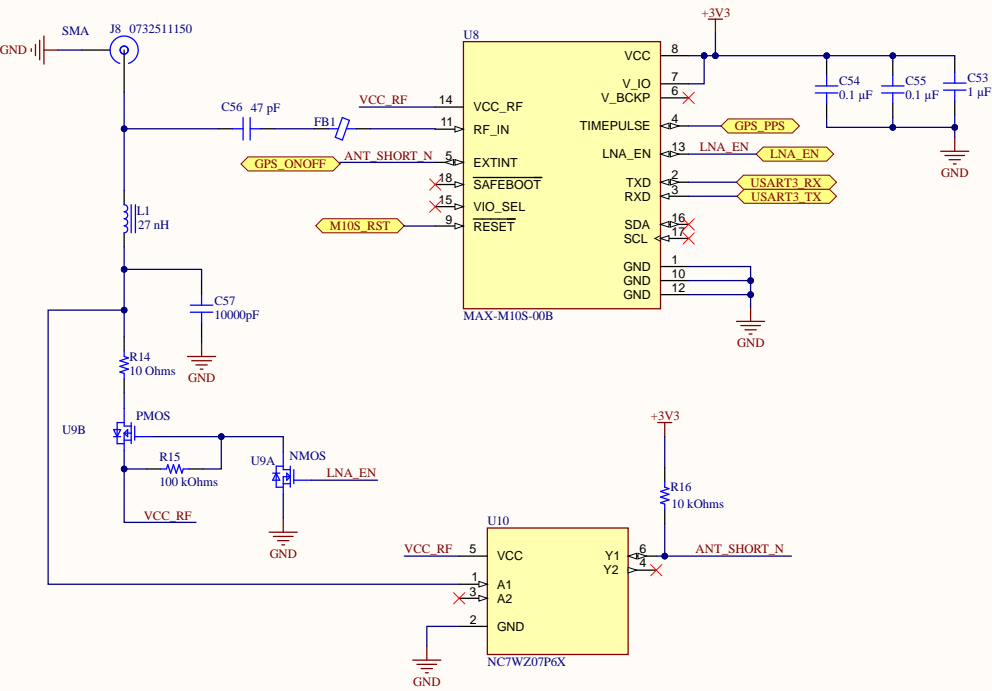
B

C

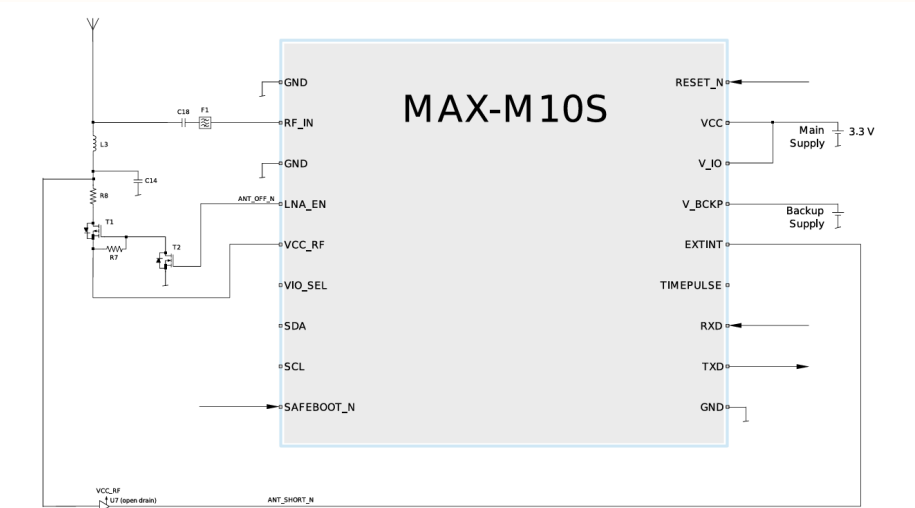
C

D

D

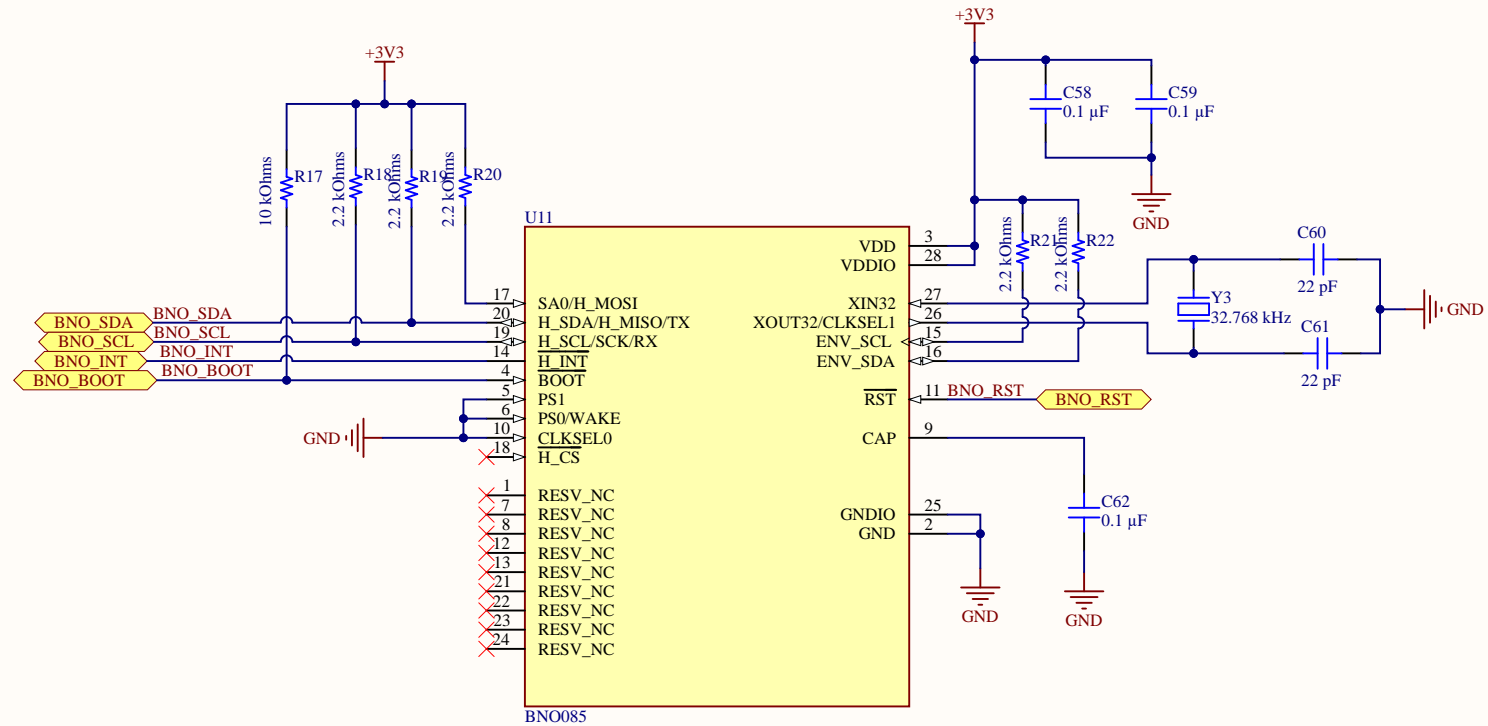


Reference Design



TITLE:		Tracking Radar PCB.PrjPcb	
DOCUMENT NAME:		GPS_GNSS.SchDoc	
PROJECT CODE:		02050838	Sheet: 8 of 9
TEMPLATE:		A3	REVISION: V1.0
AUTHOR:		İlker KESER	





BNO085

I2C Address: 100101(SAO = 1) --> 0x4B

BNO_INT: Active Low

BNO_RST: Active Low Reset

BNO_BOOT: BOOT is sampled at reset. If low the BNO08X will enter bootloader mode

TITLE: Tracking Radar PCB.PrjPcb	
DOCUMENT NAME: IMU_Sensor.SchDoc	
PROJECT CODE: 02050838	Sheet: 9 of 9
TEMPLATE: A4	REVISION: V1.0
AUTHOR: İlker KESER	

K

