Jeremiah Webb

CEC320 Workshop 6 Artifacts

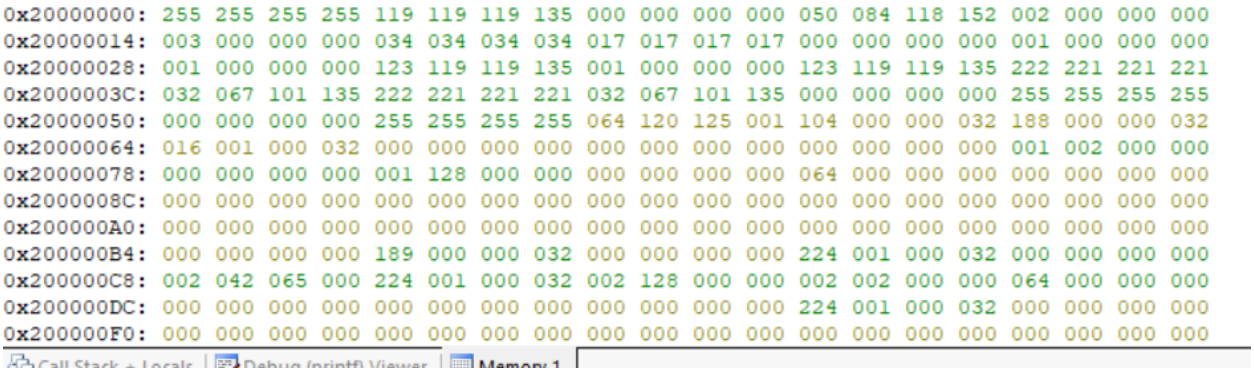
Assembly Code Snippet

```
          AREA myfunctions, CODE
          EXPORT determine_data_order
          EXPORT add_uint64_s
          EXPORT sub_uint64_s
          EXPORT tc_uint64_to_int64_s
          ALIGN
determine_data_order PROC
          BX lr
          ENDP
add_uint64_s PROC
          ADDS R0, R2
          ADC R1, R3
          BX lr
          ENDP
sub_uint64_s PROC
          SUBS R0, R2
          SBC R1, R3
          BX lr
          ENDP
tc_uint64_to_int64_s PROC
          MVN R0, R0
          MVN R1, R1
          ADDS R0, #1
          ADC R1, #0
          BX lr
```

```
        ENDP

        END
```

Addresses



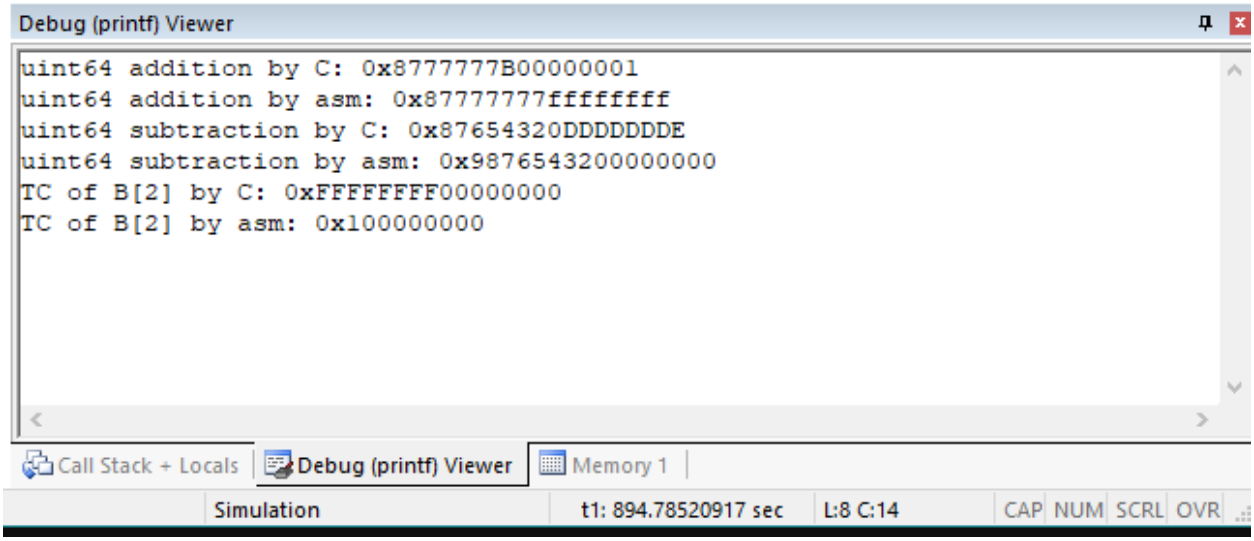
R0 = starts at 0x20000000

R1 = starts at 0x20000008

R2 = starts at 0x20000010

R3 = Starts at 0x20000018

Printf Viewer Task 1 Initial



Printf Viewer Final