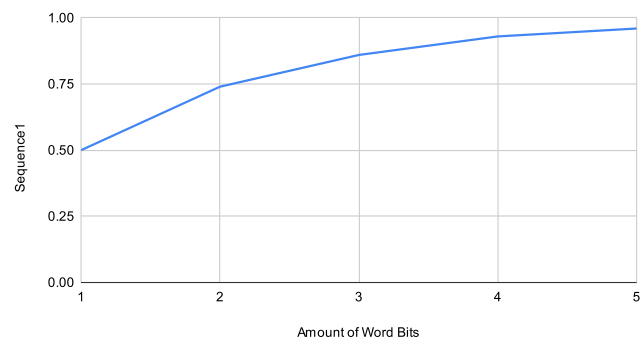
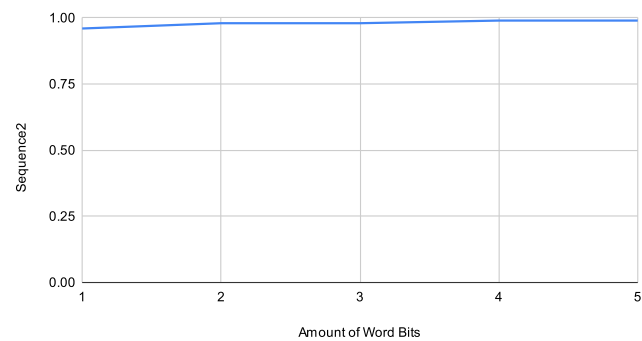


Amount of Word Bits	Sequence1	Sequence2	Sequence3	Sequence4	Sequence5
1	0.5	0.96	0.98	0.74	0.2
2	0.74	0.98	0.98	0.87	0.6
3	0.86	0.98	0.99	0.93	0.6
4	0.93	0.99	0.99	0.96	0.6
5	0.96	0.99	0.99	0.98	0.6

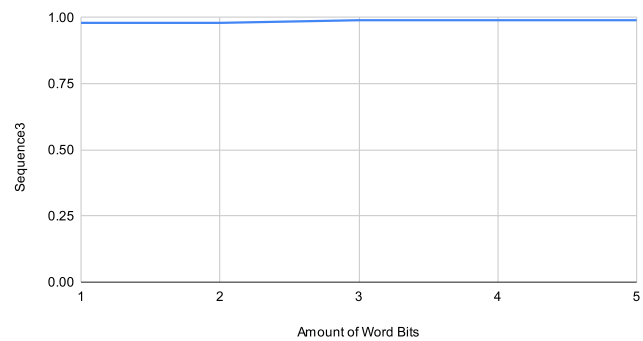
Sequence1 vs. Amount of Word Bits



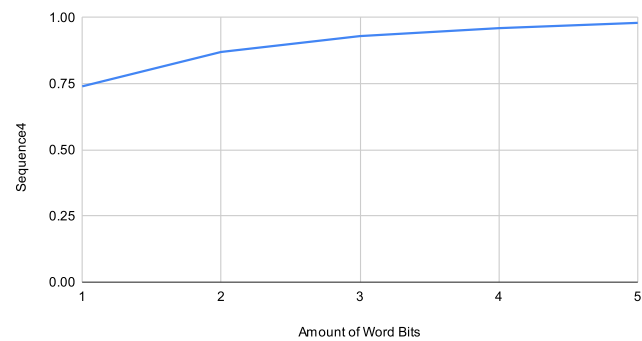
Sequence2 vs. Amount of Word Bits

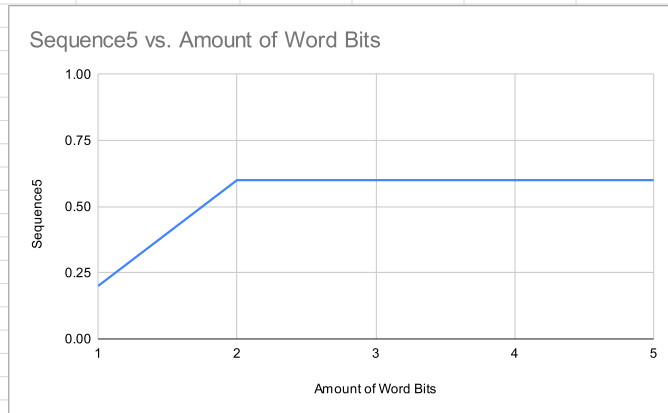


Sequence3 vs. Amount of Word Bits



Sequence4 vs. Amount of Word Bits





As shown, the cache can work. However, with some operations, for example, sequence 5, the number of word bits can be redundant, and not needed for computation. However, each sequence was different for each trial in terms of memory location calling and cache calling. As shown, using a cache can be efficient. Although the cache does depend on separate functionality and code to reserve blocks, it can be efficient for runtime and memory efficiency.