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CEC320 Workshop 6 Artifacts

Assembly Code Snippet

AREA myfunctions, CODE

EXPORT determine\_data\_order

EXPORT add\_uint64\_s

EXPORT sub\_uint64\_s

EXPORT tc\_uint64\_to\_int64\_s

ALIGN

determine\_data\_order PROC

BX lr

ENDP

add\_uint64\_s PROC

ADDS R0, R2

ADC R1, R3

BX lr

ENDP

sub\_uint64\_s PROC

SUBS R0, R2

SBC R1, R3

BX lr

ENDP

tc\_uint64\_to\_int64\_s PROC

MVN R0, R0

MVN R1, R1

ADDS R0, #1

ADC R1, #0

BX lr

ENDP

END

Addresses

Table

Description automatically generated

R0 = starts at 0x20000000

R1 = starts at 0x20000008

R2 = starts at 0x20000010

R3 = Starts at 0x20000018

Printf Viewer Task 1 Initial

Graphical user interface, text

Description automatically generated

Printf Viewer Final

Graphical user interface, text, application, email

Description automatically generated