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## \* Difference between CISC and RISC.

CISC	RISC
1) CISC stands for ' <u>Complex Instruction Set Computer</u> '	2) RISC stands for ' <u>Reduced Instruction Set Computer</u> '.
2) It is a CPU design plan based on <u>single commands</u> , which are <u>skipped</u> in multi-step operation.	2) It is a CPU design plan based on <u>simple orders</u> and <u>acts fast</u> .
3) <u>It has a huge set of instructions</u> , which takes a <u>long time to perform</u> .	3) <u>This is a small or reduced set of instructions</u> which are <u>modest &amp; simple</u> , which help in <u>comprising more complex commands</u> .
4) The CISC processors have a <u>memory unit</u> to <u>implement complex instructions</u>	4) <u>RISC has no memory unit</u> and uses a <u>separate hardware</u> to <u>implement instructions</u>
5) CISC is a <u>easy compiler design</u> and has a <u>microprogramming unit</u> .	5) <u>RISC is a complex compiler design</u> and has a <u>hard wired unit of programming</u> .
6) CISC is <u>used in low-end applications</u> such as <u>security systems, home automation, etc.</u>	6) RISC is <u>used in high-end applications</u> like <u>video processing, telecommunications &amp; image processing</u>