PROGRESS MADE SO FAR

**February 8, 2022**

Domains Explored

▷ In-memory Computing

▷ Memory Compilers

Progress Made

- **Learned Cadence -** Learned the basic concepts such as the design of schematic, symbol, layout, their validation, accounting for parasitics, parametrization, and simulation types

- **Tried a Cadence example** - Tried designing a CMOS Inverter on Cadence including schematic, symbol, and a test bench, containing a few parameters.

Symbol

Schematic
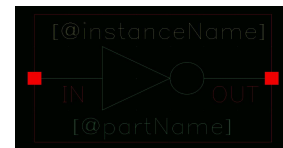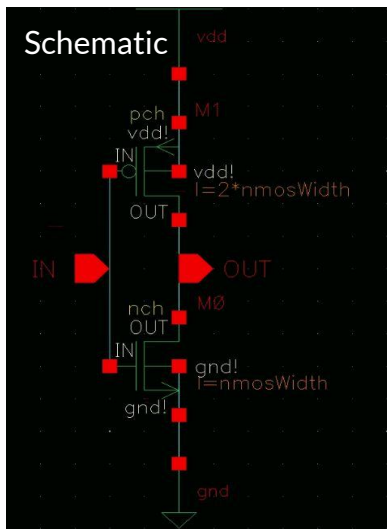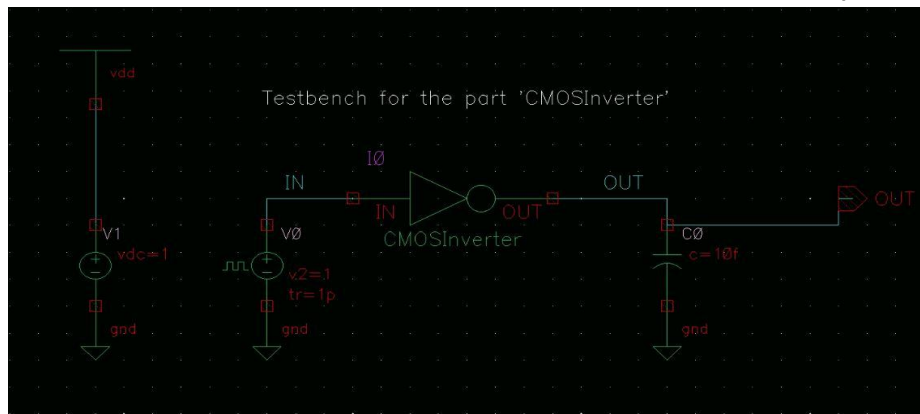
Progress Made

- **Question** - What is a 'via' in layout design

- Timeline
  - **1st week of Feb** - Learning Cadence and Starting Implementing Components
  - **2nd week of Feb -** Decoding OpenRAM's Source Code
  - **End of Feb -** Compiling all the components, code, and understanding how to join the pieces together
  - **March -** Solving issues faced before, and working on our memory compiler
  - **April -** Writing a paper

- **Next Step** - Decoding OpenRAM's Code

References

- [Cadence Tutorials - YouTube](#)
- [Design a CMOS inverter using Cadence Virtuoso - YouTube](#)