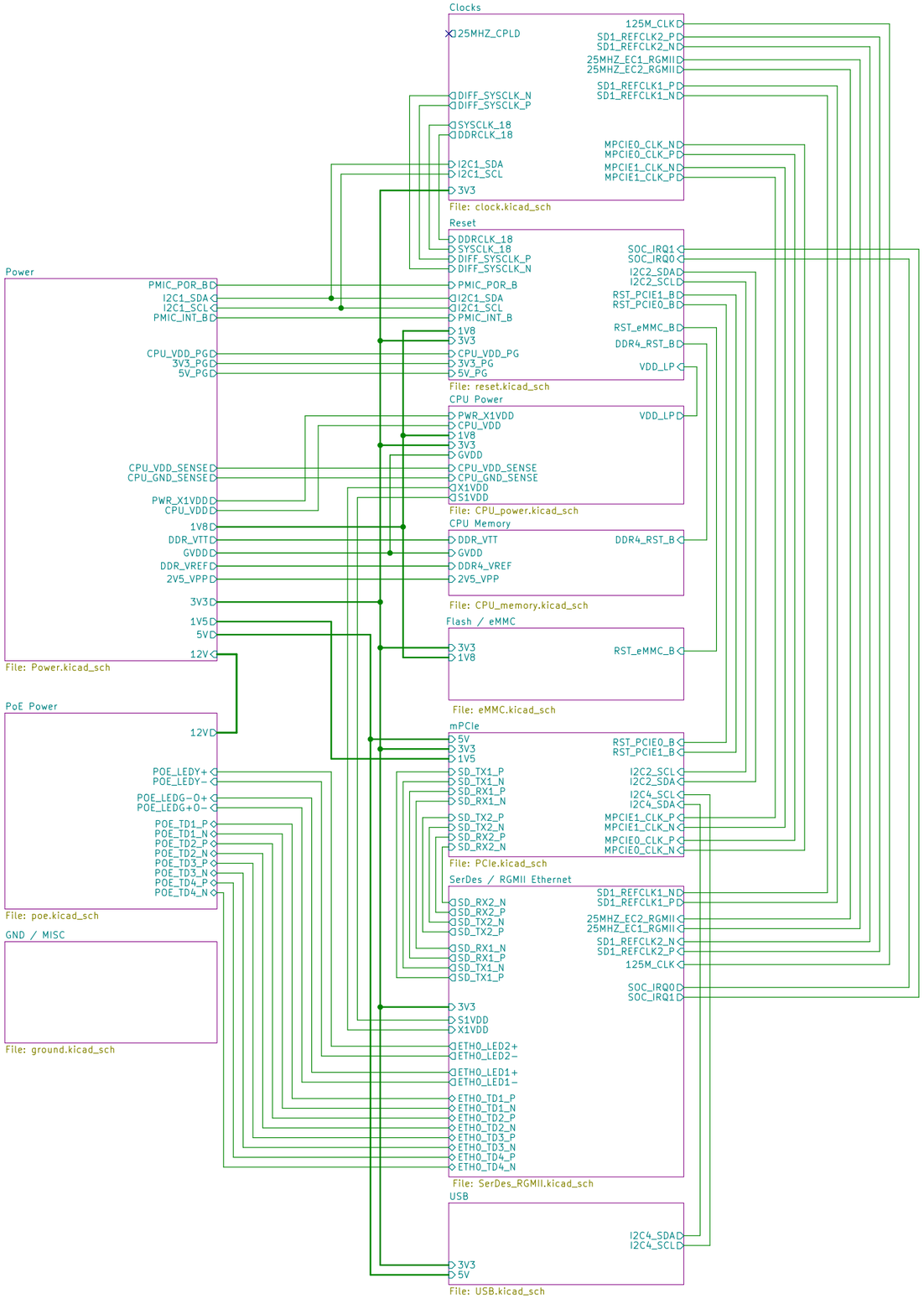

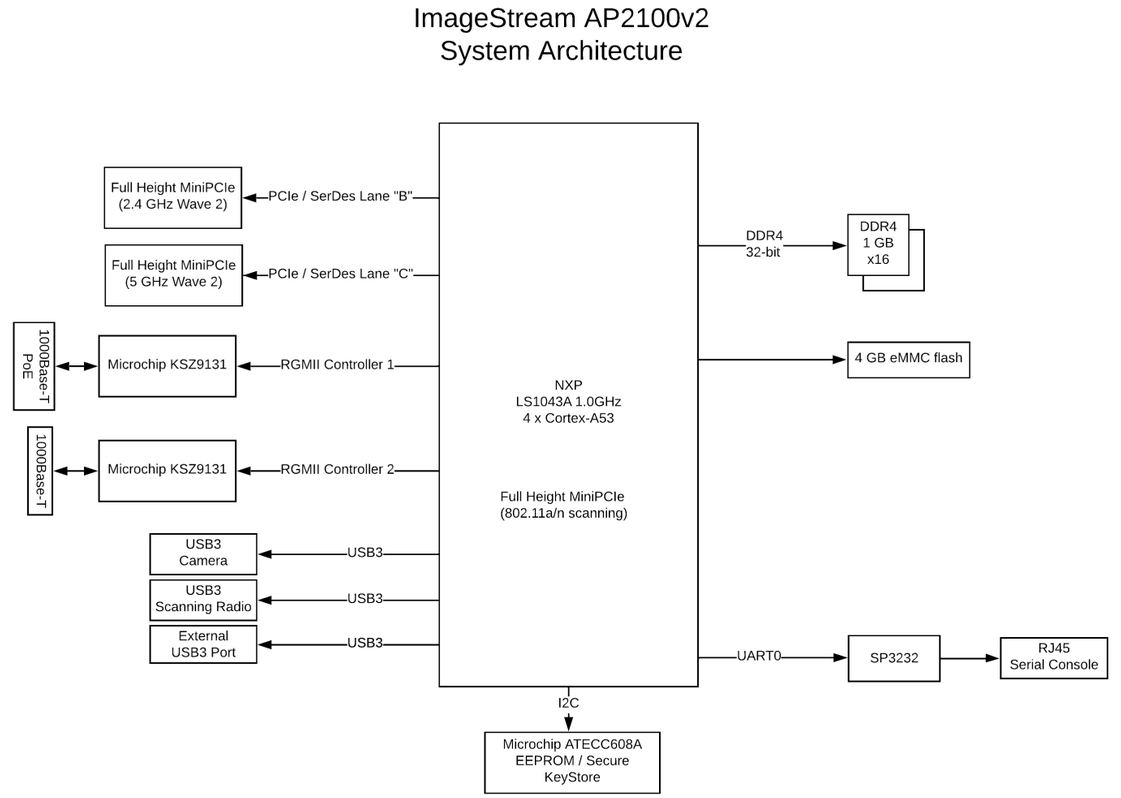

ISis ImageStream Internet Solutions, Inc.		
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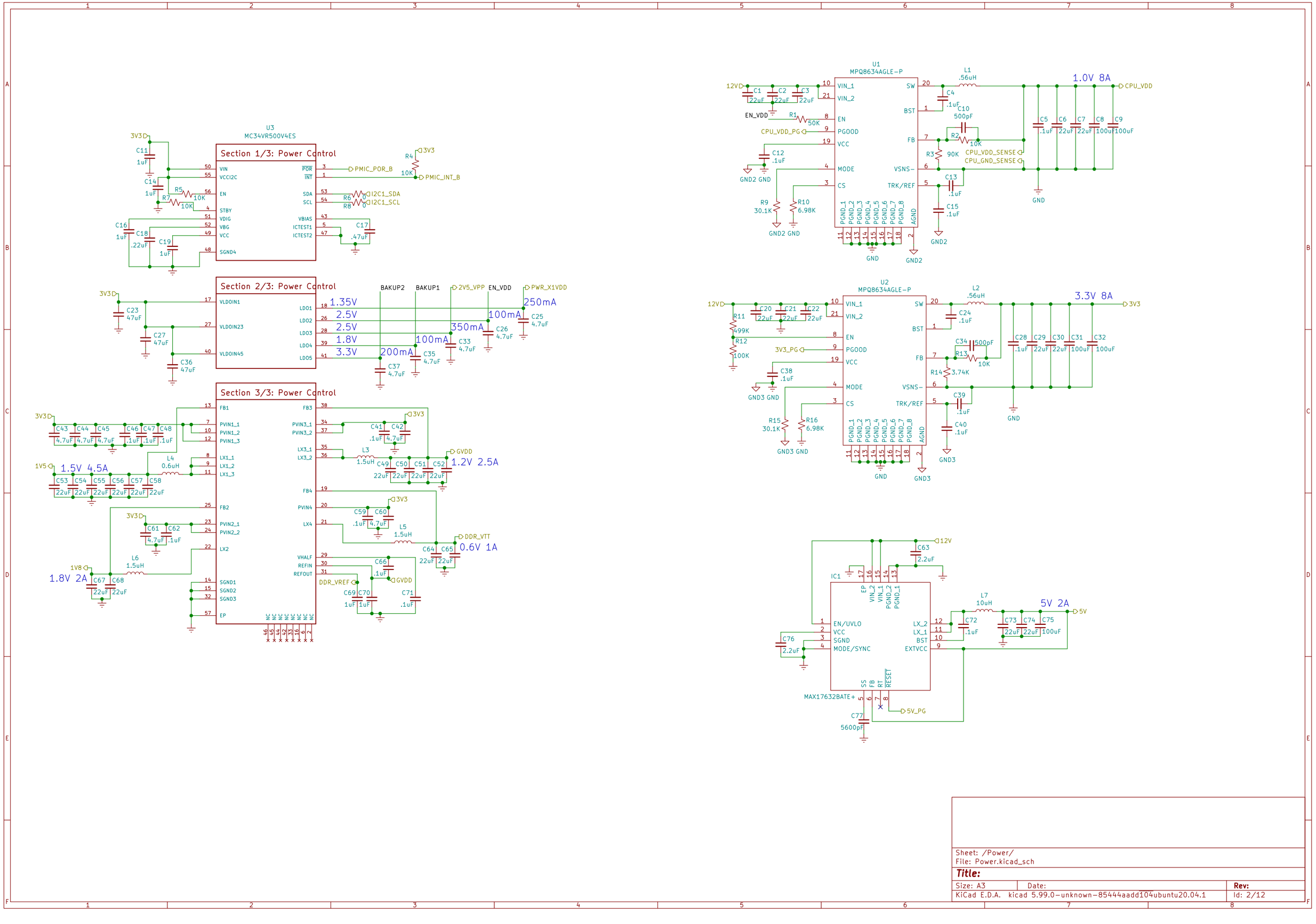
Title: AP2100		
Size: A3	Date: 2020-09-30	**Rev: 1**
KiCad E.D.A. kicad 5.99.0-unknown-85444aadd104ubuntu20.04.1		Id: 1/12



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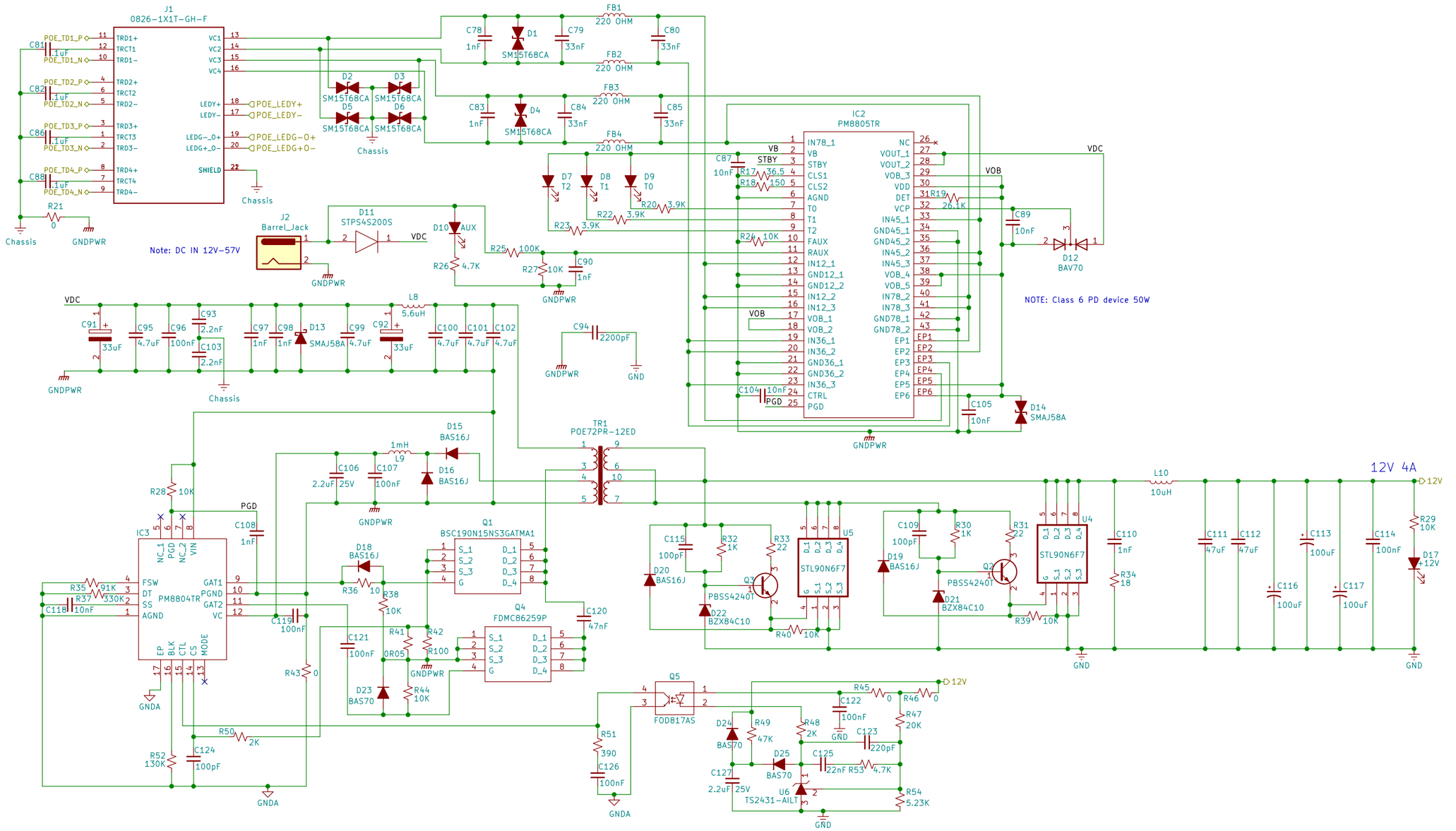
KiCad E.D.A. kicad 5.99.0-unknown-85444aadd104ubuntu20.04.1		Id: 2/12



IS ImageStream Internet Solutions, Inc.

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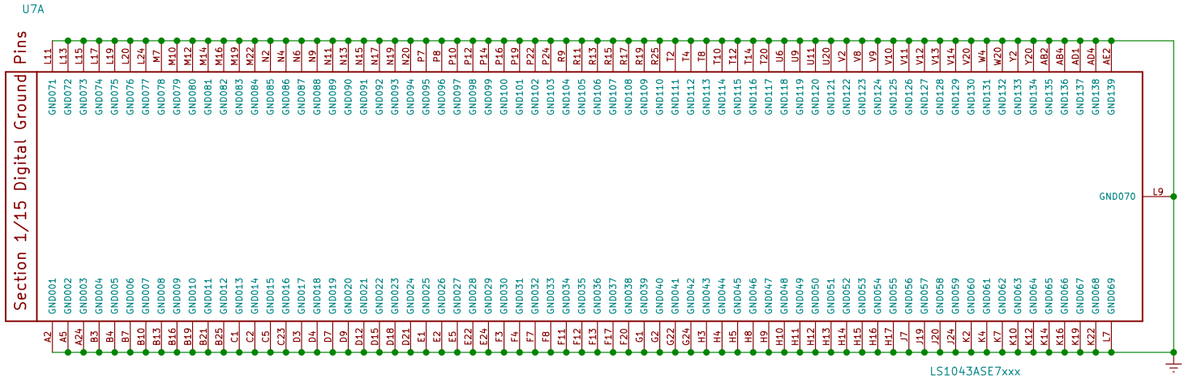
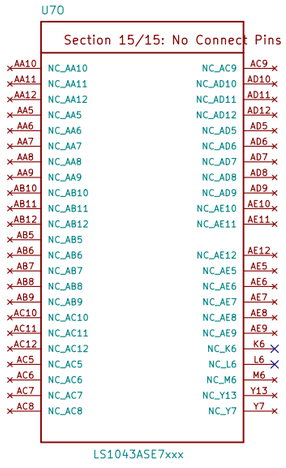
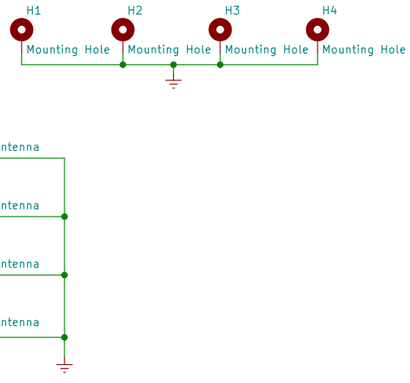
Size: A3	Date:	**Rev:**
KiCad E.D.A. kicad 5.99.0-unknown-85444aadd104ubuntu20.04.1		Id: 3/12







Sheet: /GND / MISC/
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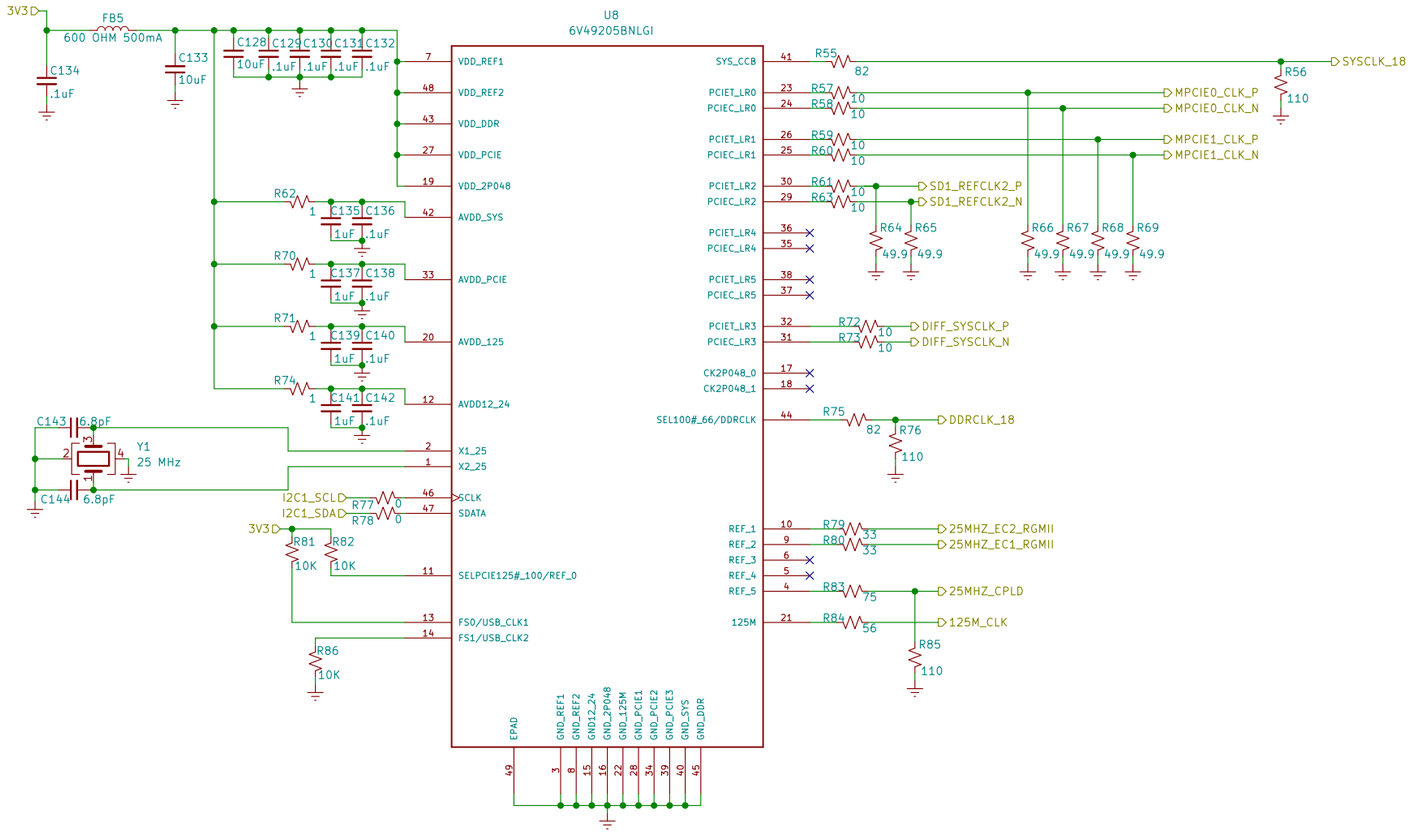
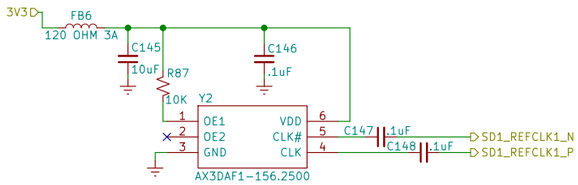
Title:

Size: A3	Date:	**Rev:**
KiCad E.D.A. kicad 5.99.0-unknown-85444aadd104ubuntu20.04.1		Id: 4/12



SERDES 10G Clock



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Title:		
Size: A3	Date:	**Rev:**
KiCad E.D.A. kicad 5.99.0-unknown-85444aadd104ubuntu20.04.1		Id: 5/12

U7E
LS1043ASE7xxx

Section 5/15: JTAG, UARTs, Interrupts and Miscellaneous Functions

IRQ00	(OVDD: 1.8V)	(Note 1)	(DVDD: 3.3V/1.8V)	GPIO1_17/UART1_SIN	H2
IRQ01	(OVDD: 1.8V)	(Note 1)	(DVDD: 3.3V/1.8V)	GPIO1_15/UART1_SOUT	H1
IRQ02	(OVDD: 1.8V)	(Note 1)	(DVDD: 3.3V/1.8V)	GPIO1_21/UART1_CTS	J1
IRQ11/GPIO1_31	(LVDD: 2.5V/1.8V)	(Note 1)	(DVDD: 3.3V/1.8V)	GPIO1_19/UART1_RTS	J2
IRQ03/GPIO1_23	(DVDD: 3.3V/1.8V) (Note 1)				
IRQ04/GPIO1_24	(DVDD: 3.3V/1.8V) (Note 1)	(Note 1)	(DVDD: 3.3V/1.8V)	GPIO1_18/UART2_SIN	K1
IRQ05/GPIO1_25	(DVDD: 3.3V/1.8V) (Note 1)	(Note 1)	(DVDD: 3.3V/1.8V)	GPIO1_16/UART2_SOUT	L2
IRQ06/GPIO1_26	(DVDD: 3.3V/1.8V) (Note 1)	(Note 1)	(DVDD: 3.3V/1.8V)	GPIO1_22/UART2_CTS	M2
IRQ07/GPIO1_27	(DVDD: 3.3V/1.8V) (Note 1)	(Note 1)	(DVDD: 3.3V/1.8V)	GPIO1_20/UART2_RTS	L1
IRQ08/GPIO1_28	(DVDD: 3.3V/1.8V) (Note 1)				
IRQ09/GPIO1_29	(DVDD: 3.3V/1.8V) (Note 1)		(DVDD: 3.3V/1.8V)	IIC1_SCL	N1
IRQ10/GPIO1_30	(DVDD: 3.3V/1.8V) (Note 1)		(DVDD: 3.3V/1.8V)	IIC1_SDA	M1
TA_TMP_DETECT	(OVDD: 1.8V)		(OVDD: 1.8V)	ASLEEP/GPIO1_13/CFG_SOIC_USE	E9
TA_BB_TMP_DETECT	(TA_BB_VDD: 0.9V/1V)				C21
TA_BB_RTC	(TA_BB_VDD: 0.9V/1V)		(OVDD: 1.8V)	TBSCAN_EN	F21
			(OVDD: 1.8V)	TEST_SEL	
RESET_REQ	(OVDD: 1.8V)				R6
HRESET	(OVDD: 1.8V)		(Analog)	TD1_ANODE	P6
PORESET	(OVDD: 1.8V)		(Analog)	TD1_CATHODE	
SYSCLK	(OVDD: 1.8V)				
DDRCLK	(OVDD: 1.8V)	(Note 1)	(DVDD: 2.5V/1.8V)	GPIO4_02/IIC2_SCL	K3
		(Note 1)	(DVDD: 2.5V/1.8V)	GPIO4_03/IIC2_SDA	L3
DIFF_SYSCLK	(OVDD: 1.8V)				
DIFF_SYSCLK	(OVDD: 1.8V)		(OVDD: 1.8V)	EVT0	E10
			(OVDD: 1.8V)	EVT1	E13
CLK_OUT	(OVDD: 1.8V)		(OVDD: 1.8V)	EVT2	E8
TH_TPA	(Analog)		(OVDD: 1.8V)	EVT3	E12
			(OVDD: 1.8V)	EVT4	E11
RTC/GPIO1_14	(OVDD: 1.8V)		(OVDD: 1.8V)	EVT9	H7
TDI	(OVDD: 1.8V)		(OVDD: 1.8V)	CKSTP_OUT	H18
TDO	(OVDD: 1.8V)		(OVDD: 1.8V)	SCAN_MODE	A21
TCK	(OVDD: 1.8V)		(OVDD: 1.8V)	JTAG_BSR_VSEL	F21
TMS	(OVDD: 1.8V)		(Reserved, Analog)	FA_ANALOG_PIN	T7
TRST	(OVDD: 1.8V)		(Reserved, Analog)	FA_ANALOG_G_V	U7

Note1: This pin has other functions not listed here.

CONSOLE_TX / CONSOLE_RX (R88 DNP 0, R89 0)
CONSOLE_RX / CONSOLE_TX (R90 DNP 0, R91 0)
CONSOLE_RTS / CONSOLE_CTS (R95 DNP 0, R96 0)
CONSOLE_CTS / CONSOLE_RTS (R97 DNP 0, R98 0)
UART_RX, UART_TX, UART_CTS, UART_RTS
RST_PCIE0, RST_PCIE1
RESET_BTN_B (R99 10K, SW1)
I2C1_SCL, I2C1_SDA
I2C2_SCL, I2C2_SDA (R100, R101, R102, R103 1K)

CPU Temp

U10 SA56004EDP,118 (SCLK, SDATA, D+, D-, VDD, ALERT, THERM, GND)
R108 4.7K, R107 DNP 4.7K, R109 1K, R111 0, R112 0
C149 .1uF, C150 1000pF, C151 .1uF
R113 0, R114 0, R115 10K, R116 10K
100 ohm = 1.6MHZ Cutoff
U9B 74LVC2G08, THERM_INTn

JTAG

Conn_ARM_JTAG_SWD_10 J3 (VTref, RESET, SWDCLK/TCK, SWDIO/TMS, SWO/TDO, NC/TDI, GNDDetect, GND)
R122 10K, R123 10K, JTAG_RST_B_18
R130 10K, R131 0
TP3, RTC_OUT, PORESET_B_18, R129 DNP 0
R117–R124 4.7K, R125 4.7K, R127 1K, R126 DNP 4.7K, R128 4.7K

Serial Console

U11 SP3232EBEY-L/TR (C1+, C1-, C2+, C2-, VCC, VS+, VS-, T1IN, T2IN, R1OUT, R2OUT, T1OUT, T2OUT, R1IN, R2IN, LOGIC, GND, RS232)
C152 .1uF, C153 .1uF, C154 .1uF, C155 .1uF, C156 .1uF
CONSOLE_TX, CONSOLE_CTS, CONSOLE_RX, CONSOLE_RTS
J4 RJ45, R141 DNP 0

Reset Logic

IC4 MIC2774N-29YM5-TR (IN, VDD, RST, GND, MR)
R132 91K, R133 0, R134 0, R135 0, R138 DNP 0
CPU_VDD_PG, 3V3_PG, 5V_PG, PMIC_POR_B
RESET_REQ_B_18, R136 0, C158 .1uF
R143 4.7K, U12 74LVC1G08, C160 .1uF, R144 330, LED1
R145 91K, U13 74LVC1G08, R146 0, C162 .1uF
JTAG_RST_B_18, PORESET_B_18
U14, U15, U16, U17, U19, U20 74LVC1G07
R147 0, R148 0, C161 .1uF, C163 .1uF, C164 .1uF, C166 .1uF, C167 .1uF, C168 .1uF
DDR4_RST_B, RST_eMMC_B, RST_PCIE0, RST_PCIE0_B, RST_PCIE1, RST_PCIE1_B

PWRLED

Real Time Clock

IC5 PCF2129T_2,518 (SCL, SDI, SDO, SDA/CE, IFS, TS, CLKOUT, VSS, VDD, VBAT, BBS, INT, NC_4, NC_3, NC_2, NC_1)
R137 1K, R139 0, R142 0, R140 0
C157 .1uF, C159 .1uF, BT1 CR2032
I2C1_SCL, I2C1_SDA, RTC_OUT

LEDs

WIFI1_LED, WIFI2_LED, STATUS_LED_R, STATUS_LED_G, STATUS_LED_B
LED2, LED3, LED4 (R, G, B)
R149 220, R150 220, R152 220, R154 220, R155 220
LED1, LED2, STATUS LED

Crypto / Key Storage

U18 ATECC608A-MAHDA (SDA, SCL, VCC, GND, EP)
I2C1_SDA, I2C1_SCL
R151 0, R153 0, C165 .1uF

Sheet: /Reset/		
File: reset.kicad_sch		
Title:		
Size: A3	Date:	**Rev:**
KiCad E.D.A. kicad 5.99.0-unknown-85444aadd104ubuntu20.04.1		Id: 6/12

U7D
LS1043ASE7xxx

Section 4/15: Power Pins

GVDD
G1VDD01 J18
G1VDD02 K18
G1VDD03 K20
G1VDD04 L18
G1VDD05 M18
G1VDD06 M20
G1VDD07 N18
G1VDD08 P18
G1VDD09 P20
G1VDD10 R21
G1VDD11 R23
G1VDD12 T22
G1VDD13 T24
G1VDD14 V22
G1VDD15 V24
G1VDD16 Y22
G1VDD17 Y24
G1VDD18 AA20
G1VDD19 AB22
G1VDD20 AB24
G1VDD21 AD22
G1VDD22 AD25
G1VDD23 AE24

GVDD
C169 470uF
C170 C171 C172 C173 C174 C175 C176 C177 C178 22uF

GVDD
C186 C187 C188 C189 C190 C191 C192 C193 C194 C195 C196 C197 .1uF

C198 C199 C200 C201 C202 C203 C204 C205 C206 C207 C208 .1uF

OVDD1 J12
OVDD2 J13
OVDD3 J14
OVDD4 J15
OVDD5 J16
OVDD6 J17
OVDD7 N7
TH_VDD T6
PROG_MTR G14
TA_PROG_SEP F14
DVDD1 K8
DVVD2 L8
EVDD M8
LVDD3 U14
LVDD2 U13
LVDD1 U12

1V8
C178 C179 C180 C181 C182 C183 C184 C185 .1uF 22uF

C209 C210 .1uF 22uF

R156 330 C211 .1uF
R158 330 C214 .1uF
JP1
JP2
C212 .1uF C213 10uF R157 330

3V3
C215 C216 C217 .1uF 22uF

EVDD
FB7 120 OHM 3A
C218 C219 .1uF 22uF

FB8 120 OHM 3A
C236 C237 C238 C239 .1uF 1500pF C240 22uF

U7C
LS1043ASE7xxx

Section 3/15 Power Pins

CPU_VDD
(1V/0.9V)
VDD01 K13
VDD02 K15
VDD03 K17
VDD04 L10
VDD05 L12
VDD06 L14
VDD07 L16
VDD08 M9
VDD09 M11
VDD10 M13
VDD11 M15
VDD12 M17
VDD13 N8
VDD14 N10
VDD15 N12
VDD16 N14

(1V/0.9V)
VDD33 U10
VDD32 U8
VDD31 T13
VDD30 T11
VDD29 T9
VDD28 R18
VDD27 R16
VDD26 R14
VDD25 R12
VDD24 R10
VDD23 R8
VDD22 P17
VDD21 P15
VDD20 P13
VDD19 P11
VDD17 N16
VDD18 P9

CPU_VDD
C227 470uF
C228 C229 C230 C231 C232 C233 C234 C235 22uF

C248 C249 C250 C251 C252 C253 C254 C255 .1uF 1500pF

C270 C271 C272 C273 C274 C275 C276 C277 .1uF 1500pF

C220 C221 C222 C223 C224 C225 C226 1500pF .1uF

C241 C242 C243 C244 C245 C246 C247 1500pF .1uF

C256 C257 C258 C259 C260 C261 C262 C263 C264 C265 C266 C267 C268 C269 1500pF .1uF

JP3
R159 330 C278 .1uF C279 .1uF

(Pull-down/1V/0.9V)
FA_VL R7

S1VDD
S1VDD1 T15
S1VDD2 T16
S1VDD3 T17
S1VDD4 T18
S1VDD5 W14

USB_SVDD1 J10
USB_SVDD2 J11

USB_SDVDD1 K9
USB_SDVDD2 K11

TA_BB_VDD H20

VDD_LP
C315 22uF C316 .1uF R162 330

1V8
R163 5.1 C321 10uF C322 1uF
R164 5.1 C323 10uF C324 1uF
R165 5.1 C327 10uF C328 1uF
R168 5.1 C329 10uF C330 1uF

(1.8V)
AVDD_CGA1 G12
AVDD_CGA2 G11
AVDD_PLAT G13
AVDD_D1 T19

FB9 120 OHM 3A
FB11 120 OHM 3A
C280 C281 C282 C283 C284 C285 C286 C287 C288 2.2uF 3000pF 1500pF .1uF

FB13 120 OHM 3A
FB14 120 OHM 3A
C298 C299 C300 C301 2.2uF 3000pF .1uF

FB15 120 OHM 3A
FB16 120 OHM 3A
C305 C306 C307 C308 2.2uF 3000pF .1uF

X1VDD
C289 .1uF C295 .1uF
C290 C296 .1uF
C291 C297 3000pF
C292 1500pF
C293 2.2uF
FB10 120 OHM 3A
FB12 120 OHM 3A
PWR_X1VDD
C294 C302 2.2uF
C303 22uF
C304 22uF

(1.35V)
X1VDD1 W15
X1VDD2 W16
X1VDD3 W17
X1VDD4 W18
X1VDD5 Y19

C309 3000pF C310 4.7uF C311 .47uF
R160 0R33
X1VDD
R161 0R33
C312 3000pF C313 4.7uF C314 .47uF

AVDD_SD1_PLL1 W12
AVDD_SD1_PLL2 V19

(3.3V)
USB_HVDD2 J9
USB_HVDD1 J8

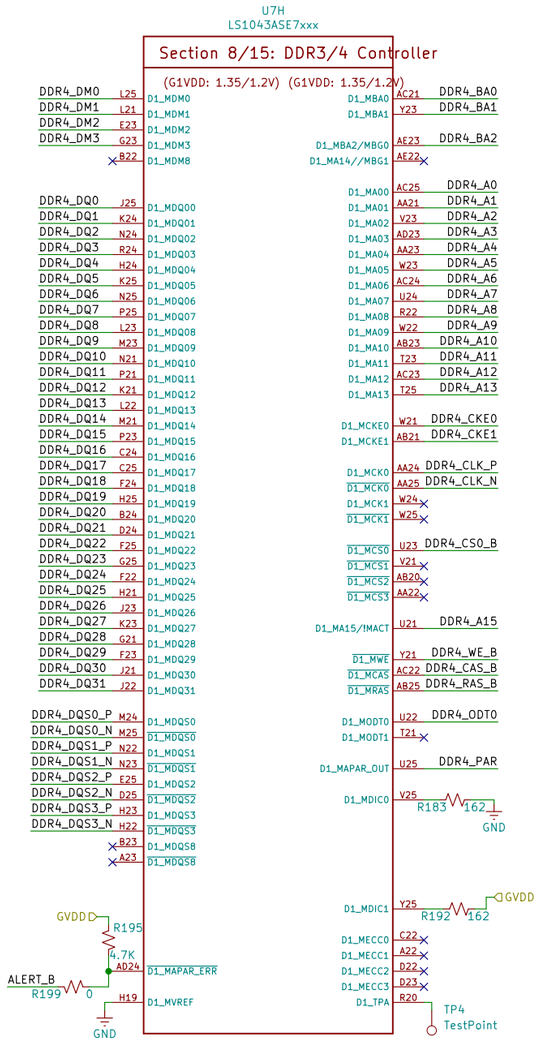
C317 .1uF C318 .1uF C319 3000pF C320 2.2uF
FB17 120 OHM 3A
FB18 120 OHM 3A
3V3

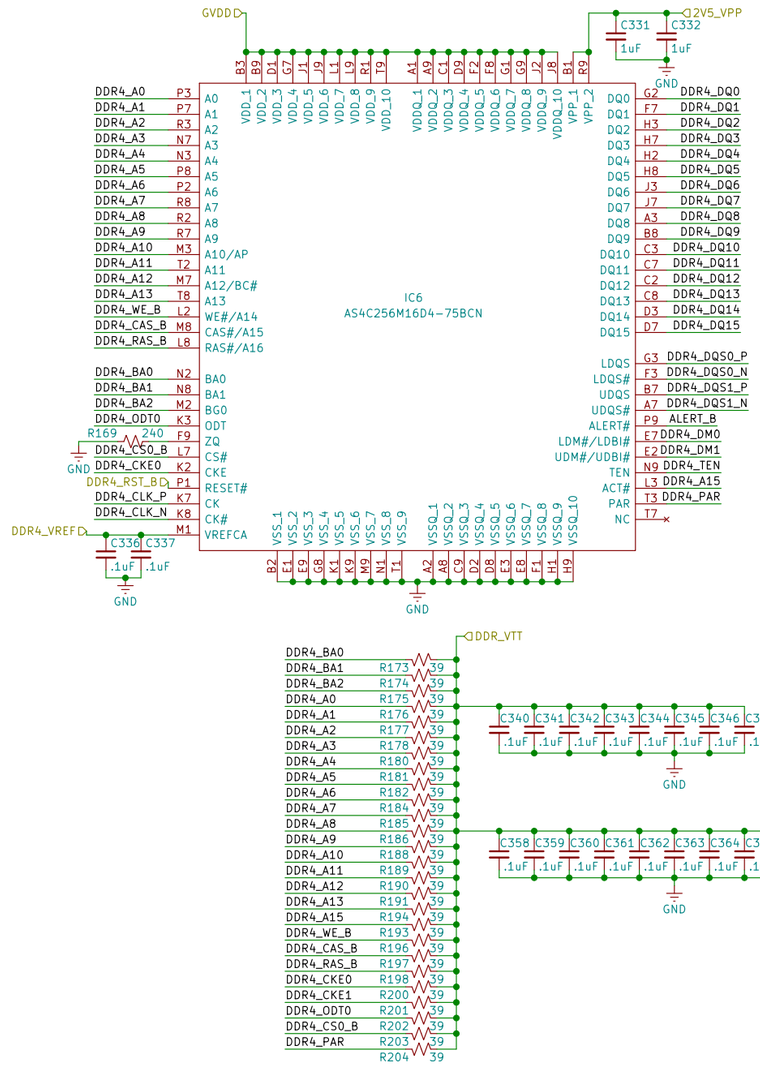
(2.5V/1.8V/1.2V)
TVDD V6
C325 .1uF C326 22uF
FB19 120 OHM 3A
1V8

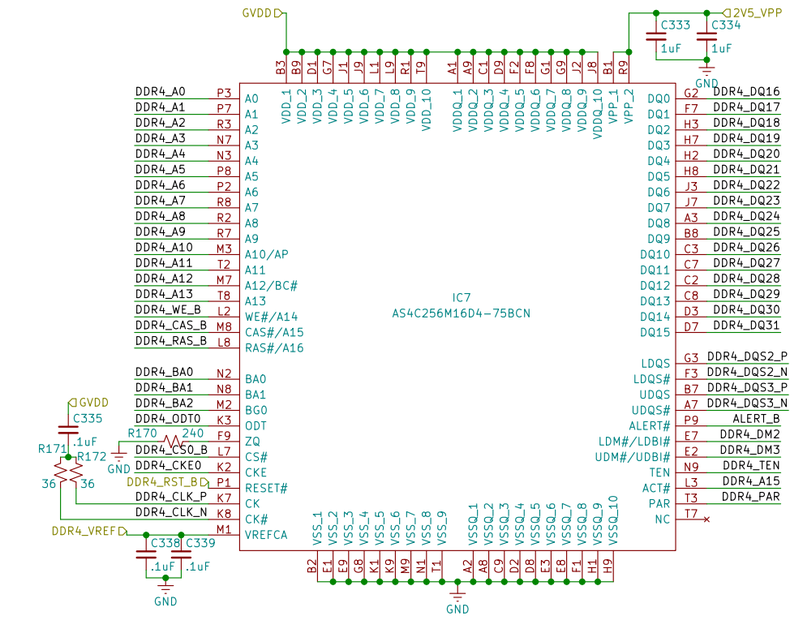
(Power Sense Pins)
SENSEVDD V7
SENSEGND V7

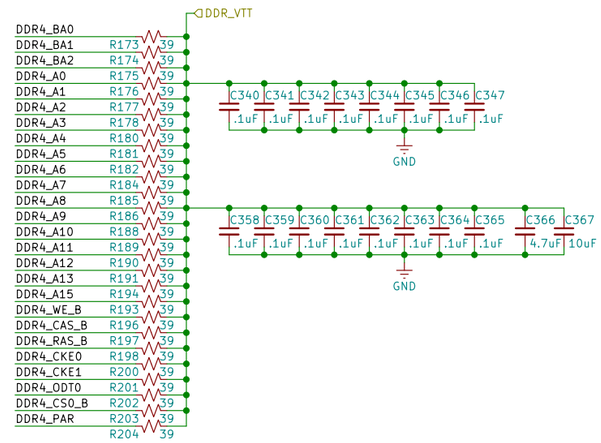
CPU_GND_SENSE
CPU_VDD_SENSE
R166 10
R167 10

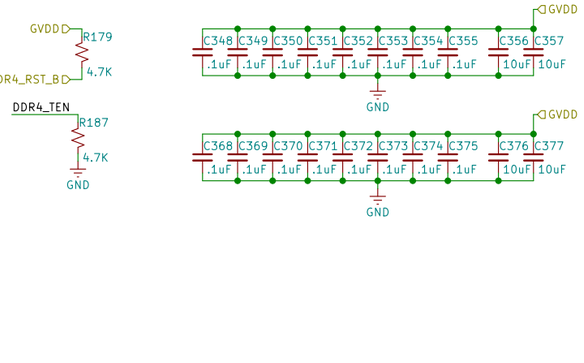
Sheet: /CPU Power/		
File: CPU_power.kicad_sch		
Title:		
Size: A3	Date:	**Rev:**
KiCad E.D.A. kicad 5.99.0-unknown-85444aadd104ubuntu20.04.1		Id: 7/12











IS ImageStream Internet Solutions, Inc.	
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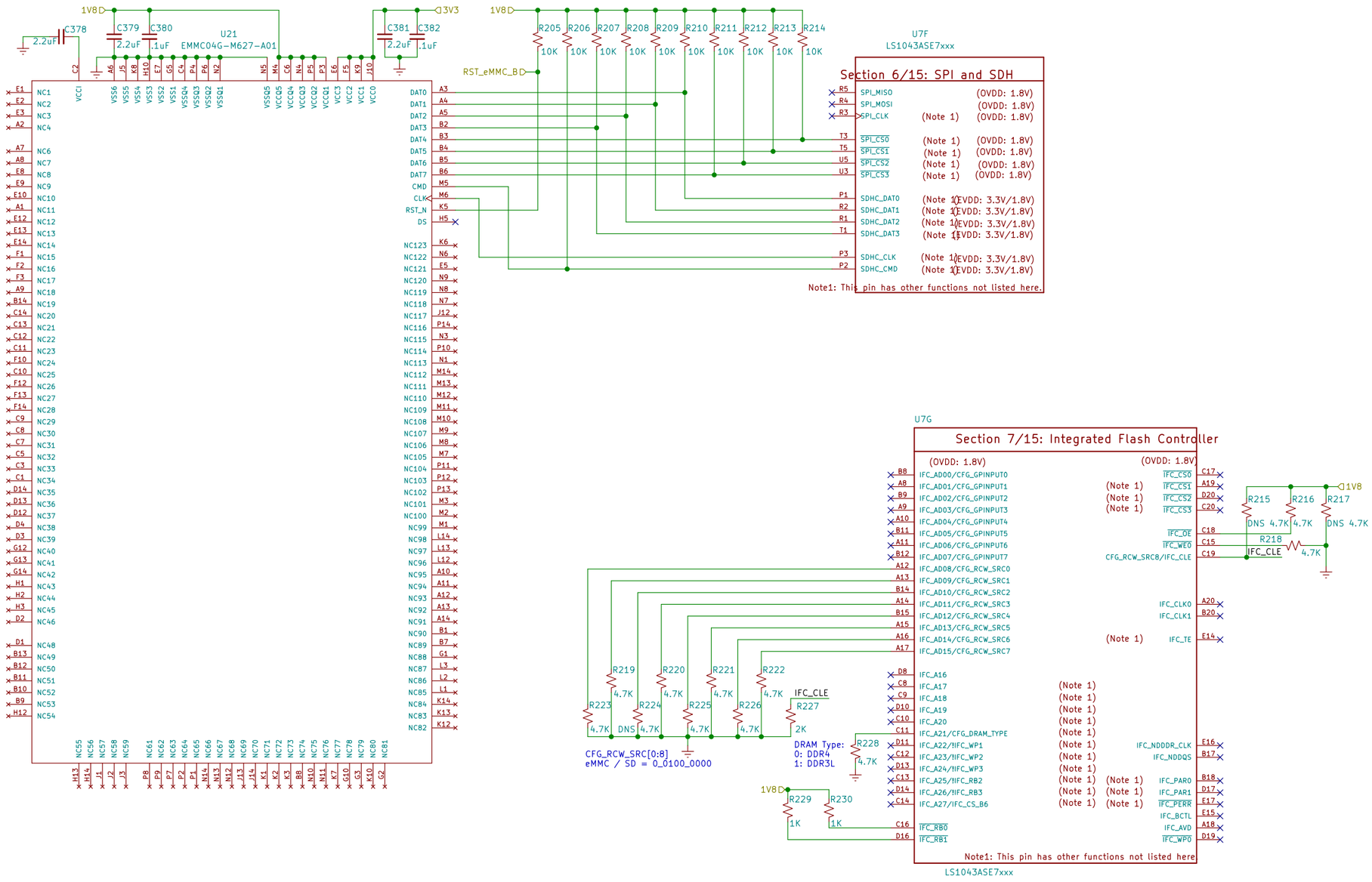
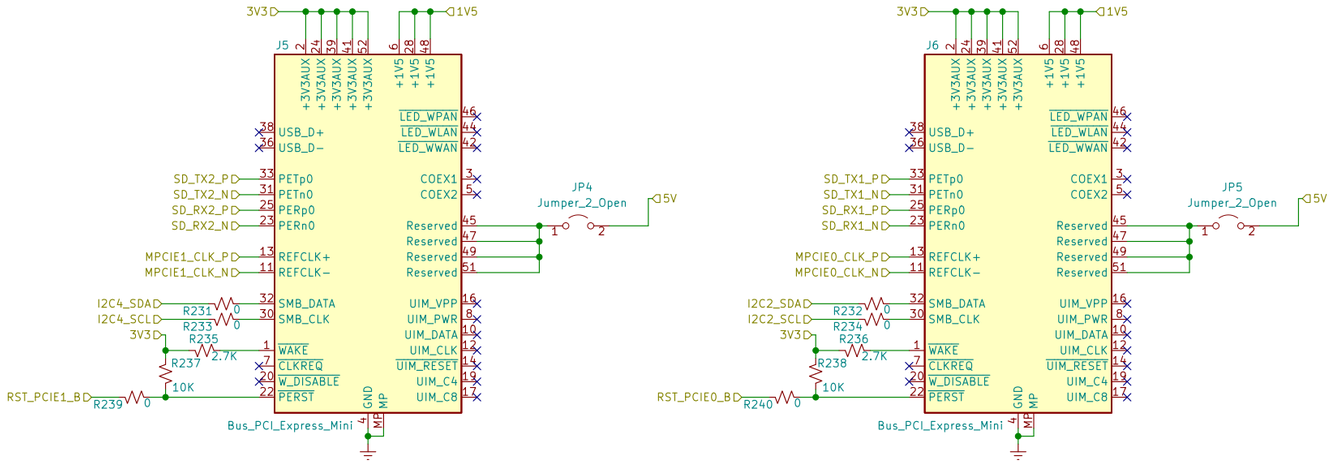
Title: AP2100	
Size: A3	Date:
KiCad E.D.A. kicad 5.99.0-unknown-85444aadd104ubuntu20.04.1	Id: 8/12



Sheet: /Flash / eMMC/	
File: eMMC.kicad_sch	
Title:	
Size: A3	Date:
KiCad E.D.A. kicad 5.99.0-unknown-85444aadd104ubuntu20.04.1	**Rev:** Id: 9/12

Mini PCI Express



IS ImageStream Internet Solutions, Inc.

Sheet: /mPCIe/
File: PCIe.kicad_sch

Title: AP2100

Size: A3	Date:	Rev:
KiCad E.D.A. kicad 5.99.0-unknown-85444aadd104ubuntu20.04.1		Id: 10/12

Ethernet 0 (PoE In)

U7L

Section 12/15: Ethernet Controller 1

(LVDD: 2.5V/1.8V)

EC1_RXD0 W2
EC1_RXD1 W1
EC1_RXD2 V1
EC1_RXD3 U2
EC1_RX_DV Y1
EC1_RX_CLK U1
EC1_TXD0 Y3
EC1_TXD1 W3
EC1_TXD2 V4
EC1_TXD3 V3
EC1_TX_EN Y4
EC1_GTX_CLK U4
EMI1_MDIO W5
EMI1_MDC Y5
EC1_GTX_CLK125 AA3 → 125M_CLK

Note1: All pin have other functions not listed here.

LS1043ASE7xxx

3V3 → NTA4151P Q6 (1,2,3) R241 0 → 1

IC8 KSZ9131RNXC

AVDDL_1 4, AVDDL_2 9, AVDDL_PLL 44, DVDDL_1 18, DVDDL_2 23, DVDDL_3 26, DVDDL_4 30, DVDDL_5 39, LDO_O 43
AVDDH_1 1, AVDDH_2 12, DVDDH_1 16, DVDDH_2 34, DVDDH_3 40 ← 3V3

RXD0/MODE0 32 ← R242 22 ← P0_RXD0
RXD1/MODE1 31 ← R243 22 ← P0_RXD1
RXD2/MODE2 28 ← R244 22 ← P0_RXD2
RXD3/MODE3 27 ← R245 22 ← P0_RXD3
TXD0 19
TXD1 20
TXD2 21
TXD3 22
RX_CTL/CLK125_EN 33 ← R246 22 ← P0_RX_CTL/CLK125_EN
TX_CTL 25
MDIO 37
MDC 36
INT_N/PME_N2/ALLPHYAD 38 ← 3V3 R249 1K
RXC/PHYAD2 35 ← R250 22 ← P0_RX_CLK ← SOC_IRQ0
TXC 24
XO 45
XI 46 ← 25MHZ_EC1_RGMII
NC_1 13, NC_2 14, NC_3 47 (X)
VSS 29, P_VSS 49, ISET 48 → R261 6.04K

TXRXP_A 2 → ETH0_TD1_P
TXRXM_A 3 → ETH0_TD1_N
TXRXP_B 5 → ETH0_TD2_P
TXRXM_B 6 → ETH0_TD2_N
TXRXP_C 7 → ETH0_TD3_P
TXRXM_C 8 → ETH0_TD3_N
TXRXP_D 10 → ETH0_TD4_P
TXRXM_D 11 → ETH0_TD4_N

LED1/PME_N1//PHYAD0 17 → P0_LED1 → 3V3 → ETH0_LED1+ → R247 220 → ETH0_LED1−
LED2/PHYAD1 15 → P0_LED2 → 3V3 → ETH0_LED2+ → R248 220 → ETH0_LED2−

CLK125_NDO/LED_MODE 41 → R251 10K → 3V3
RESET_N 42 → R252 100K

NOTE: LED Mode is Individual

NOTE: 125MHz Output Clock is Disabled
P0_RX_CTL/CLK125_EN → R253 1K

NOTE: Mode is 1110 = 10/100 F/H, 1000BT Full (RGMII)

3V3 → R254 10K, R255 10K, R256 10K, R257 DNP 10K
P0_RXD3, P0_RXD2, P0_RXD1, P0_RXD0
R262 DNP 1K, R263 DNP 1K, R264 DNP 1K, R265 1K

NOTE: PHY Address is 01h

3V3 → R258 DNP 10K, R259 DNP 10K, R260 10K
P0_RX_CLK, P0_LED2, P0_LED1
R266 1K, R267 1K, R268 DNP 1K

Mode [3:0] Description
1000 10/100 Full and Half, 1000BT Full (RGMII), PME Enable on LED1
1010 10/100 Full and Half, 1000BT Full (RGMII), PME Enable on INT_N
1100 1000BT Full (RGMII)
1110 10/100 Full and Half, 1000BT Full (RGMII)

U7B

Section 2/15 SerDes Ground Pins

SD_GND01 U15, SD_GND02 U16, SD_GND03 U17, SD_GND04 U18, SD_GND05 U19, SD_GND06 W8, SD_GND07 W11, SD_GND08 Y8, SD_GND09 Y9, SD_GND10 Y10, SD_GND11 Y11, SD_GND12 Y12, SD_GND13 Y14, SD_GND14 Y15, SD_GND15 Y16, SD_GND16 Y17, SD_GND17 Y18, SD_GND18 AA13, SD_GND19 AA16
SD_GND20 AA19
SD_GND21 AB13, SD_GND22 AB16, SD_GND23 AB19, SD_GND24 AC13, SD_GND25 AC14, SD_GND26 AC15, SD_GND27 AC16, SD_GND28 AC17, SD_GND29 AC18, SD_GND30 AC19, SD_GND31 AC20, SD_GND32 AD13, SD_GND33 AD16, SD_GND34 AD19, SD_GND35 AD21, SD_GND36 AE13, SD_GND37 AE16, SD_GND38 AE19, SD_GND39 AE21

LS1043ASE7xxx

U7N

Section 14/15: SerDes

SD1_TX0_P AA14, SD1_TX0_N AB14
SD1_RX0_P AE14, SD1_RX0_N AD14
SD1_TX1_P AA15 → SD_TX1_P, SD1_TX1_N AB15 → SD_TX1_N
SD1_RX1_P AE15 → SD_RX1_P, SD1_RX1_N AD15 → SD_RX1_N
SD1_REFCLK1_P W10, SD1_REFCLK1_N W9
SD1_REFCLK2_P AE20, SD1_REFCLK2_N AD20
SD1_TX2_P AA17 → SD_TX2_P, SD1_TX2_N AB17 → SD_TX2_N
SD1_RX2_P AE17 → SD_RX2_P, SD1_RX2_N AD17 → SD_RX2_N
SD1_PLL1_TPA V16, SD1_PLL1_TPD V15
SD1_PLL2_TPA V18, SD1_PLL2_TPD V17
SD1_IMP_CAL_TX W19 ← R270 200 ← X1VDD
SD1_IMP_CAL_RX W13 ← R271 698 ← S1VDD
SD1_TX3_P AA18, SD1_TX3_N AB18
SD1_RX3_P AE18, SD1_RX3_N AD18

LS1043ASE7xxx

Ethernet 1

U7M

Section 13/15: Ethernet Controller 2

(LVDD: 2.5V/1.8V)

EC2_RXD0 AC2
EC2_RXD1 AC1
EC2_RXD2 AB1
EC2_RXD3 AA2
EC2_RX_DV AD2
EC2_RX_CLK AA1
EC2_TXD0 AD3
EC2_TXD1 AC4
EC2_TXD2 AC3
EC2_TXD3 AB3
EC2_TX_EN AE3
EC2_GTX_CLK AA4
TVDD: 1.2V
EMI2_MDIO W6
EMI2_MDC Y6
(LVDD: 2.5V/1.8V)
EC2_GTX_CLK125 AE4 → 125M_CLK

Note1: All pin have other functions not listed here.

LS1043ASE7xxx

3V3 → NTA4151P Q7 (1,2,3) R269 0 → 1

IC9 KSZ9131RNXC

AVDDL_1 4, AVDDL_2 9, AVDDL_PLL 44, DVDDL_1 18, DVDDL_2 23, DVDDL_3 26, DVDDL_4 30, DVDDL_5 39, LDO_O 43
AVDDH_1 1, AVDDH_2 12, DVDDH_1 16, DVDDH_2 34, DVDDH_3 40 ← 3V3

RXD0/MODE0 32 ← R272 22 ← P1_RXD0
RXD1/MODE1 31 ← R273 22 ← P1_RXD1
RXD2/MODE2 28 ← R274 22 ← P1_RXD2
RXD3/MODE3 27 ← R275 22 ← P1_RXD3
TXD0 19
TXD1 20
TXD2 21
TXD3 22
RX_CTL/CLK125_EN 33 ← R276 22 ← P1_RX_CTL/CLK125_EN
TX_CTL 25
MDIO 37
MDC 36
INT_N/PME_N2/ALLPHYAD 38 ← 3V3 R279 1K
RXC/PHYAD2 35 ← R280 22 ← P1_RX_CLK ← SOC_IRQ1
TXC 24
XO 45
XI 46 ← 25MHZ_EC2_RGMII
NC_1 13, NC_2 14, NC_3 47 (X)
VSS 29, P_VSS 49, ISET 48 → R284 6.04K

TXRXP_A 2 → C383 1uF → J7 TRD1+ 11, TRD1− 10, TRCT1 12
TXRXM_A 3
TXRXP_B 5 → C384 1uF → J7 TRD2+ 4, TRD2− 5, TRCT2 6
TXRXM_B 6
TXRXP_C 7 → C385 1uF → J7 TRD3+ 3, TRD3− 2, TRCT3 1
TXRXM_C 8
TXRXP_D 10 → C386 1uF → J7 TRD4+ 8, TRD4− 9, TRCT4 7
TXRXM_D 11

LED1/PME_N1//PHYAD0 17 → P1_LED1 → 3V3 → YELLOW
LED2/PHYAD1 15 → P1_LED2 → R277 220 → R278 220 → GRN, ORG

CLK125_NDO/LED_MODE 41 → R281 10K → 3V3
RESET_N 42 → R282 100K

NOTE: LED Mode is Individual

NOTE: 125MHz Output Clock is Disabled
P1_RX_CTL/CLK125_EN → R283 1K

NOTE: Mode is 1110 = 10/100 F/H, 1000BT Full (RGMII)

3V3 → R285 10K, R286 10K, R287 10K, R288 DNP 10K
P1_RXD3, P1_RXD2, P1_RXD1, P1_RXD0
R292 DNP 1K, R293 DNP 1K, R294 DNP 1K, R295 1K

NOTE: PHY Address is 02h

3V3 → R289 DNP 10K, R290 10K, R291 DNP 10K
P1_RX_CLK, P1_LED2, P1_LED1
R296 1K, R297 DNP 1K, R298 1K

Mode [3:0] Description
1000 10/100 Full and Half, 1000BT Full (RGMII), PME Enable on LED1
1010 10/100 Full and Half, 1000BT Full (RGMII), PME Enable on INT_N
1100 1000BT Full (RGMII)
1110 10/100 Full and Half, 1000BT Full (RGMII)

J7 L836-1J1T-43

TRD1+ 11, TRD1− 10, TRCT1 12
TRD2+ 4, TRD2− 5, TRCT2 6
TRD3+ 3, TRD3− 2, TRCT3 1
TRD4+ 8, TRD4− 9, TRCT4 7
3V3 → YELLOW 14, 13
GRN, ORG 16, 17
15
SHIELD 52

IS ImageStream Internet Solutions, Inc.

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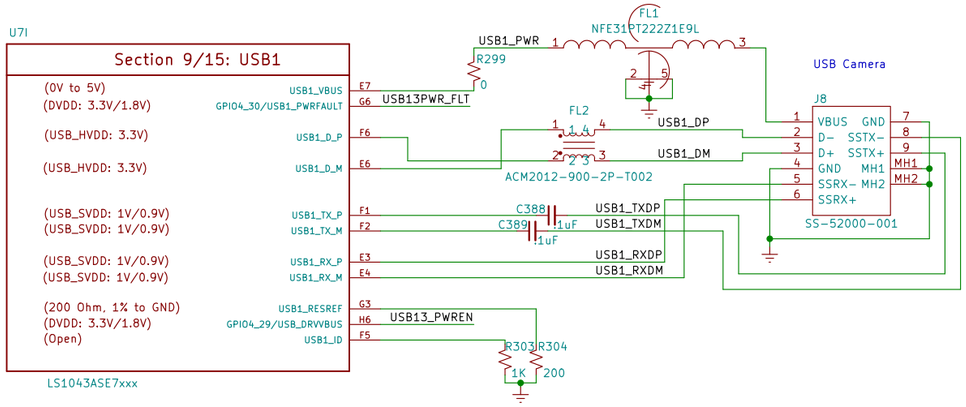

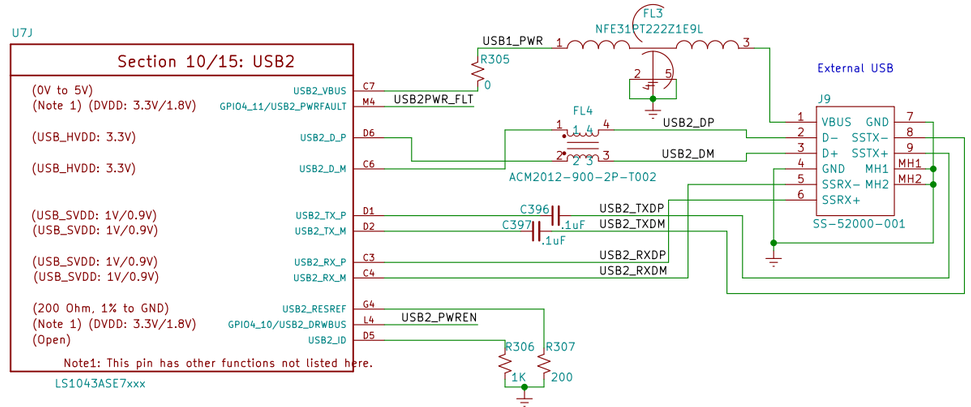

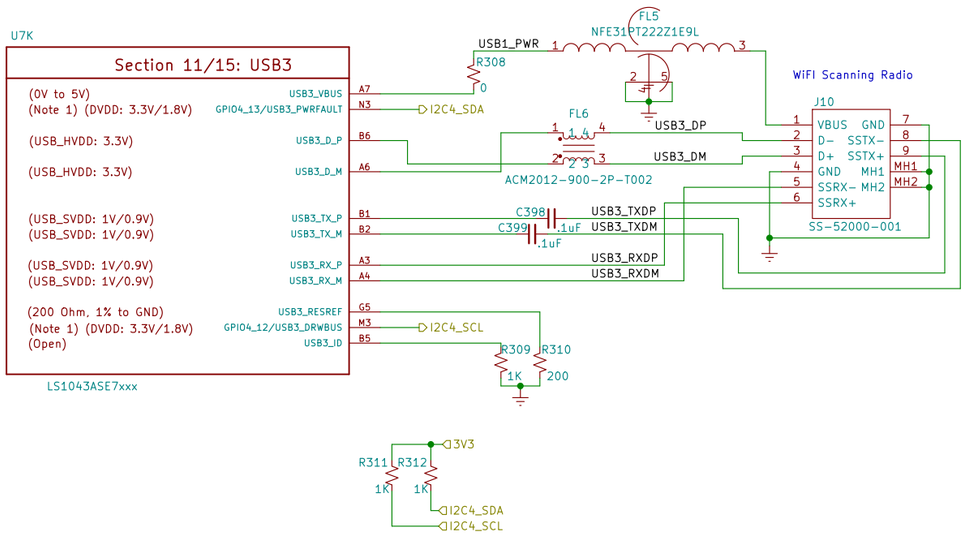

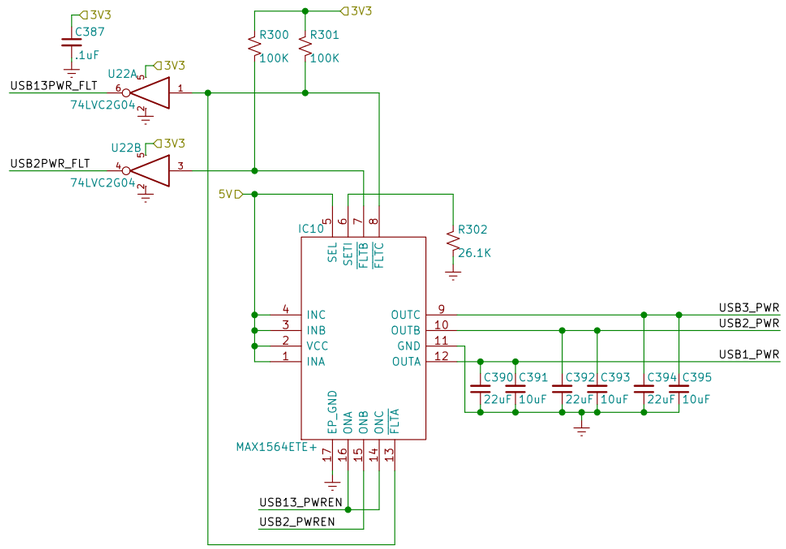

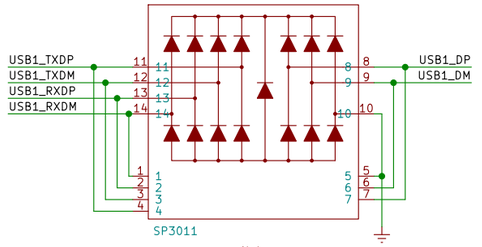

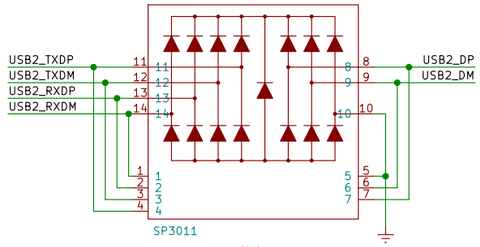

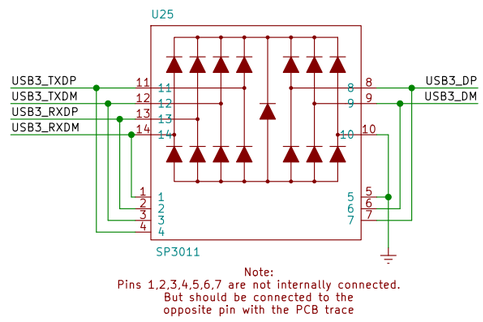

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