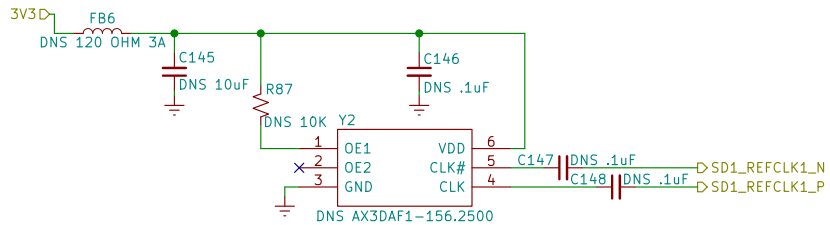


Note: I2C Address: 0xD2

SERDES 10G Clock



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Sheet: /Clocks/
File: clock.kicad_sch

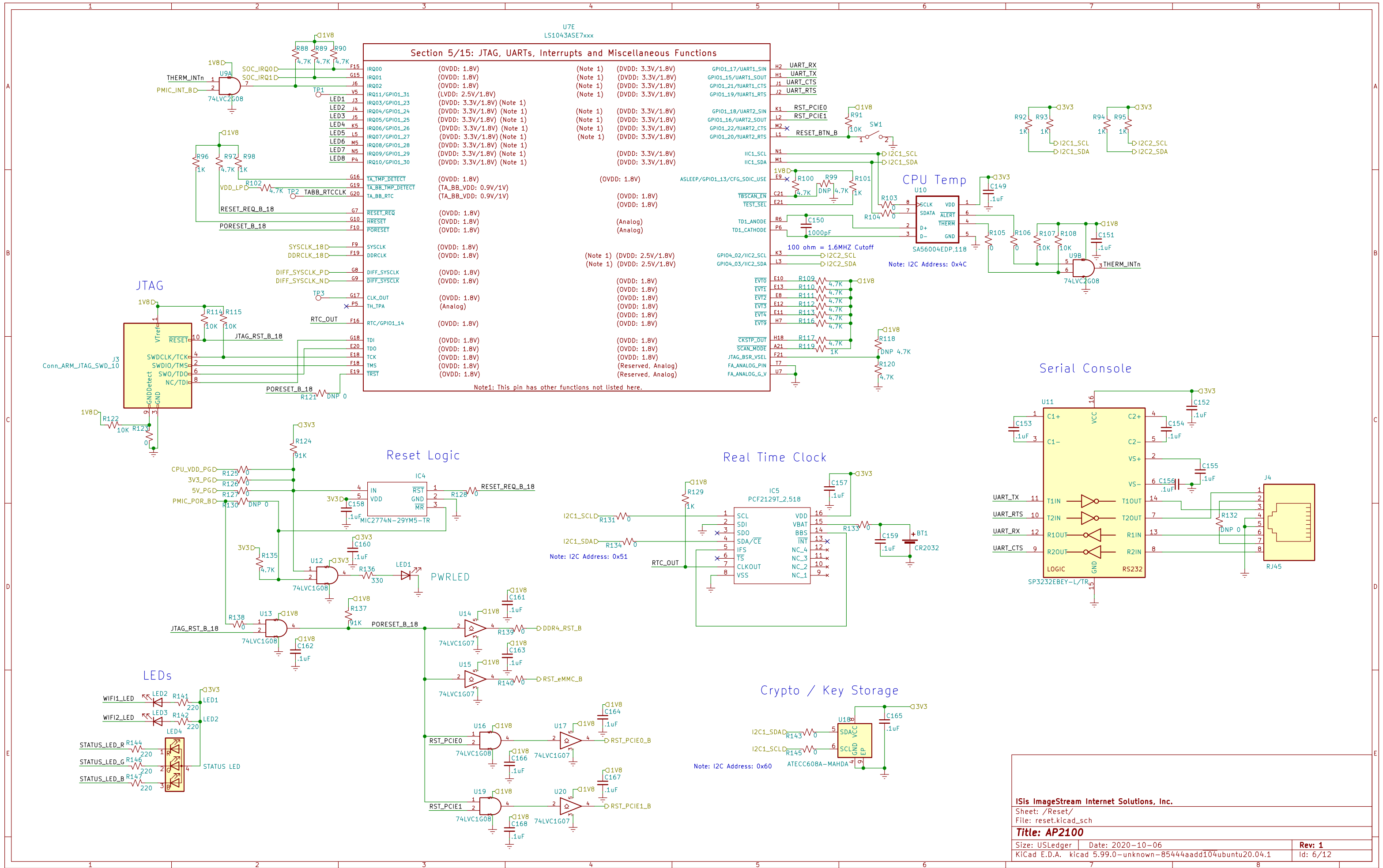
Title: AP2100

Size: USLedger Date: 2020-10-06

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Sheet: /Reset/
File: reset.kicad_sch

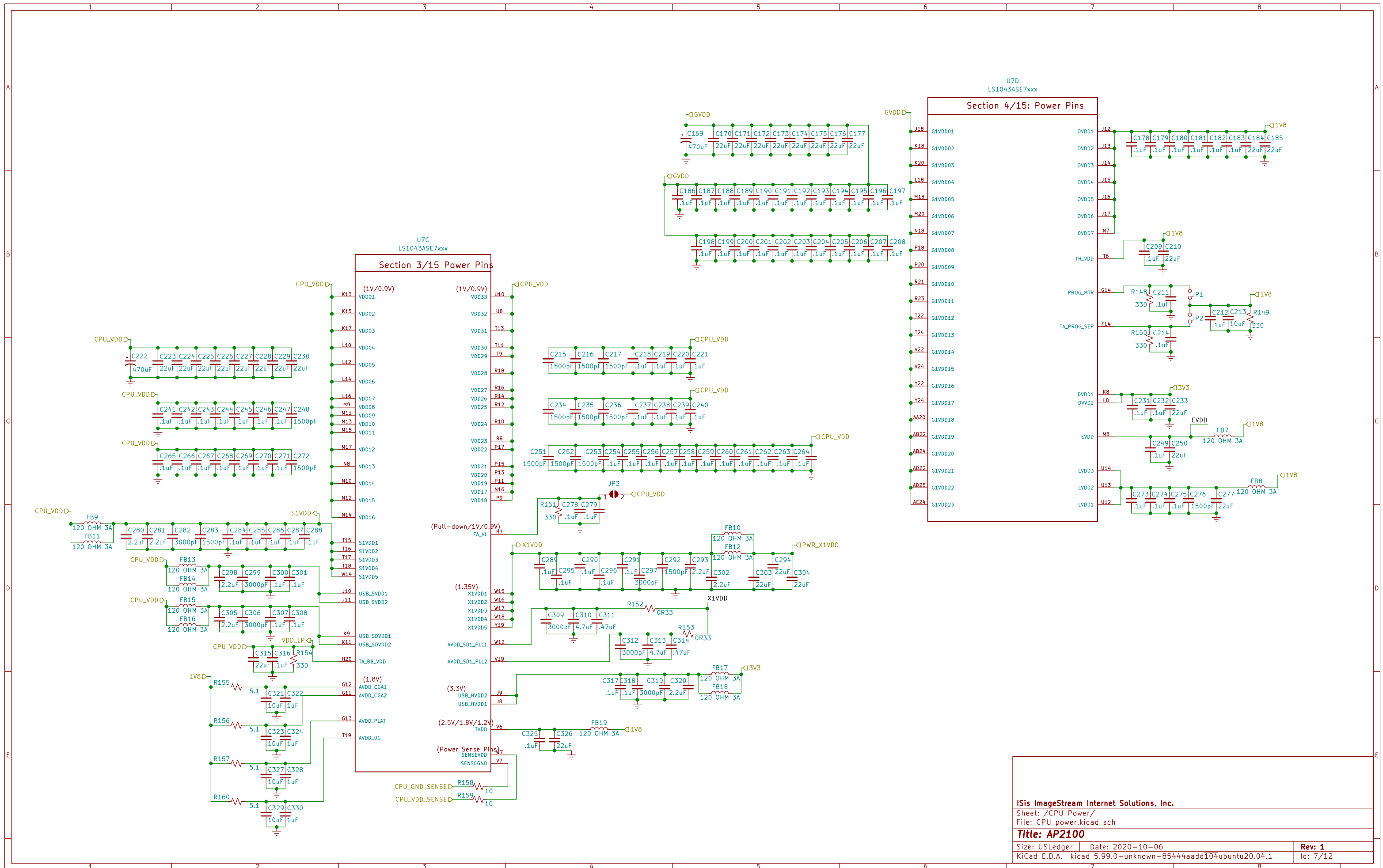
Title: AP2100

Size: USLedger Date: 2020-10-06

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Rev: 1

Id: 6/12



The diagram illustrates the electrical connections for 1GB DDR4 Memory. It features three main sections:

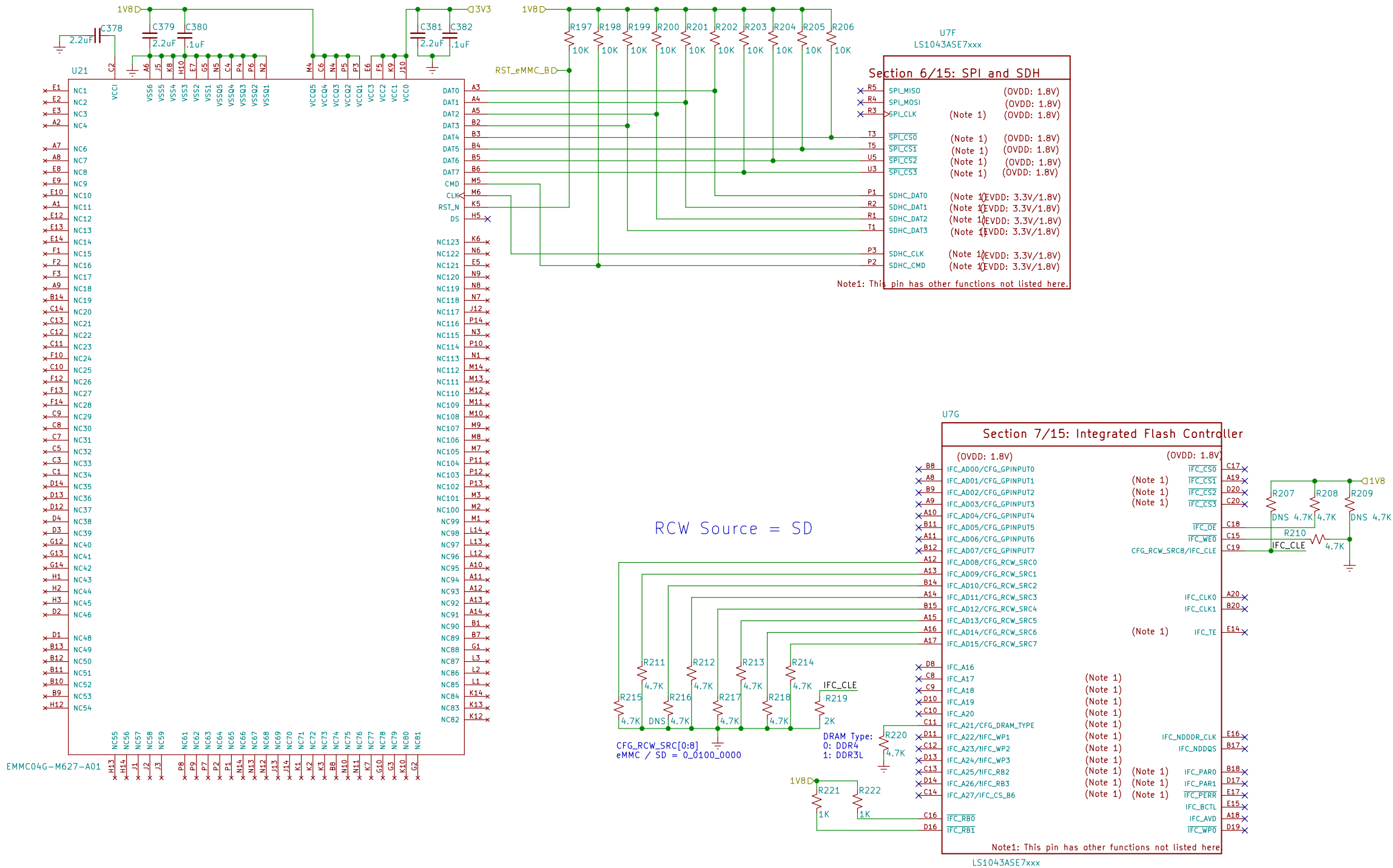
- 1GB DDR4 Memory:** This section shows the memory module's internal components, including the controller (Section 8/15: DDR3/4 Controller) and various signal lines (e.g., DDR4_DM0, DDR4_DM1, DDR4_DM2, DDR4_DM3, DDR4_DQ0, DDR4_DQ1, etc.).
- DDR4 256Mx16:** This section shows the memory module's internal components, including the controller (Section 8/15: DDR3/4 Controller) and various signal lines (e.g., DDR4_A0, DDR4_A1, DDR4_A2, DDR4_A3, etc.).
- DDR4 256Mx16:** This section shows the memory module's internal components, including the controller (Section 8/15: DDR3/4 Controller) and various signal lines (e.g., DDR4_A0, DDR4_A1, DDR4_A2, DDR4_A3, etc.).

The diagram includes various components like resistors, capacitors, and connectors, with labels for signals and components. The components are connected to the memory module's internal components, including the controller (Section 8/15: DDR3/4 Controller) and various signal lines (e.g., DDR4_DM0, DDR4_DM1, DDR4_DM2, DDR4_DM3, DDR4_DQ0, DDR4_DQ1, etc.).

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 Sheet: /DDR4 Memory/
 File: CPU_memory.kicad_sch
Title: AP2100
 Size: USLedger | Date: 2020-10-06 | Rev: 1
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Sheet: /DDR4 Memory/
 File: CPU_memory.kicad_sch
Title: AP2100
 Size: USLedge | Date: 2020-10-06 | Rev: **1**
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4GB eMMC Flash



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Sheet: /Flash / eMMC/
File: eMMC.kicad_sch

Title: AP2100

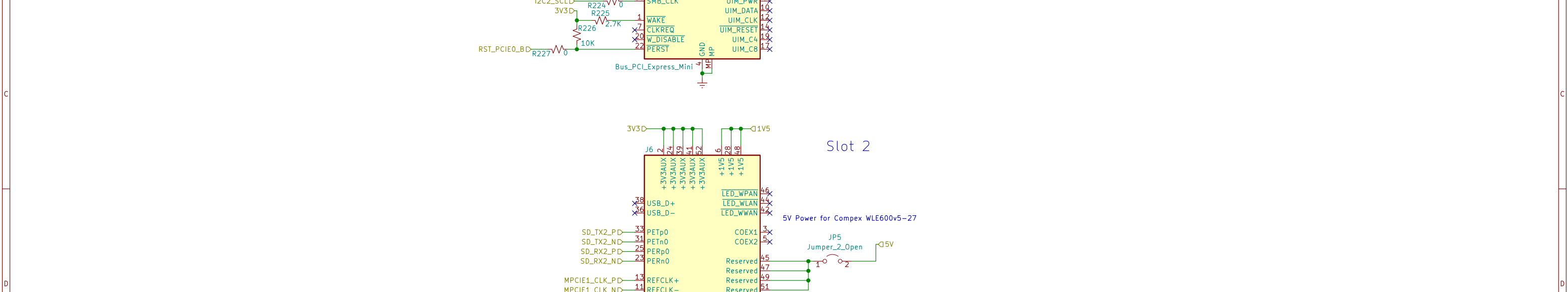
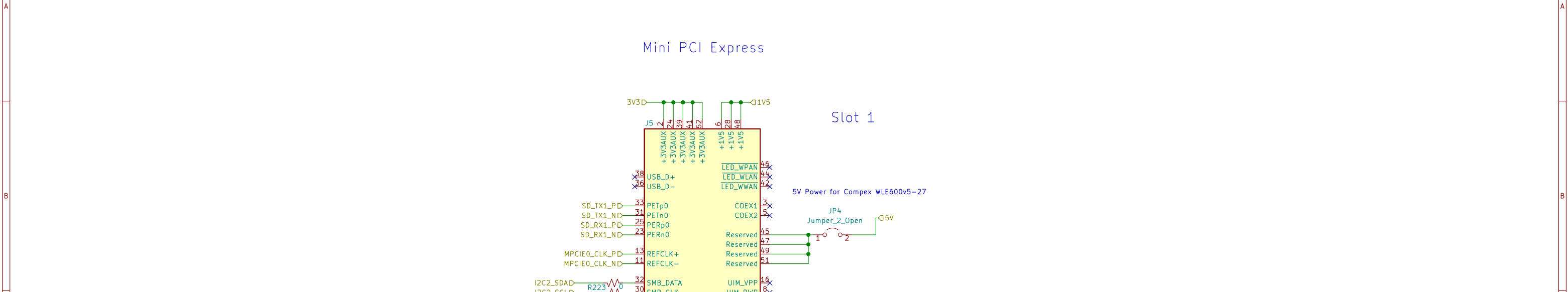
Size: USLedger Date: 2020-10-06

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Rev: 1

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1	2	3	4	5	6	7	8	
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Sheet: /mPCIe/
File: PCIe.kicad_sch

Ethernet 0 (PoE In)

U7L

Section 12/15: Ethernet Controller 1

(LVDD: 2.5V/1.8V)

EC1_RXD0
EC1_RXD1
EC1_RXD2
EC1_RXD3
EC1_RX_DV
EC1_RX_CLK

EC1_TXD0
EC1_TXD1
EC1_TXD2
EC1_TXD3
EC1_TX_EN
EC1_TX_CLK

EMI1_MDIO
EMI1_MDC

EC1_GTX_CLK125

Note1: All pin have other functions not listed here.
LS1043ASE7xxx

3V3D
NTA4151P
Q6

R233 0

1

3

4

9

44

18

23

26

30

39

43

1

12

16

34

40

3V3

IC8

AVDDL1

AVDDL2

AVDDL_P1L

DVDDL1

DVDDL2

DVDDL3

DVDDL4

DVDDL5

LDO_0

AVDDH1

AVDDH2

DVDDH1

DVDDH2

DVDDH3

AVDDH_P1

AVDDH_P2

AVDDH_P3

AVDDH_P4

AVDDH_P5

AVDDH_P6

AVDDH_P7

AVDDH_P8

AVDDH_P9

AVDDH_P10

AVDDH_P11

AVDDH_P12

AVDDH_P13

AVDDH_P14

AVDDH_P15

AVDDH_P16

AVDDH_P17

AVDDH_P18

AVDDH_P19

AVDDH_P20

AVDDH_P21

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AVDDH_P47

AVDDH_P48

AVDDH_P49

AVDDH_P50

AVDDH_P51

AVDDH_P52

AVDDH_P53

AVDDH_P54

AVDDH_P55

AVDDH_P56

AVDDH_P57

AVDDH_P58

AVDDH_P59

AVDDH_P60

AVDDH_P61

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AVDDH_P63

AVDDH_P64

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AVDDH_P102

AVDDH_P103

AVDDH_P104

AVDDH_P105

AVDDH_P106

AVDDH_P107

AVDDH_P108

AVDDH_P109

AVDDH_P110

AVDDH_P111

AVDDH_P112

AVDDH_P113

AVDDH_P114

AVDDH_P115

AVDDH_P116

AVDDH_P117

AVDDH_P118

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AVDDH_P191

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AVDDH_P212

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AVDDH_P215

AVDDH_P216

AVDDH_P217

AVDDH_P218

AVDDH_P219

AVDDH_P220

AVDDH_P221

AVDDH_P222

AVDDH_P223

AVDDH_P224

AVDDH_P225

AVDDH_P226

AVDDH_P227

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AVDDH_P229

AVDDH_P230

AVDDH_P231

AVDDH_P232

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AVDDH_P234

AVDDH_P235

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AVDDH_P238

AVDDH_P239

AVDDH_P240

AVDDH_P241

AVDDH_P242

AVDDH_P243

AVDDH_P244

AVDDH_P245

AVDDH_P246

AVDDH_P247

AVDDH_P248

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AVDDH_P250

AVDDH_P251

AVDDH_P252

AVDDH_P253

AVDDH_P254

AVDDH_P255

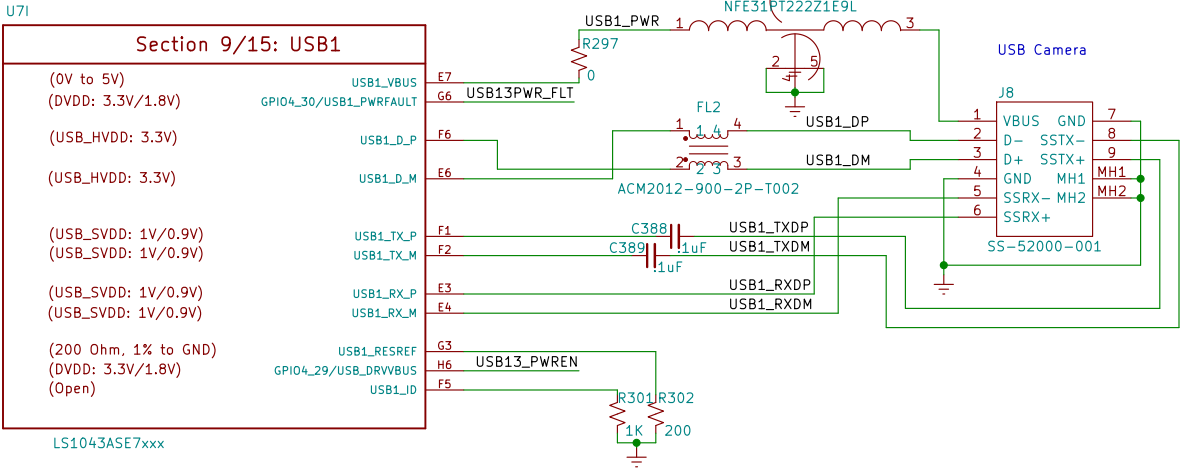
AVDDH_P256

AVDDH_P257

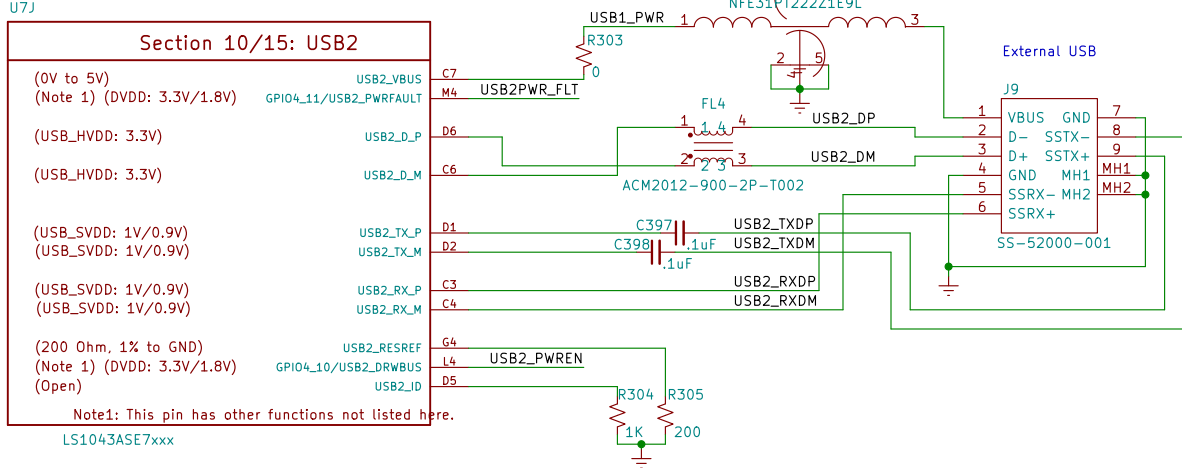
AVDDH_P258

AVDDH_P259

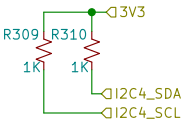
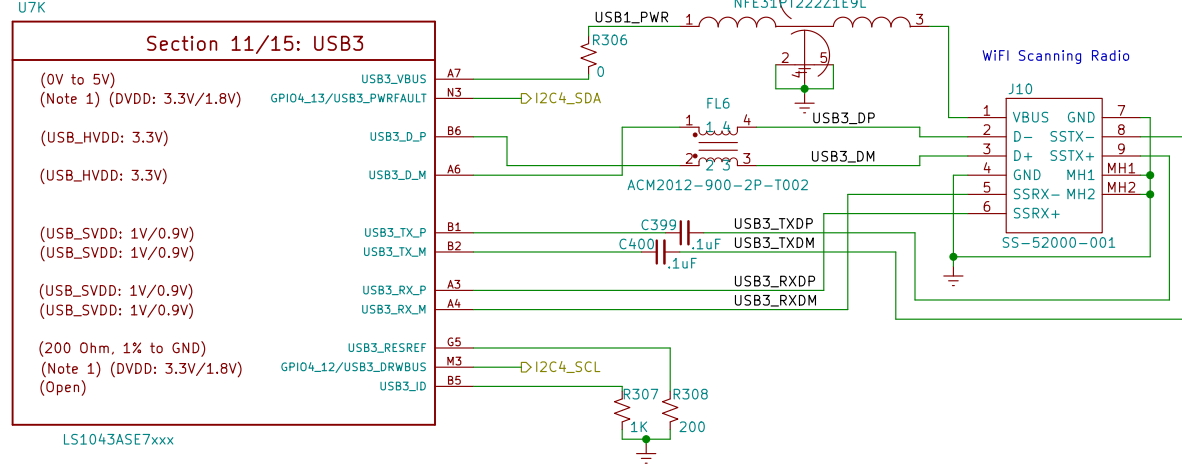
USB3 Port 1



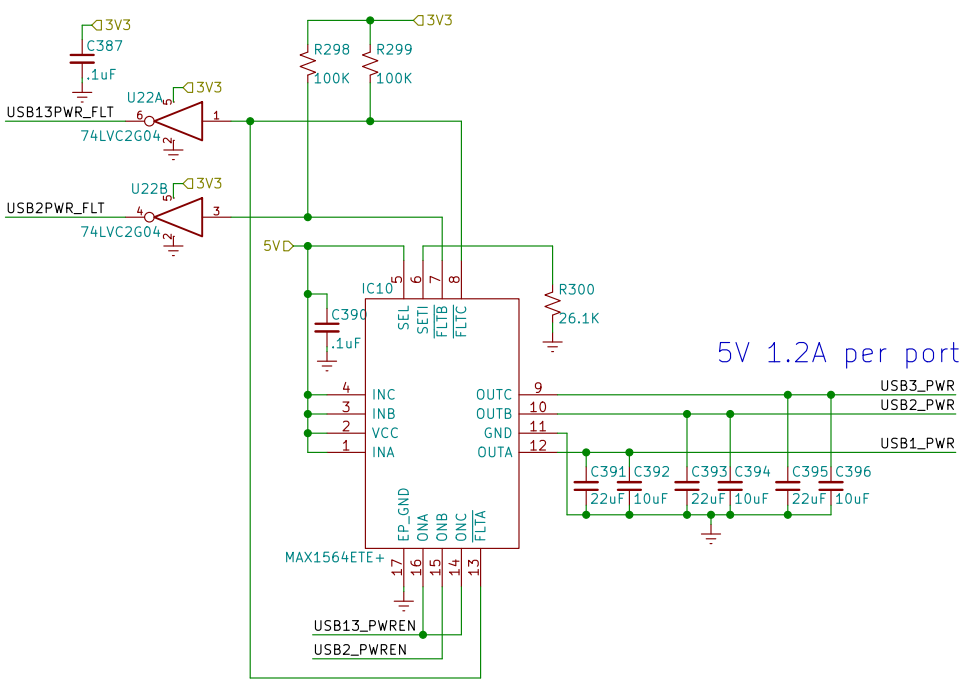
USB3 Port 2



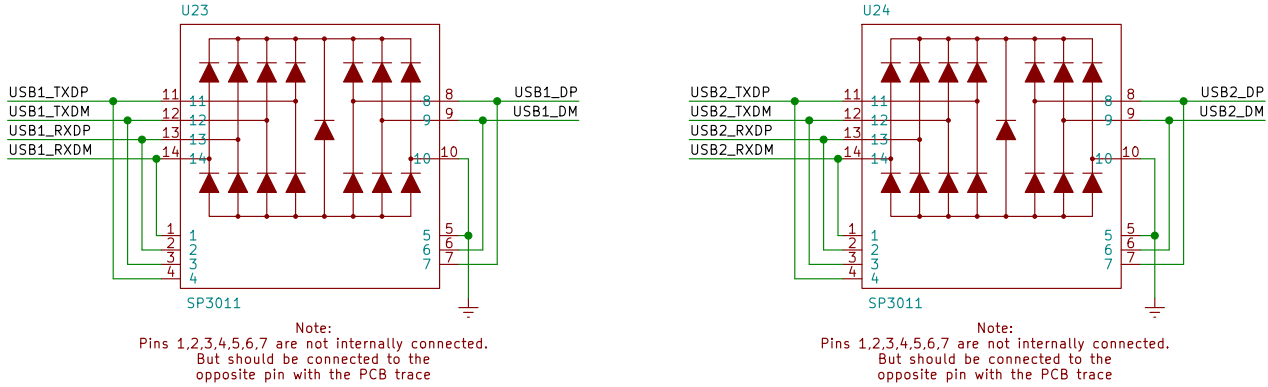
USB3 Port 3



USB Power



ESD Protection



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Sheet: /USB/

File: USB.kicad_sch

Title: AP2100

Size: USLedger

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Rev: 1

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