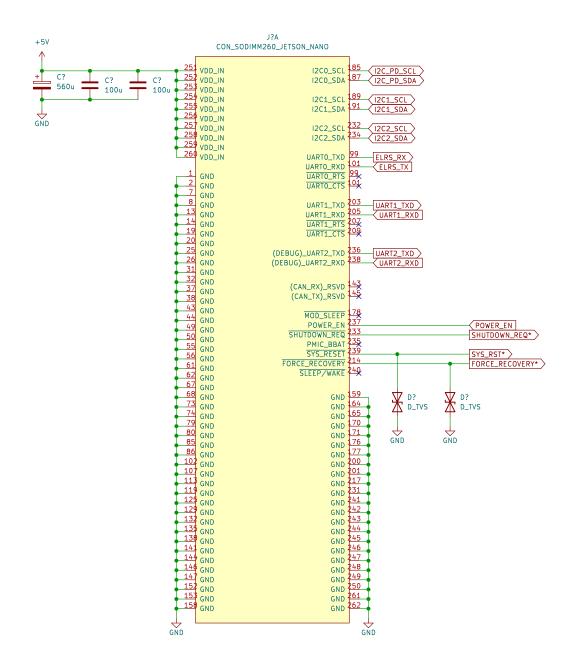
	DC Jack	Micro USB 2.0	Fan & Debug
	File: dc_jack.kicad_sch	File: micro ush kicad sch	File: fan_debug.kicad_sch
	SODIMM Connector 1	Pre-reg 3V3	Microphone
JART, I2C, Power			
	File: sodimm1.kicad_sch	File: pre-reg3V3.kicad_sc	tfile: microphone.kicad_sch
	SODIMM Connector 2	M.2 SSD	CSI
JSB2, I2S, GPIO, SPI, CAM			
	File: sodimm2.kicad_sch	_	File: csi.kicad_sch
	SODIMM Connector 3	M.2 WiFi	IMU
JSB3, PCIE, DP			
	File: sodimm3.kicad_sch	File: m2wifi.kicad_sch	File: imu.kicad_sch
	Pre-reg 5V	USB C	1V8 LDO & 3V3 AO Rails
	File: pre-reg5V.kicad_sch	File: usbC.kicad sch	File: ldoAo.kicad_sch
	GPIO	DP	Power MCU
	File: gpio.kicad_sch	File: dp.kicad_sch	File: power_mcu.kicad_sch



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				1	
(UCD H DD)	111	HCD0 D	CCIO DO	6	CCIO DO D
USB_M_DP	109	USBO_D	CSI0_D0	4	CSIO_DO_P
(USB_M_DN)		USBO_D	CSIO_DO	18	CSIO_DO_N
(USB_C_DP)	117	USB1 D	CSIO_D1	16	CSIO_D1_P
USB_C_DN	115	USB1_D	CSIO_D1	12	CSIO_D1_N >
<u> </u>	4.00		CSI0_CLK		CSI0_CLK_P
(USB_AP_P)	123	U3BZ_D	CSI0_CLK	10	CSIO_CLK_N
USB_AP_N	121	USB2_D	CSI1_D0	5.	
	1 93	ISCO DOUT		30	
Luis sp	195	I2SO_DOUT I2SO_DIN	CSI1_D0	17	
MIC_SD	197	1250_DIN	CSI1_D1	15.	
MIC_WS	199	1230_F3	CSI1_D1	11	
< MIC_SCK		I2S0_SCLK	CSI1_CLK(RSVD)	<u></u>	
(I2S1_SDOUT	220	12S1_DOUT	CSI1_CLK(RSVD)	9×	
12S1_SDIN	222	12S1_DIN	CSI2_D0	24	CSI2_DO_P
	224	12S1_DIN	CSI2_D0	22	
I2S1_LRCK	226			36	CSI2_DO_N
(I2S1_SCLK)		I2S1_SCLK	CSI2_D1	34	CSI2_D1_P
	188		CSI2_D1	30	CSI2_D1_N
	10/	001_110_1	CSI2_CLK	28	CSI2_CLK_P
	X3.	GBE_LED_ACT	CSI2_CLK	20	CSI2_CLK_N
	Xo	GBE_MDIO	CSI3_D0	23	
	*84	GBE_MDIO	CSI3_D0	21	
	19 4	GBE_MDI1		35	
	19 9	GBE_MDI1	CSI3_D1	33.	
	198	GBE_MDI2	CSI3_D1	20	
	196	GBE_MDI2	CSI3_CLK	5	
	204	GBE_MDI3	CSI3_CLK	- ×	
	202	GBE_MDI3	CS14_D2	42	
	^	GBE_MDIS	CSI4_D2	4Q	
	219	COUNC DATA		48.	
	221	SDMMC_DATO	CSI4_D0	46	
	227	SDMMC_DAT1	CS14_D0	54	
	221	SDMMC_DAT2	CSI4_CLK	53X	
	22	SDMMC_DAT3	CS14_CLK	50×	
	220	SDMMC_CMD	CSI4_D1	FX.	
	X	SDMMC_CLK	CSI4_D1	58	
				66	
			CSI4_D3	64×	
			CAMO MCIK	116	CHIO HOLK
			CAMO_MCLK	114	CAMO_MCLK
			CAMO_PWDN	22	CAMO_PWDN
USB0_VBUS_DET*	87	GPI000(USB_VBUS_EN0)	CAM1_MCLK	20	CAM1_MCLK
0000_1000_01	118	GPI001(CLK)	CAM1_PWDN	-20	CAM1_PWDN
	124	GPI002	CAM_I2C_SCL	213	CAM_I2C_SCL
	126	GP1003	CAM_I2C_SDA	215	CAM_I2C_SDA
	127	GP1004	CAN_IZC_SDA		CAM_IZC_SDA
	128	GP1004	SPI0_SCK	91	
CAM MIN CEL	130	GF1003	SPI0_MISO	93	
CAM_MUX_SEL	206	GF1000	SPI0_MOSI	<u>89</u>	
[=			SPI0_CS0	<u>95</u>	
FAN_TACH >	211	GPIO08(SDMMC_CD)	SPI0_CS1	97	
	212	GPI009(AUD_MCLK)		106	
		GPI010	SPI1_SCK	106	
	210	GPI011(CLK)	SPI1_MISO	108	
	210	GPI012	SPI1_MOSI	104	
	228		SPI1_CS0	110	
FAN_PWM	230	GPI014(PWM)	SPI1_CS1	112	
		` '			

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73				
×2	DSI_D0	PCIE_WAKE	79	PCIE_WAKE
22 20 84 25 25 26	DSI_D0 DSI_D1	PCIEO_RST	181	PCIEO_RST >
82	DSI_D1			
, 28	DSI_CLK	PCIEO_CLKREQ	180	PCIEO_CLKREQ
, 76	DSI_CLK	PCIEO_CLK	162	PCIEO_CLK_P
	55,25211	PCIEO_CLK	160	PCIEO_CLK_N
41 35 47 45 45 45 46 46 46 46 46 46 46 46 46 46 46 46 46	DPO_TXDO	DOLEO TVO	136	
×39	DPO_TXDO	PCIEO_TX0	134	PCIEO_TXO_P
× 4. /	DPO_TXD1	PCIEO_IXO	.33	PCIEO_TXO_N > PCIEO_RXO_P
X #3	DPO_TXD1	PCIEO_RX0	131	PCIEO_RXO_N
×2.3	DP0_TXD2		142	
×21	DP0_TXD2	FCIEO_IXI	140	PCIEO_TX1_P
.57	DP0_TXD3	PCIEO_TX1	39	PCIEO_TX1_N
		PCIEO_RX1	37	PCIEO_RX1_P
,92 ,90	DP0_AUX			PCIEO_RX1_N
×	DPO_AUX	I CILO_IXZ	150 148	PCIEO_TX2_P
<u>,88</u>	DPO_HPD	PCIE0_TX2	151	PCIEO_TX2_N >
	_	PCIE0_RX2	149	PCIEO_RX2_P
DPI_IXDU_P	DETTIVOO(UDMITIVOS)	PCIEO_RX2		PCIEO_RX2_N
DP1_TXD0_N 71			156	PCIEO_TX3_P
DP1_TXD1_N 69		PCIE0_TX3	.54	PCIEO_TX3_N
DP1_TXD2_P 75	DP1_TXD2(HDMI_TXD0)	PCIE0_RX3	157 155	PCIE0_RX3_P
DP1_TXD2_N 75	DP1_TXD2(HDMI_TXD0)	PCIEO_RX3	رد	PCIEO_RX3_N
DP1 TXD3 P 83	DP1 TXD3(HDML TXC)	USBSS_TX	168	USBSS_TX_P
DP1_TXD3_N 81	DP1_TXD3(HDMI_TXC)	USBSS_TX	166	USBSS_TX_N
DP1_AUX_P 10	DP1_AUX_P(HDMI_DDC_SCL)	USBSS_RX	63	USBSS_RX_P
DP1_AUX_N 98	DP1_AUX_N(HDMI_DDC_SDA)	USBSS_RX	161	USBSS_RX_N
	` '	(PCIE1_RST)RSVD	83	PCIE1_RST >
DP1_HPD 96	טו בבווו ט(ווטויובווו ט)	*	182	
X-	HDMI_CEC	(PCIE1_CLKREQ)RSVD		PCIE1_CLKREQ
		(PCIE1_CLK)RSVD	75	PCIE1_CLK_P
		(PCIE1_CLK)RSVD	1.73	PCIE1_CLK_N
		(PCIE1_TX0)RSVD	74	PCIE1_TX0_P
		(PCIE1_TX0)RSVD	72	PCIE1_TX0_N
		(PCIE1_RX0)RSVD	169	PCIE1_RX0_P
21	CLK_32K_OUT	(PCIE1_TX0)RSVD	167	PCIE1_RX0_N
			ı	

