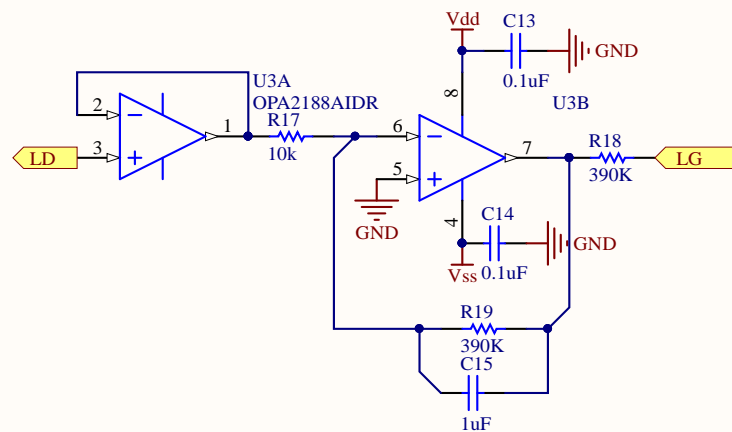
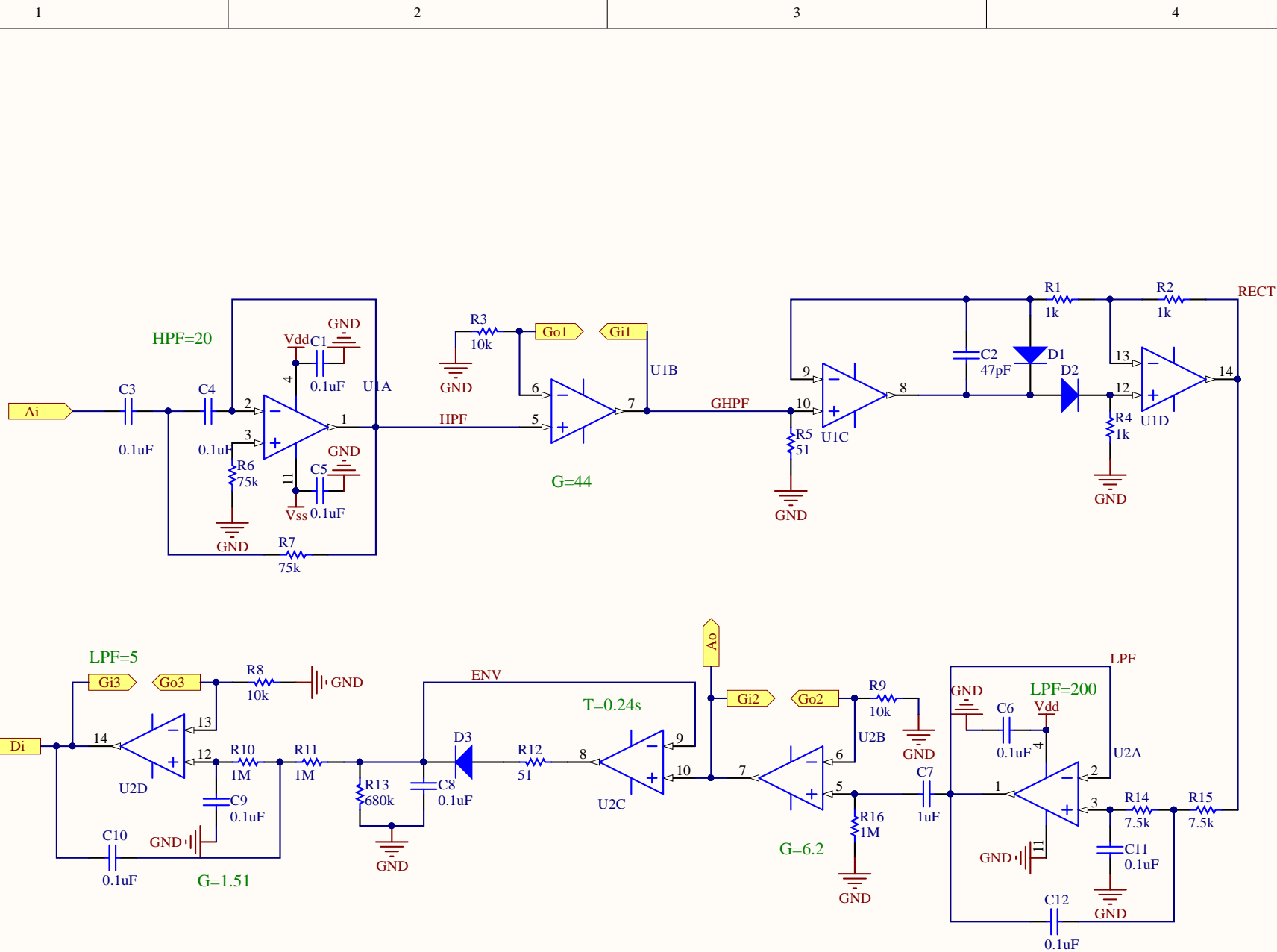


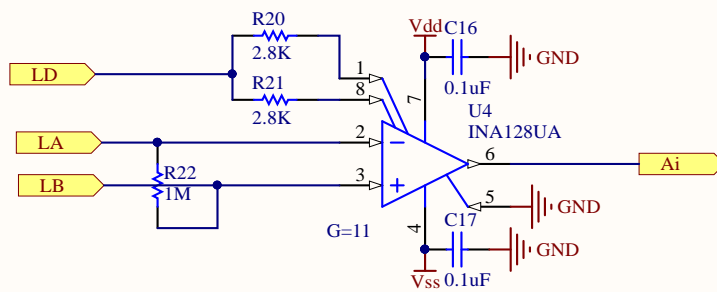
Title		
Size	Number	Revision
A		
Date:	11/3/16	Sheet of
File:	\\.\Digital.SchDoc	Drawn By:



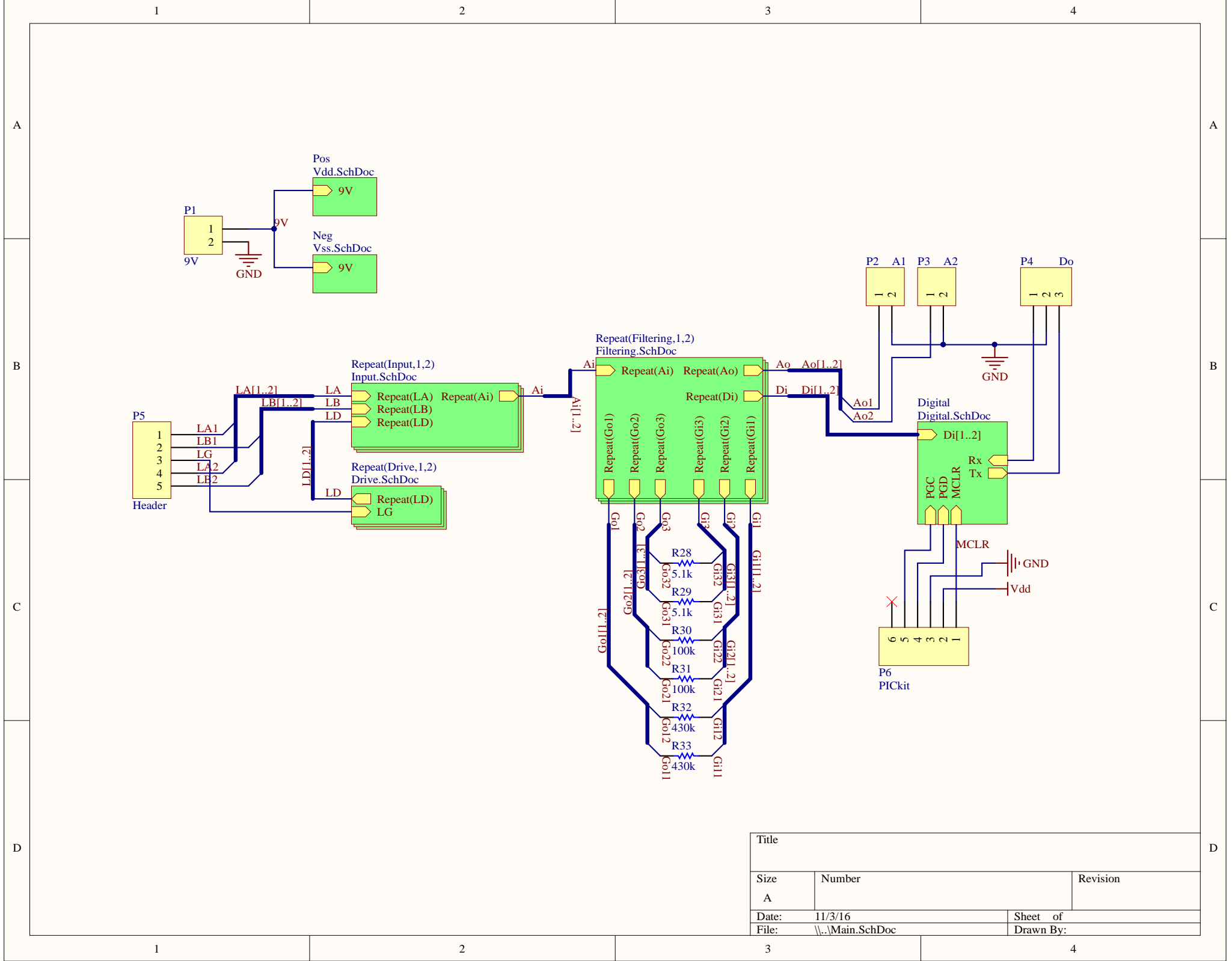
Title		
Size	Number	Revision
A		
Date:	11/3/16	Sheet of
File:	\\..\Drive.SchDoc	Drawn By:



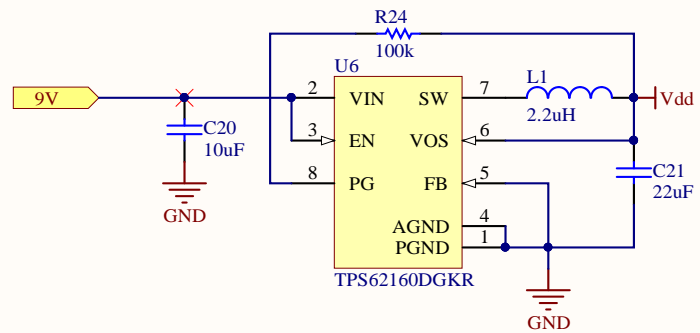
Title		
Size	Number	Revision
A		
Date:	11/3/16	Sheet of
File:	\\..Filtering.SchDoc	Drawn By:



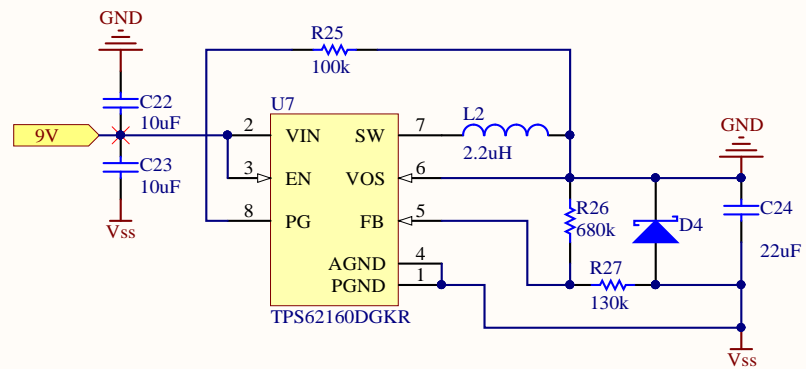
Title		
Size	Number	Revision
A		
Date:	11/3/16	Sheet of
File:	\\..\Input.SchDoc	Drawn By:



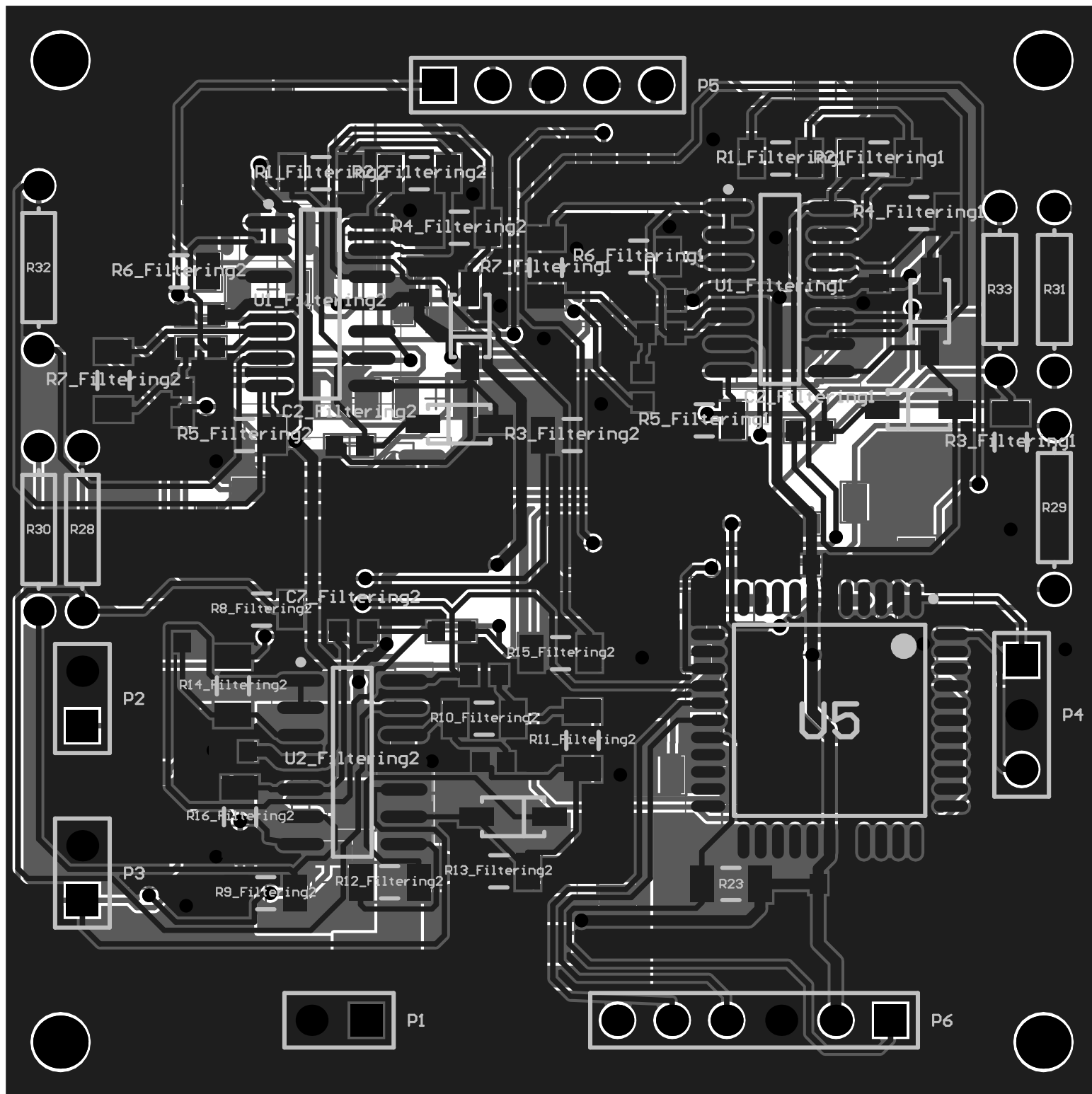
Title		
Size	Number	Revision
A		
Date:	11/3/16	Sheet of
File:	\\..Main.SchDoc	Drawn By:



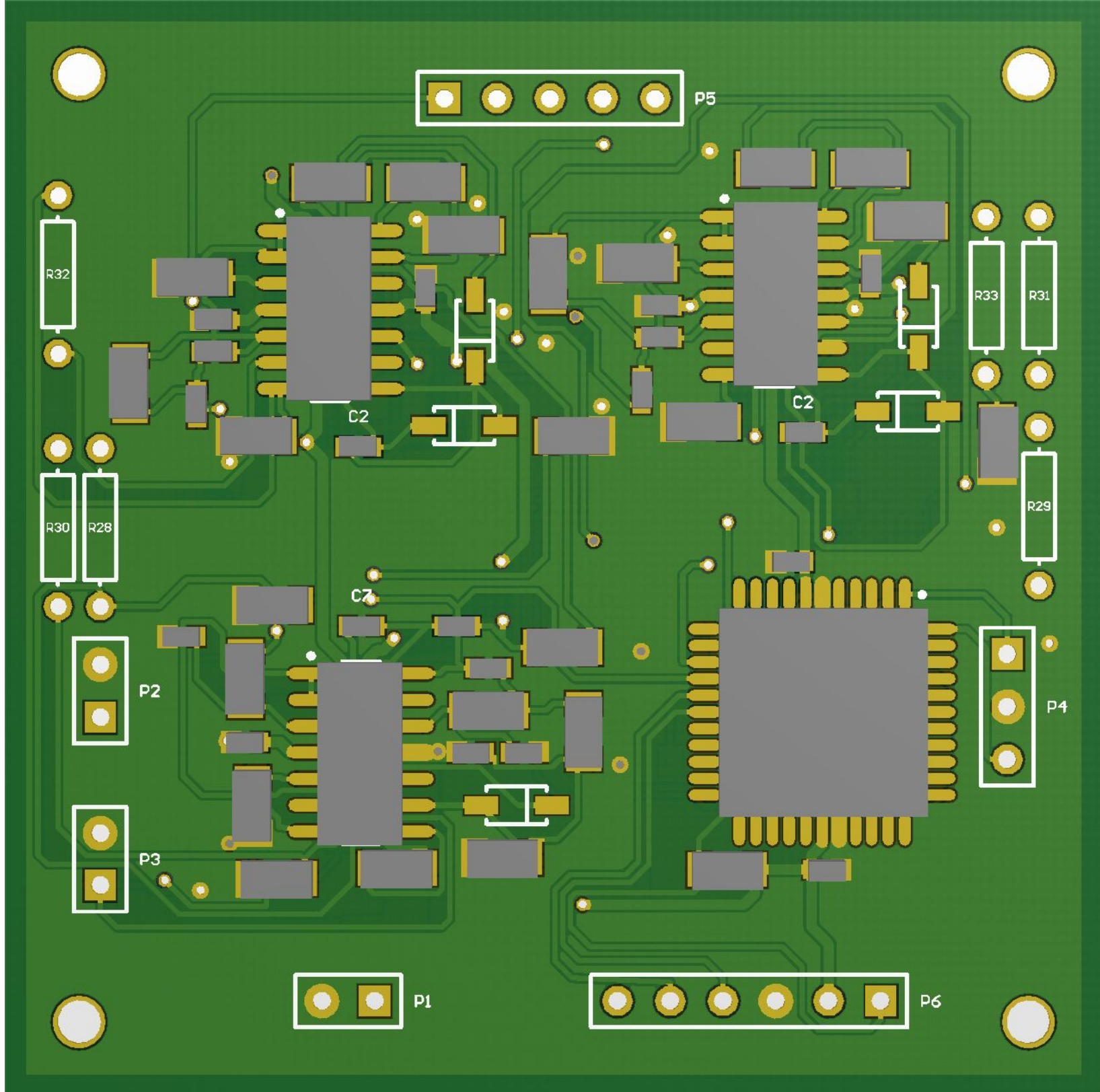
Title		
Size A	Number	Revision
Date:	11/3/16	Sheet of
File:	\\..\Vdd.SchDoc	Drawn By:

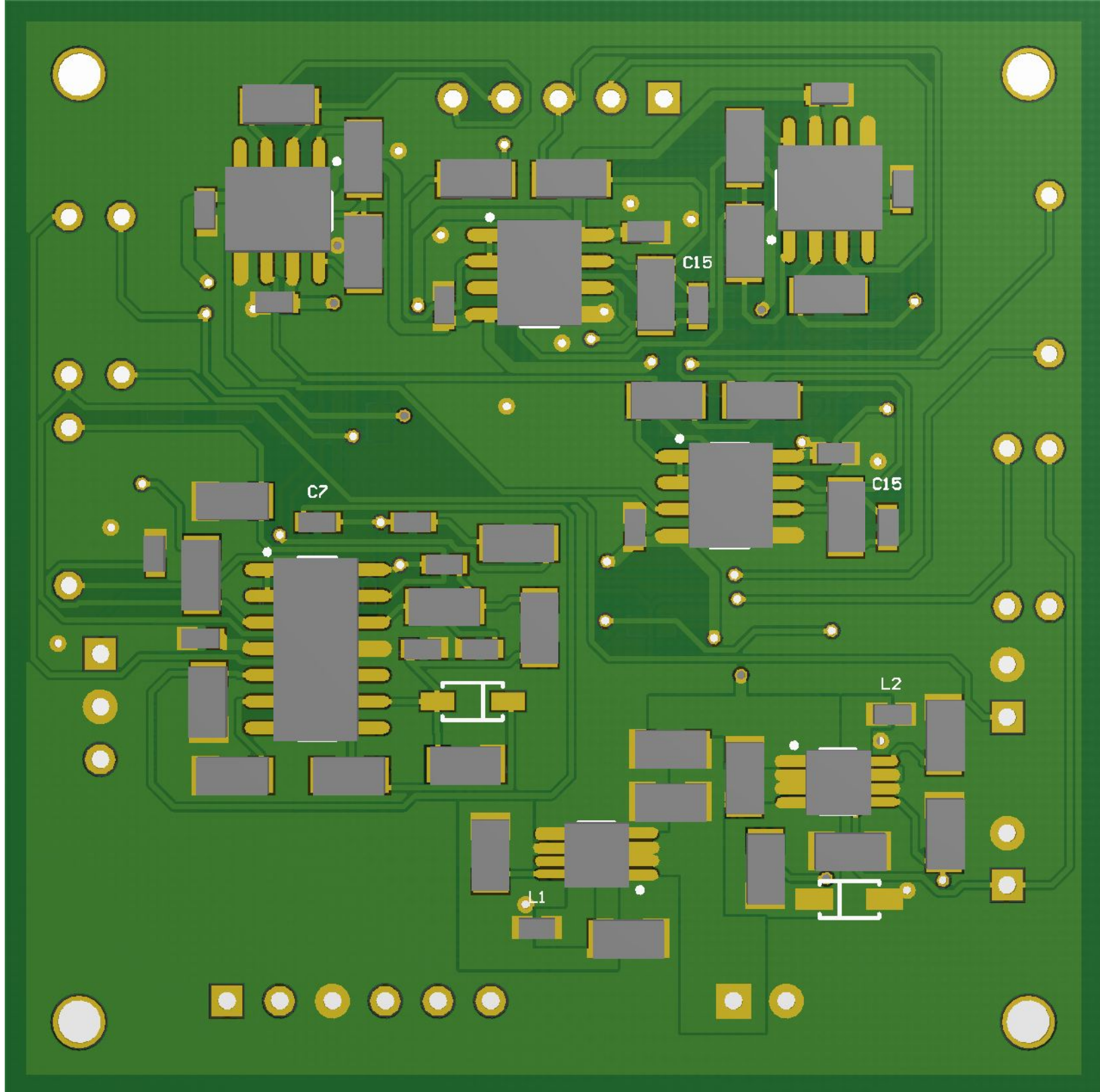


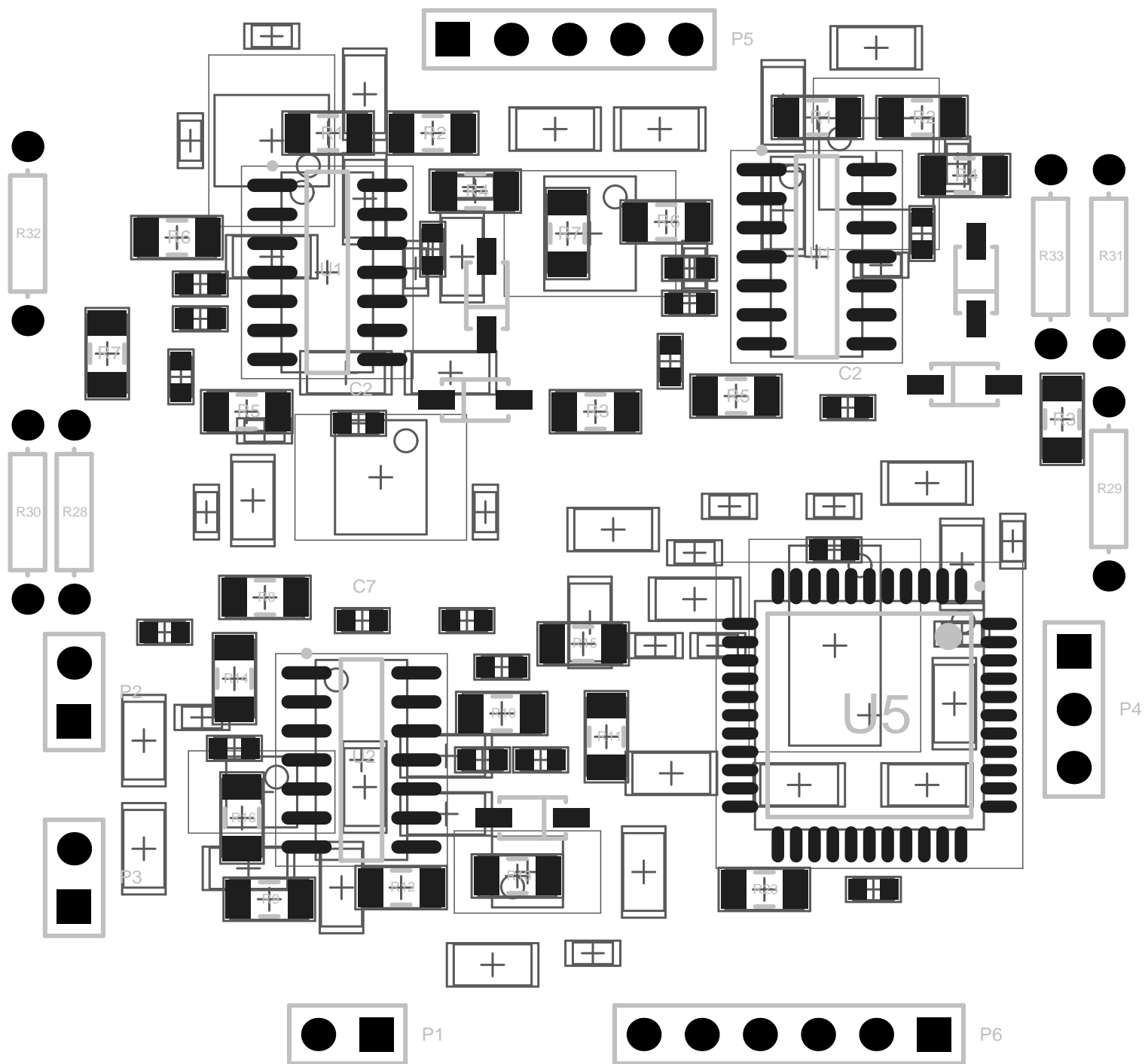
Title		
Size	Number	Revision
A		
Date:	11/3/16	Sheet of
File:	\\.\Vss.SchDoc	Drawn By:

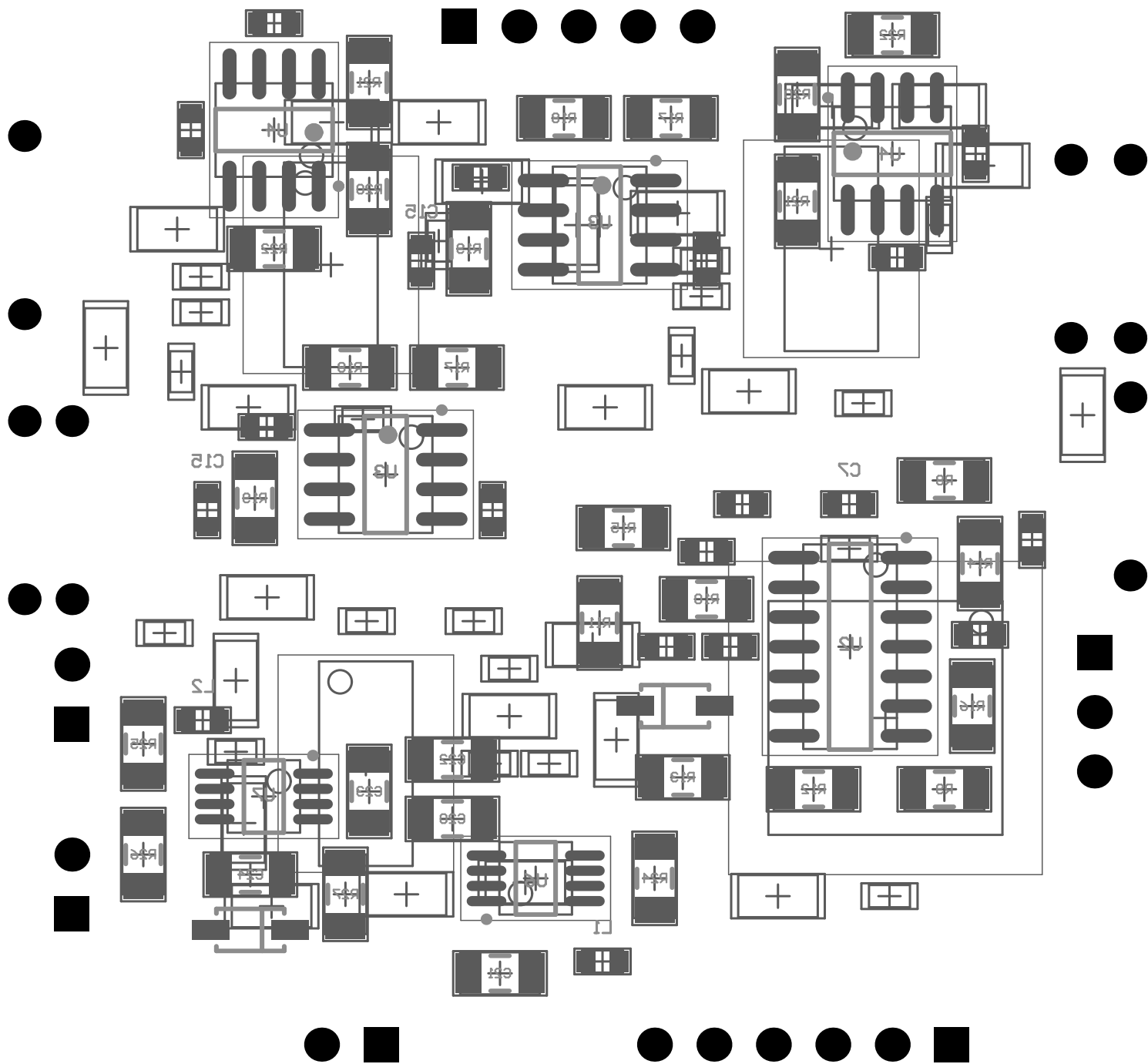












## Electrical Rules Check Repor

Class	Document	Message
		Successful Compile for Electronics.PrjPc

## Design Rules Verification Report

Filename : \\Mac\Home\Desktop\Duke\BME464\Electronics\Main.PcbDr

Warnings C  
Rule Violations C

Warnings	
Total	0

Rule Violations	
Room Drive2 (Bounding Region = (1307.559mil, 1960mil, 1887.559mil, 2360m	0
Room Drive1 (Bounding Region = (1667.559mil, 2380mil, 2247.559mil, 2780m	0
Room Neg (Bounding Region = (1200mil, 1280mil, 1880mil, 1773.15m	0
Room Pos (Bounding Region = (1680mil, 1200mil, 2180mil, 1560mi	0
Room Input2 (Bounding Region = (2300mil, 2420mil, 2700mil, 2900m	0
Room Input1 (Bounding Region = (1300mil, 2420mil, 1700mil, 2900m	0
Room Main (Bounding Region = (920mil, 2940mil, 3080mil, 3040mi	0
Room Input2 (Bounding Region = (1153.78mil, 1313.07mil, 2180.001mil, 2765r	0
Room Filtering1 (Bounding Region = (1945.591mil, 1495mil, 2971.811mil, 2820r	0
Room Digital (Bounding Region = (2208.267mil, 1310mil, 2788.267mil, 2005m	0
Width Constraint (Min=10mil) (Max=20mil) (Preferred=20mil) (InNet('GND	0
Width Constraint (Min=10mil) (Max=20mil) (Preferred=20mil) (InNet('VDD	0
Width Constraint (Min=10mil) (Max=20mil) (Preferred=20mil) (InNet('VSS	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Net Antennae (Tolerance=0mil) (All)	0
Silk to Silk (Clearance=2mil) (All),(All)	0
Silk To Solder Mask (Clearance=5mil) (Disabled)(IsPad),(A	0
Minimum Solder Mask Sliver (Gap=1mil) (All),(Al	0
Hole To Hole Clearance (Gap=10mil) (All),(Al	0
Hole Size Constraint (Min=1mil) (Max=150mil) (Al	0
Height Constraint (Min=0mil) (Max=1000mil) (Preferred=500mil) (A	0
Width Constraint (Min=10mil) (Max=20mil) (Preferred=10mil) (A	0
Power Plane Connect Rule(Direct Connect )(Expansion=20mil) (Conductor Width=10mil) (	0
Clearance Constraint (Gap=5mil) (All),(Al	0
Un-Routed Net Constraint ( (All)	0
Short-Circuit Constraint (Allowed=No) (All),(Al	0
SMD Neck-Down Constraint (Percent=100%) (Al	0
Total	0