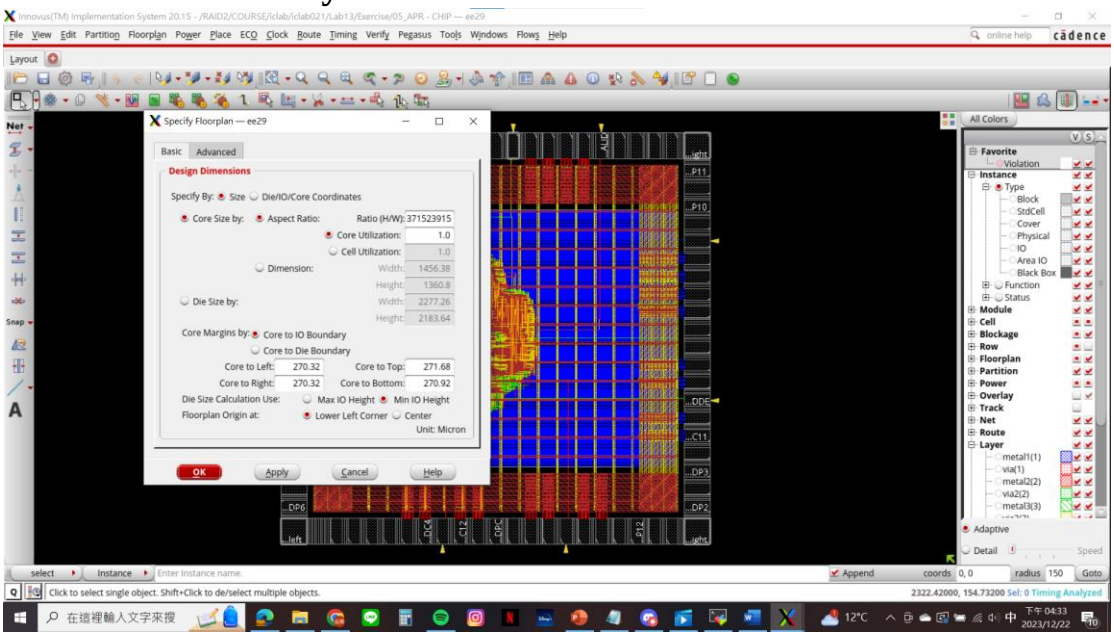
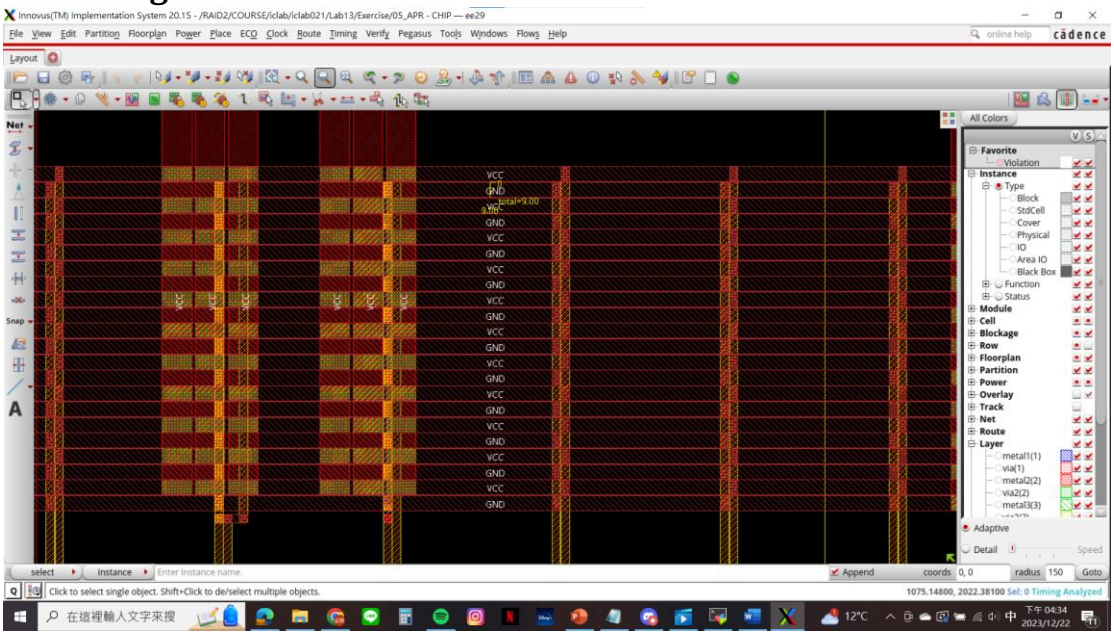
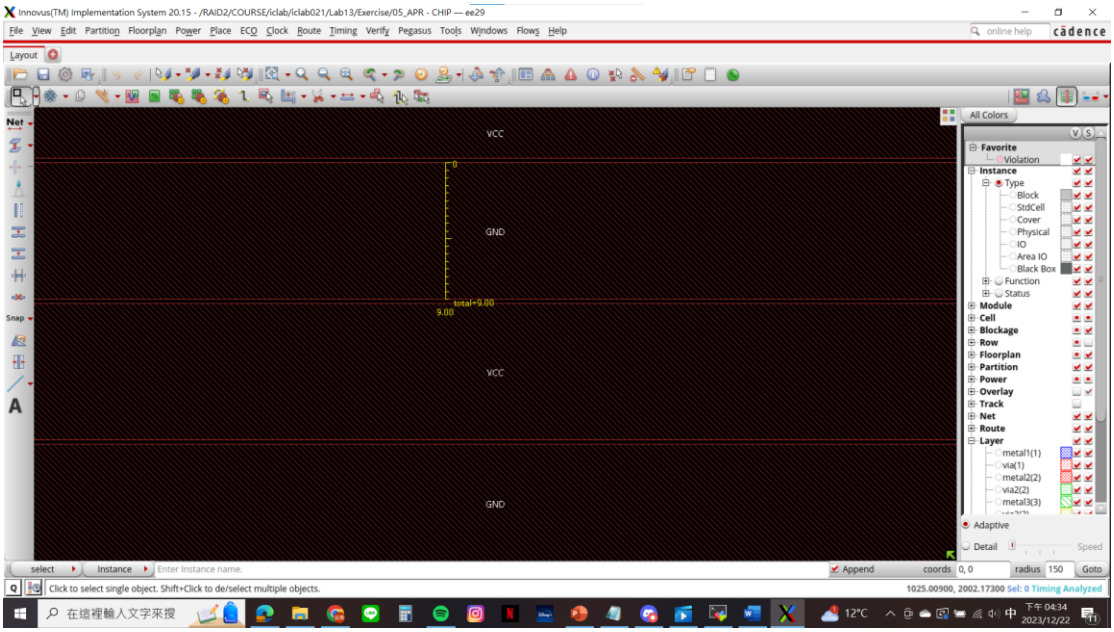
Report

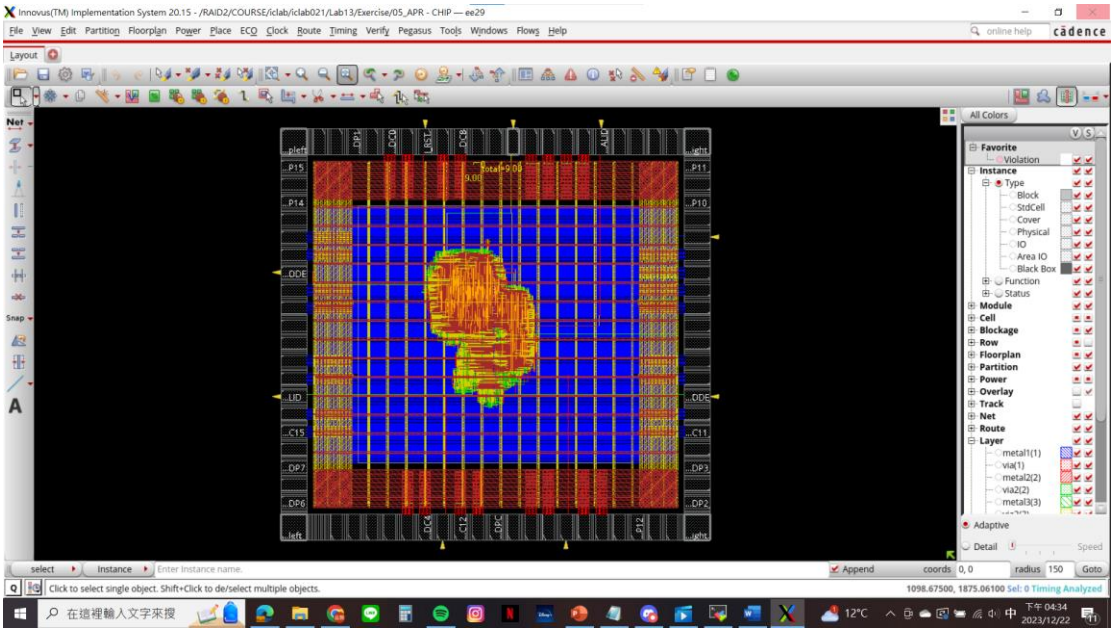
1. Core to IO boundary :



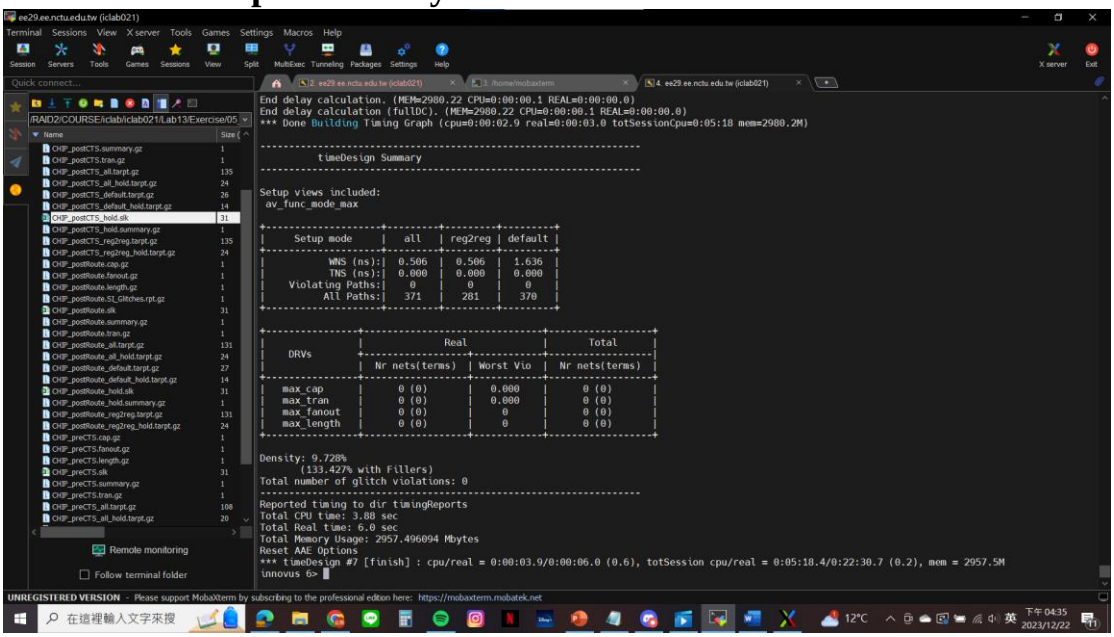
2. Core Ring :



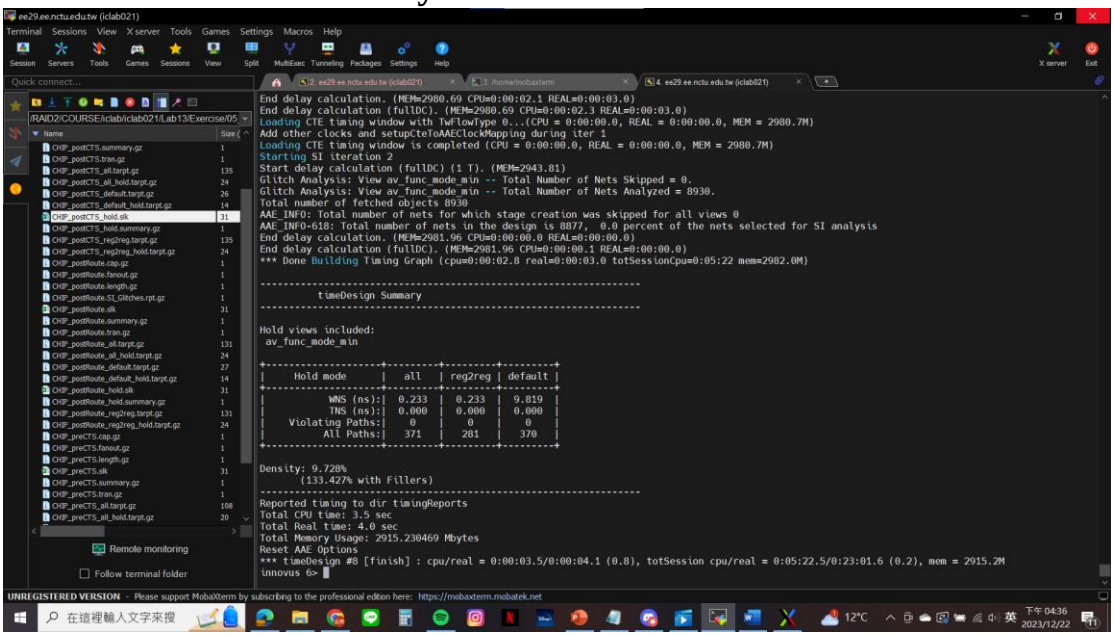

Innovus(TM) Implementation System 20.15 - /RAID2/COURSE/iclab/iclab021/Lab13/Exercise/05_APR - CHIP — ee29
File View Edit Partition Floorplan Power Place ECO Clock Route Timing Verify Pegasus Tools Windows Flows Help
cadence
Layout
VCC
GND
total=9.00
9.00
VCC
GND
All Colors
Favorite
Violation
Instance
Type
Block
StdCell
Cover
Physical
IO
Area IO
Black Box
Function
Status
Module
Cell
Blockage
Row
Floorplan
Partition
Power
Overlay
Track
Net
Route
Layer
metal1(1)
via(1)
metal2(2)
via2(2)
metal3(3)
Adaptive
Detail
Speed
select
Instance
Enter Instance name.
Append
coords 0, 0
radius 150
Goto
Click to select single object. Shift+Click to de/select multiple objects.
1025.00900, 2002.17300 Sel: 0 Timing Analyzed
在這裡輸入文字來搜
12°C
中
下午 04:34
2023/12/22


Innovus(TM) Implementation System 20.15 - /RAID2/COURSE/iclab/iclab021/Lab13/Exercise/05_APR - CHIP — ee29
File View Edit Partition Floorplan Power Place ECO Clock Route Timing Verify Pegasus Tools Windows Flows Help
cadence
Layout
P15
P14
P11
P10
DDE
LID
C15
C11
DP7
DP3
DP6
DP2
C12
P12
total=9.00
9.00
All Colors
Favorite
Violation
Instance
Type
Block
StdCell
Cover
Physical
IO
Area IO
Black Box
Function
Status
Module
Cell
Blockage
Row
Floorplan
Partition
Power
Overlay
Track
Net
Route
Layer
metal1(1)
via(1)
metal2(2)
via2(2)
metal3(3)
Adaptive
Detail
Speed
select
Instance
Enter Instance name.
Append
coords 0, 0
radius 150
Goto
Click to select single object. Shift+Click to de/select multiple objects.
1098.67500, 1875.06100 Sel: 0 Timing Analyzed
在這裡輸入文字來搜
12°C
中
下午 04:34
2023/12/22

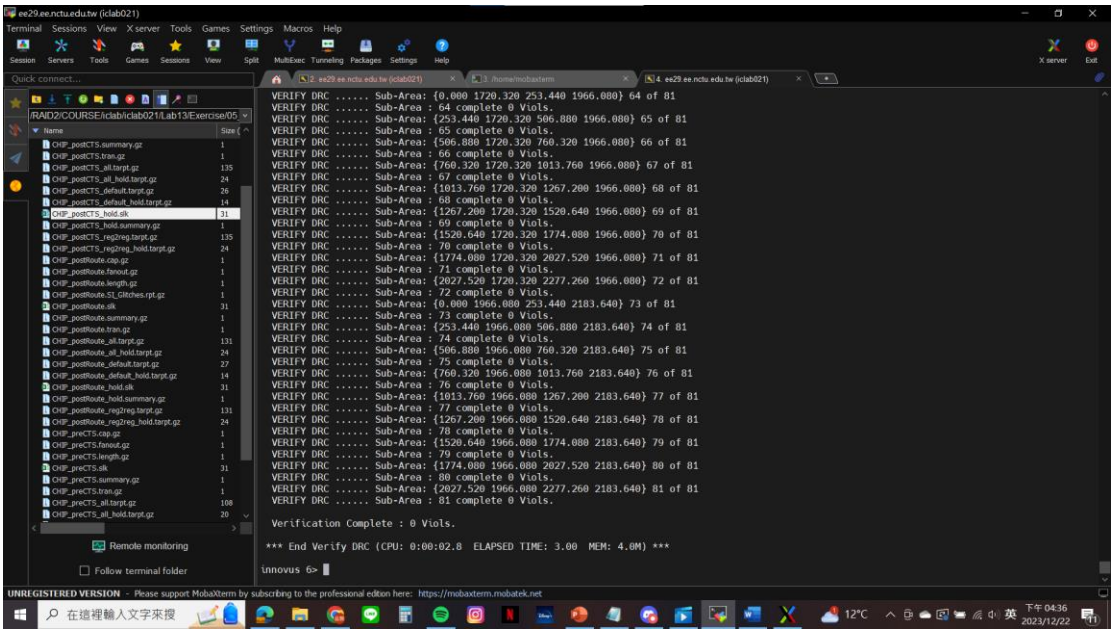
3. Post-Route setup time analysis :



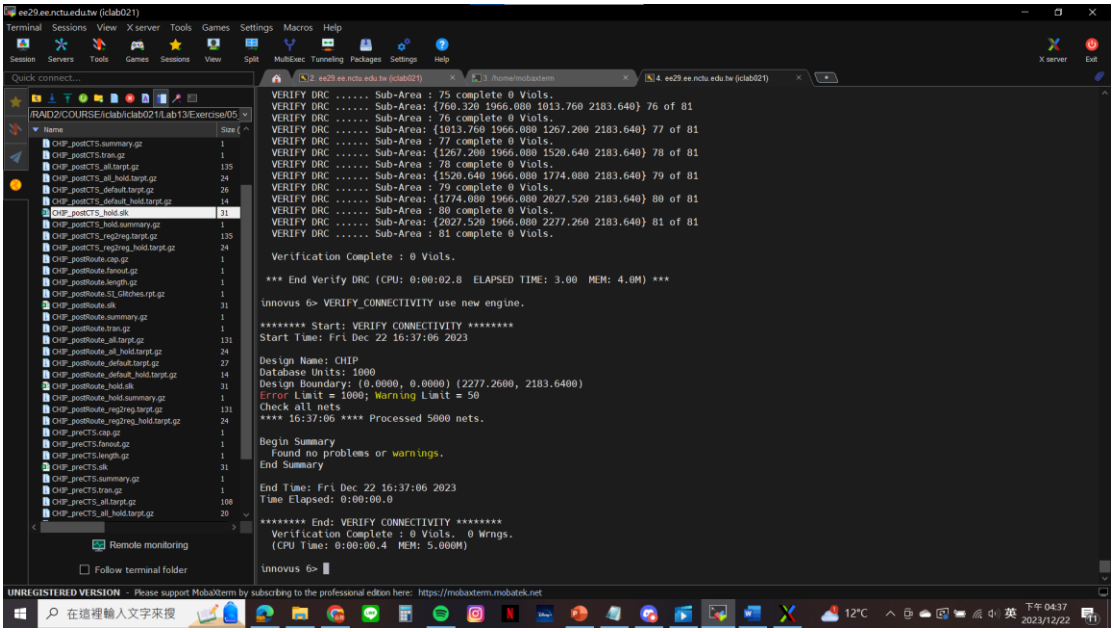
4. Post-Route hold time analysis :



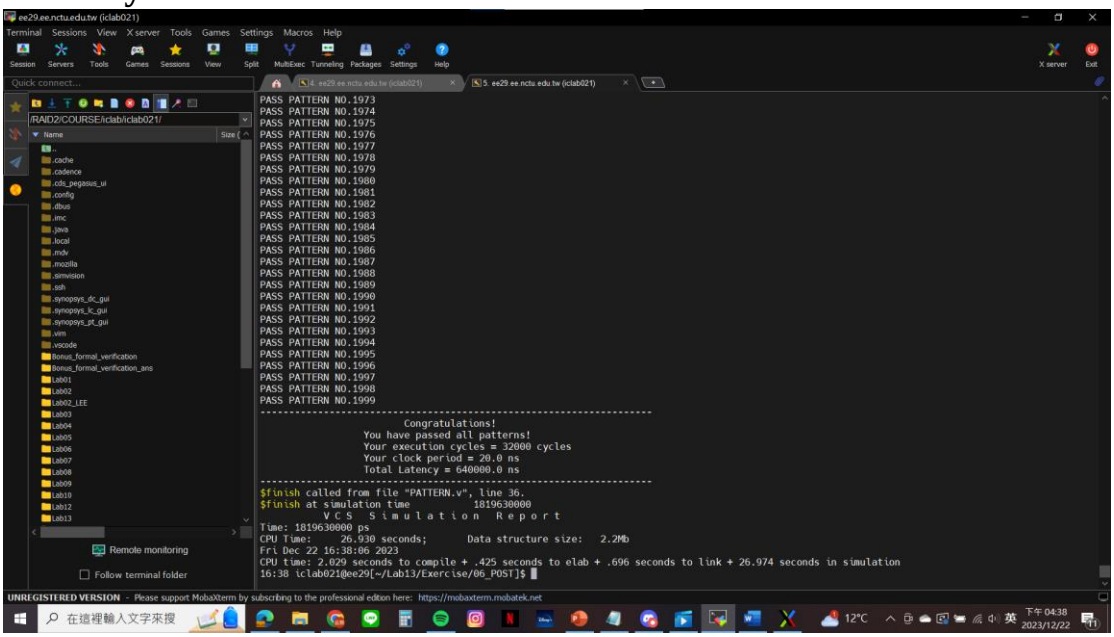
5. DRC result :



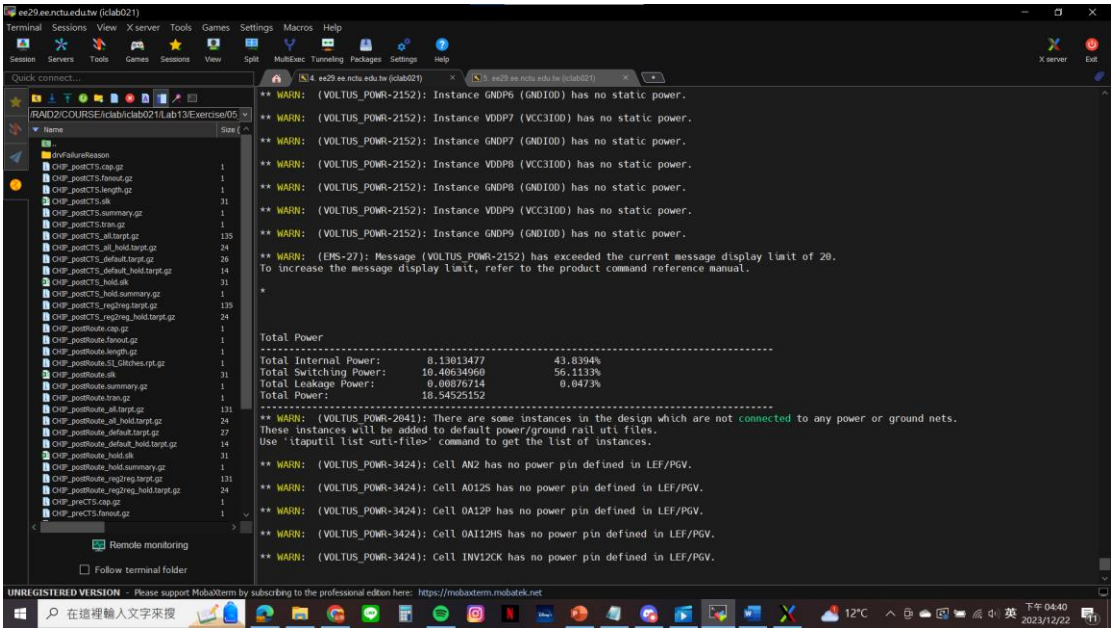
6. LVS result :



7. Post Layout simulation result :



8. Power result :



9. IR Drop Results :

使用兩組power ring，每組裡面包含11組VCC和GND，且使用四組stripes(metal 2, 3, 4, 5)，每條stripe的寬度為5。