**Name: Dilipan.V**
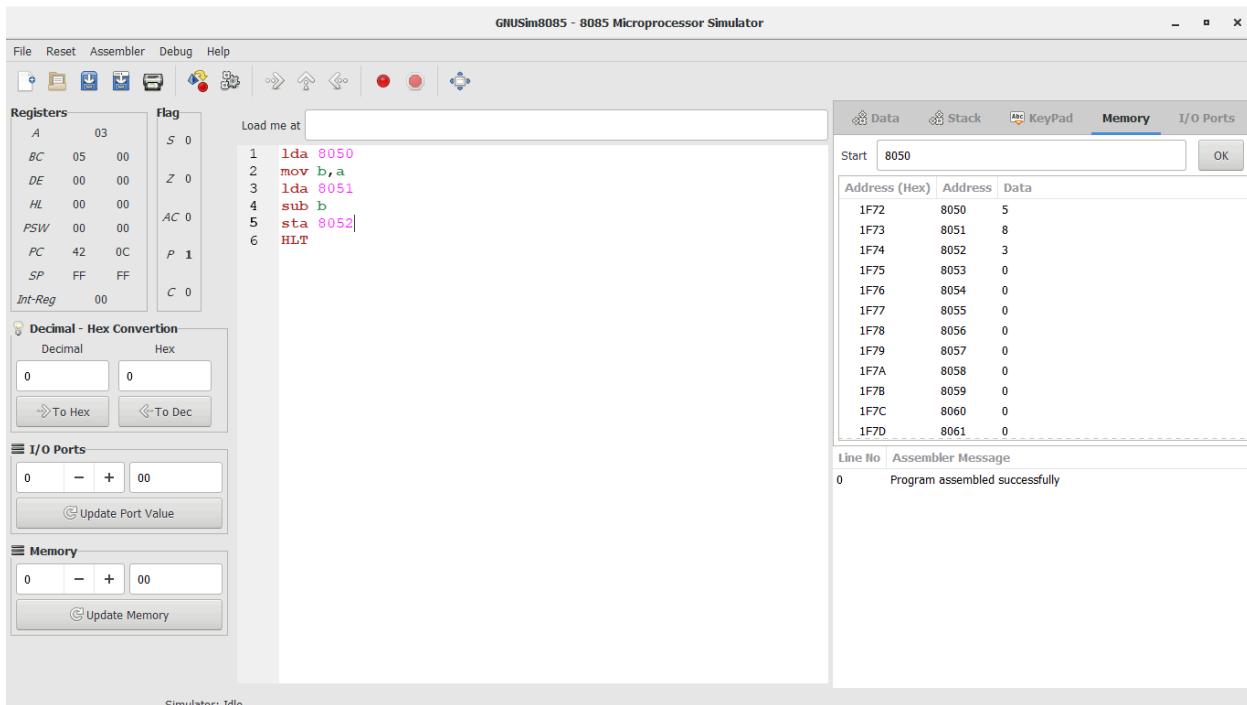
Reg no: 192421243
Course: Computer Architecture
Course code:CSA1257
**Microprocessor 8085**
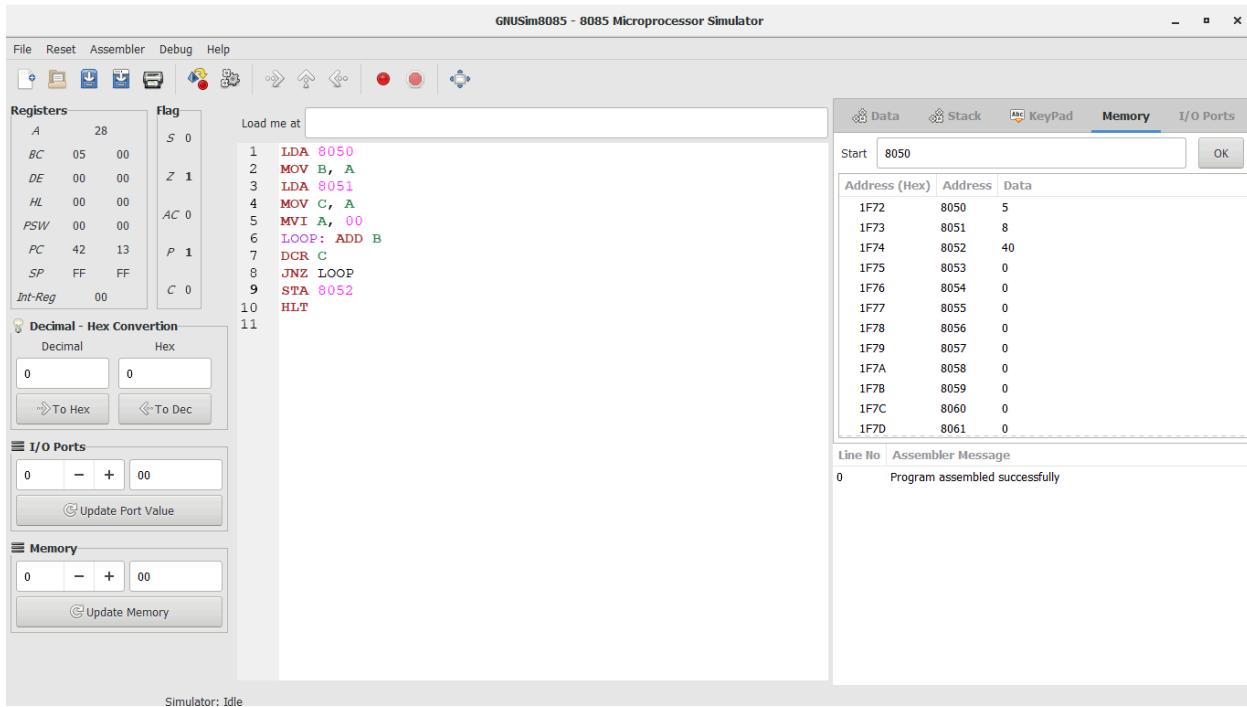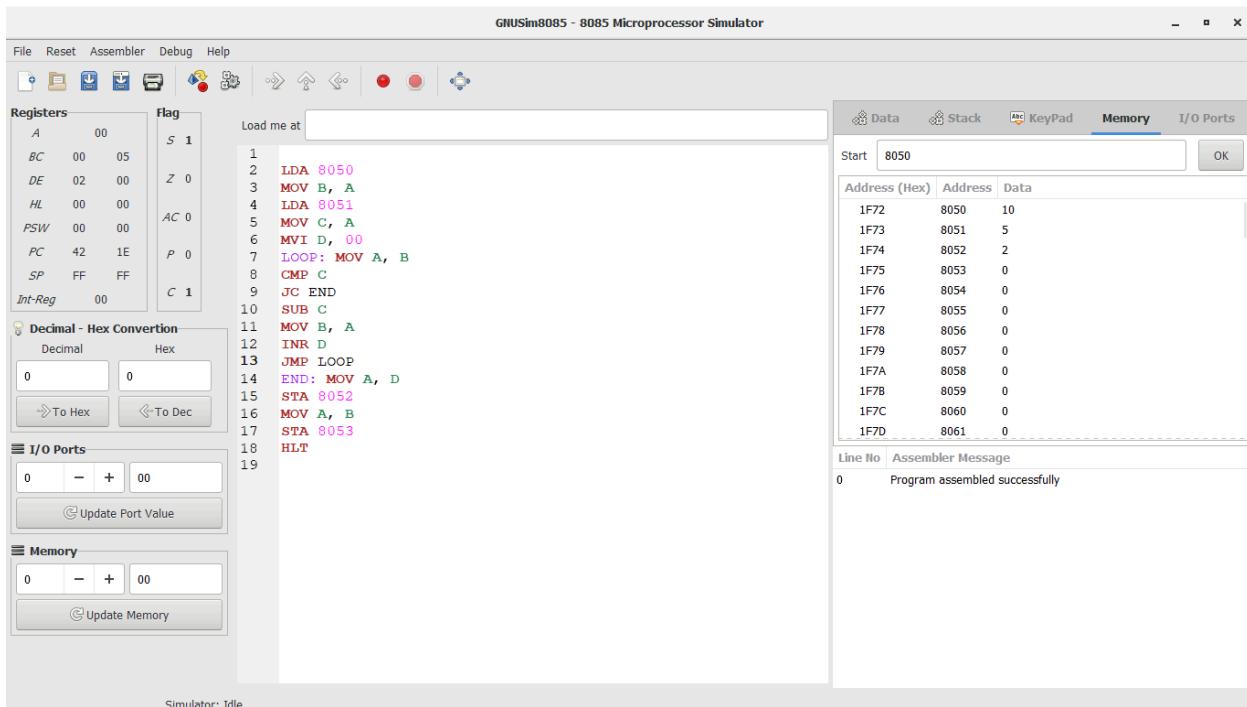
1: Add
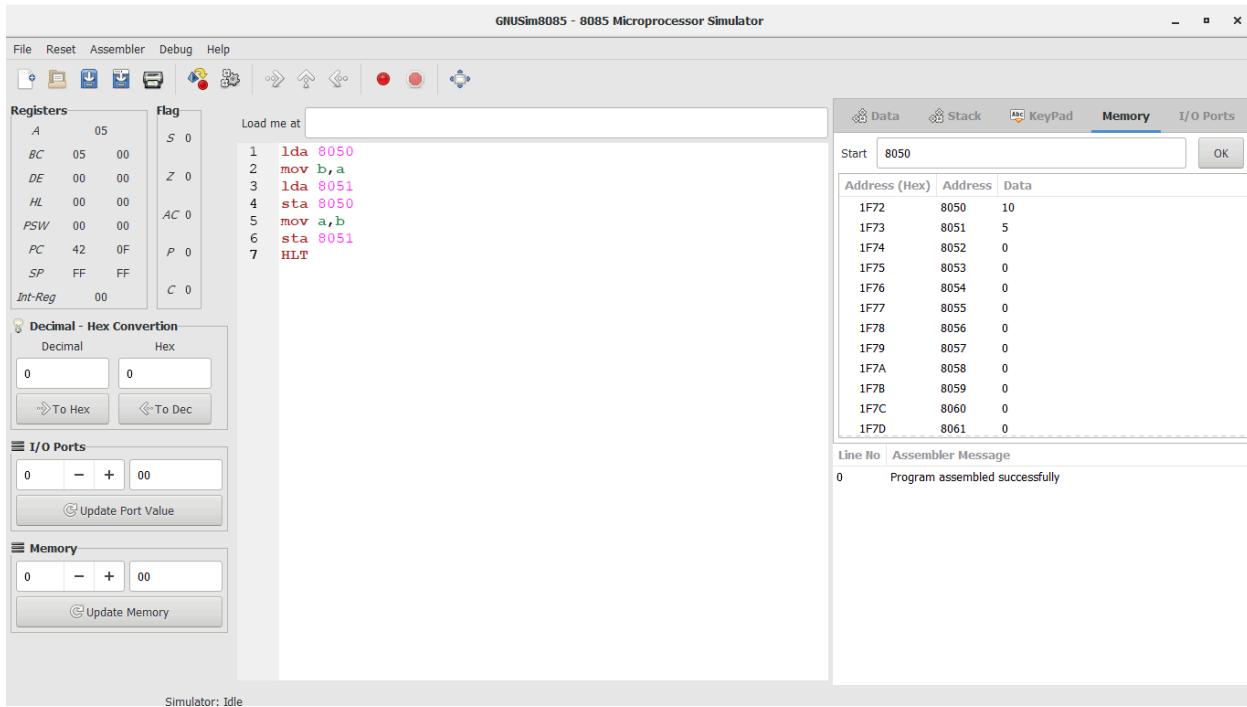
2: Subtract

3:Multiplication

```
1   LDA 8050
2   MOV B, A
3   LDA 8051
4   MOV C, A
5   MVI A, 00
6   LOOP: ADD B
7   DCR C
8   JNZ LOOP
9   STA 8052
10  HLT
11
```


4:Divide

```
1
2   LDA 8050
3   MOV B, A
4   LDA 8051
5   MOV C, A
6   MVI D, 00
7   LOOP: MOV A, B
8   CMP C
9   JC END
10  SUB C
11  MOV B, A
12  INR D
13  JMP LOOP
14  END: MOV A, D
15  STA 8052
16  MOV A, B
17  STA 8053
18  HLT
19
```
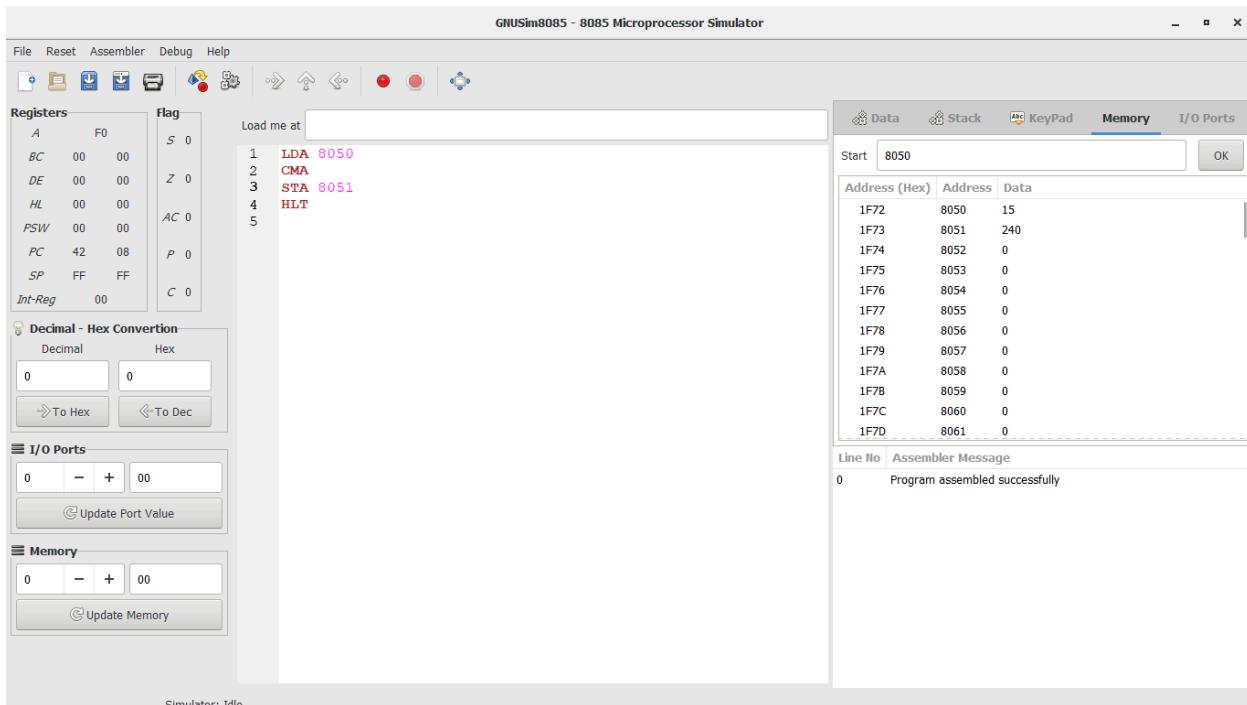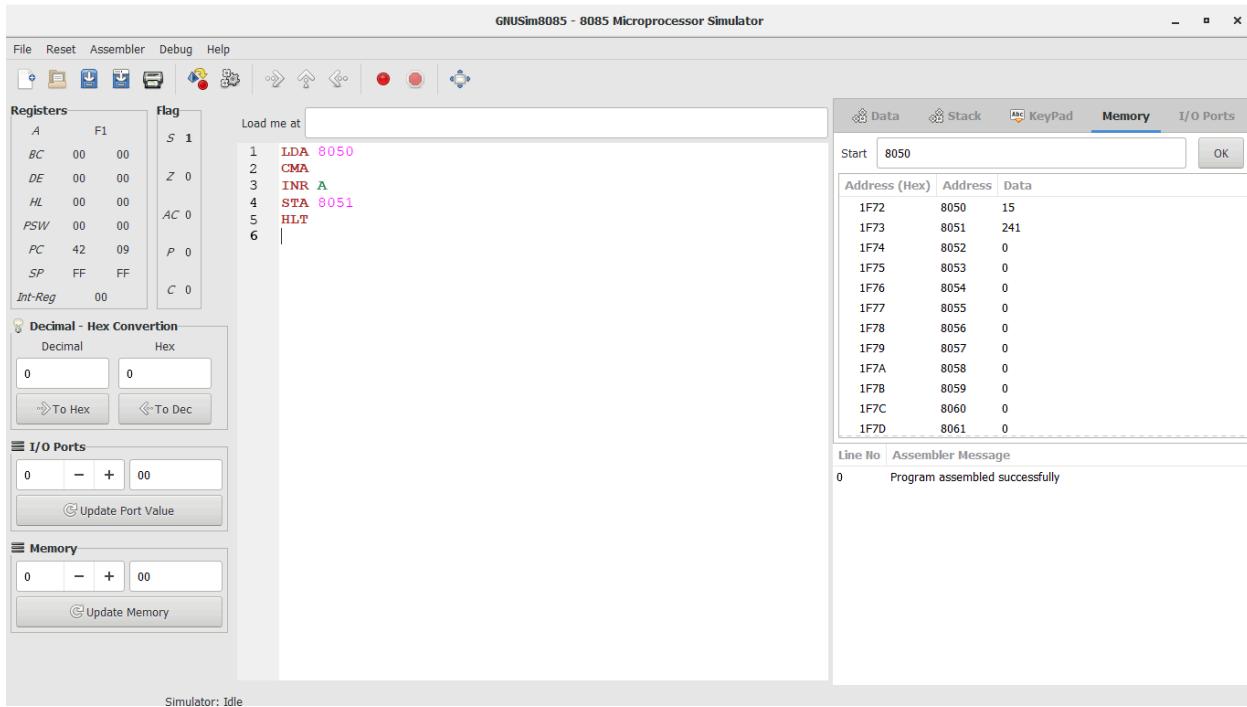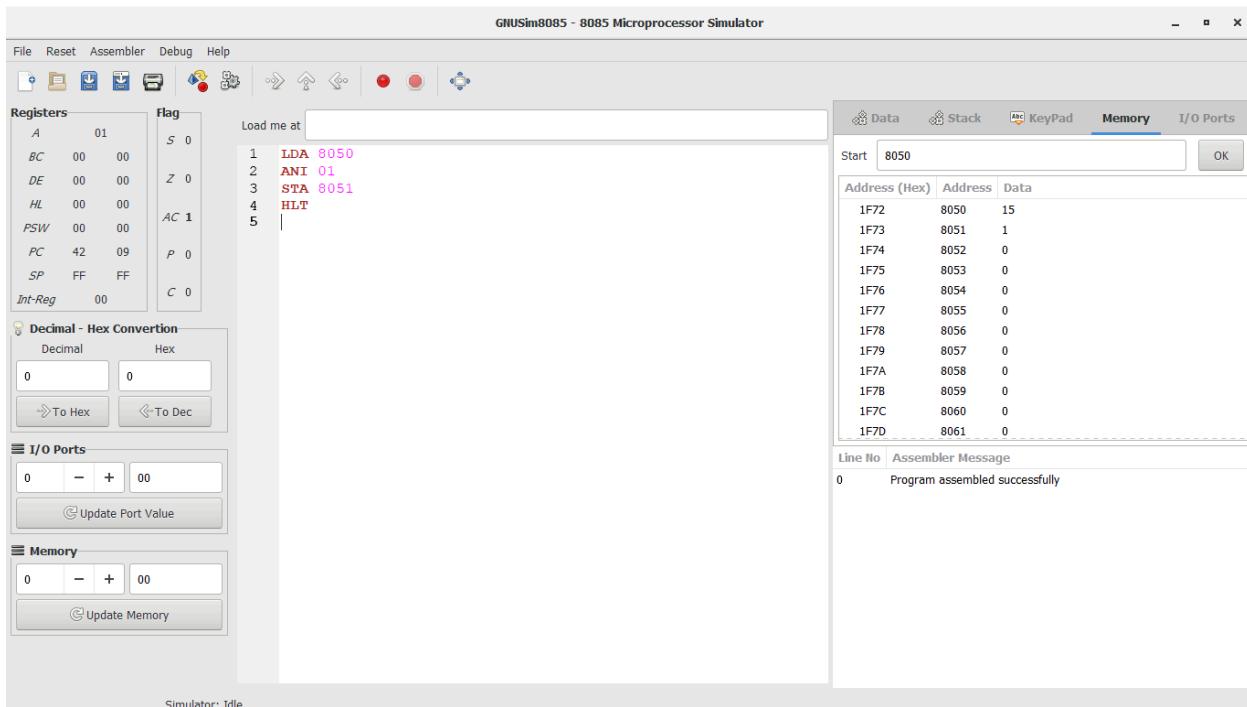
5:Swap the number
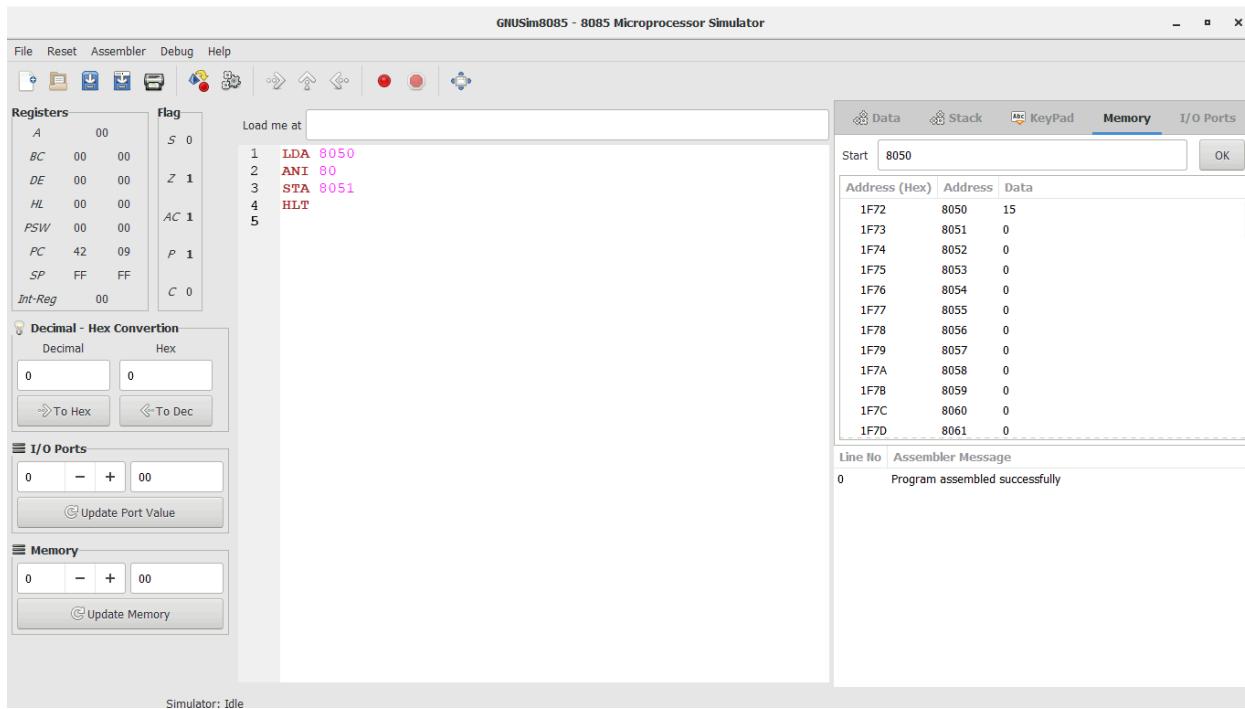
# 6: 1' Compliment

7: 2' Compliment

8: Odd or Even

9: Positive or Negative number