Computer Organization and Assembly Language Assignment 03

Task 1: Assembly Language Programming
Write down the states of RAM and registers, in line by line debugging mode, in following given

scenarios.

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Question 1:-

[000 x 100]

mov ax, 3 add ax, 1

mov ax, 2

mov bx 3

sub bx, ax

add ax, bx

mov ax, 0x4000

15 xo fri

$$ax = 3$$

$$ax = 4$$

ax=3, bx=3

bx = 1, ax = 3

ax=3, bx=1

ax = 4C, bx = 1

ax = 0, bx = 1

Mon Tue Wed Thu Fri Sat Sun



Question 02:-

[org 0x100]

mov ax 2 mor bx 1

sub ax, bx

add ax, bx

add ax, bx

mov ax 4

mov cx 4

mor ax ox4000

int 0x21

ax = 2

bx=1 , ax=2

ax=1, bx=1

ax=2, bx=1

ax=3, bx=1.

ax = 4, bx = 1

ax = 4, bx = 1, cx = 4

ax=40, bx=1, cx=4

ax=0, bx=0, cx=

Question 03:-

Lorg 0x100]

mov. ax, 2

mov cx54

mov dx,1

add cxidx

add ax, dx.

sub ex dx

add dx, ax

mov bx ,8

mov ax, 0 x 4000

int 0x21

anc = 2

ax=2 > cx=4

 $\alpha x = 2$, $\alpha x = 4$, $\alpha x = 1$

 $\alpha x = 2$, $\alpha x = 1$

ax=3 ax=5 ax=1

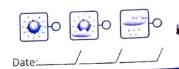
ax=3, cx=4, dx=1

ax=3 2cx=4 2dx=4

ax=3, bx=8, cx=4, dx=4

ax=40, bx=8, cx=4, dx=4

ax=0, bx=0, cx=0, dx=0

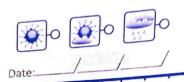


3 00000000 BB0A00 mov bx, 10 bx = A4 00000003 B80A00 mov ax, 2 ax = 2, bx = A5 00000006 F7E3 mul bx ax = 14, bx = A

6 00000008 B80400 mov ax > 4 ax = 4, bx = A7 0000000B 01C3 and bx > ax ax = 4, bx = E8 0000000D 29C3 sub bx > ax ax = 4, bx = A

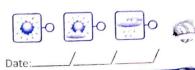
9
10 8000000F B8004C mov ax, 0x4000 ax=40, bx=A11 00000012 CD21 inf 0x21 ax=0, bx=0





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0 1 01	304	A. A.	1. 185715-1-1	7		or your	
Question O			=01	_			
Explain a	and diffe	rentiale	the f	700	instr	uction	grou
of the ass	embly	language.	in "	which	the	instr	uction
have been	categor	rized i.e.					
1. Data n	navement	- instruct	ions	obje	•		
2. Arithmal	tio Lagio	inclass	Linn				
3. Program	Control	anupik	struction	ns	(net	lorder	1710179
L. Sessial	200110	not sting	Sa we are	7			10
4 SPECIAL	group	11 STOLL CIONS	3				
- 1	<u> </u>	Calo	oondd.	1			
Instruction	ישיים ובו	64° V2	sembly	rangu	ale.	• -	
1. Data Mo	vement	rustructio	nsi ea	* 31			* * * *
Data r	novement	instruction	ns m	ione c	lata	trom	one
place (som	re) to	another	plac	e (d	estin	ation).	For
example				54	1/4		
)	mov	ax	4 -	→ sc	urce	operat	d
		,				z erryje:	
, J. C.		destinat	*	y P	- , ,		
		operar		N 124 2	April 1	1, 11	
	0 (11)	Speria	N /		7 1 1	1	
					1		

WaRQnotes



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2. Arithmetic Logic Operation:	5
The arithmetic logic operation define the set of	
The arithmetic logic operation define the set of operations performed by the processor's Arithmetic	W.
Logic Unit. For example.	
add ax, bx	-6
	7
opcode Demands	P
	-3
3. Program Control Group Instruction:	V
3. Program Control Group Instruction: Program control group instructions are the machine codes that are used to command the	
codes that are used to command the	<u> </u>
processor to act accordingly. These instructions	P
basically control the execution of program- For	8
example,	
comp ax, bx	-
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i plant of the first of the state of the sta	
1. Special Group Instruction:	9
special group instruction are instructions that	-
change the behaviour of CPU-For example,	

